



FEATURES

RoHS compliant
High efficiency to 89%
Power density up to 2.4W/cm ³
UL 94V-0 package material
Industry standard pinout
UL 1950 recognised
Non latching current limit
Constant 350kHz frequency
1.5kV input to output isolation
Versatile control options
Continuous rating to 30W at 40°C without heatsink
Operation to zero load
Protected against load faults
Internal over temperature protection
Uses no electrolytic capacitors

DESCRIPTION

The NPH15S series of DC-DC converters combines ease of application with versatility. The pin pattern is based on the popular industry standard, but two additional pins may optionally be fitted to provide a variety of features not commonly found on units of this type. High efficiency enables full rating to be achieved in a small package without heatsinking, and a high surge capability will provide for start-up and transient loads, whilst being thermally protected against sustained overload. Overload protection of the "constant current" type ensures start-up into complex load conditions. The copper case achieves efficient heat transfer and screening. The product range has been recognised by Underwriters Laboratory (UL) to UL 1950 for operational insulation, file number E151252 applies.



NPH15S Series

Isolated 15W Single Output DC-DC Converters

SELECTION GUID)E							
Order Code ¹	Nominal Input Voltage	Output Voltage	Output Current	Curre	nt Limit	Efficiency	MTTF ²	Recommended Alternative
	V	V	А	A (Min.)	A (Max.)	%	kHrs	
				NRND				
NPH15S2403EiC	24	3.4	4.4	7.5	11.0	81	335	SPM15-033-Q12
NPH15S2403iC	24	3.4	4.4	7.5	11.0	81	335	SPM15-033-Q12
NPH15S2405EiC	24	5.1	3.0	5.0	7.5	84	286	SPM15-050-Q12
NPH15S2405iC	24	5.1	3.0	5.0	7.5	84	286	SPM15-050-Q13
NPH15S2412iC	24	12.1	1.3	2.5	3.7	86	286	SPM15-120-Q12
NPH15S2415EiC	24	15.1	1.0	2.0	3.0	87	281	SPM15-150-Q12
NPH15S2415iC	24	15.1	1.0	2.0	3.0	87	281	SPM15-150-Q12
				To be				
				iscontinue				
NPH15S2412EiC	24	12.1	1.3	2.5	3.7	86	286	SPM15-120-Q12
NPH15S4803EiC	48	3.4	4.4	7.5	11.0	83	295	SPM15-033-Q48
NPH15S4803iC	48	3.4	4.4	7.5	11.0	83	295	SPM15-033-Q48
NPH15S4805EiC	48	5.1	3.0	5.0	7.5	85	301	SPM15-050-Q48
NPH15S4805iC	48	5.1	3.0	5.0	7.5	85	301	SPM15-050-Q48
NPH15S4812EiC	48	12.1	1.3	2.5	3.7	88	302	SPM15-120-Q48
NPH15S4812iC	48	12.1	1.3	2.5	3.7	88	302	SPM15-120-Q48
NPH15S4815EiC	48	15.1	1.0	2.0	3.0	89	296	SPM15-150-Q48
NPH15S4815iC	48	15.1	1.0	2.0	3.0	89	296	SPM15-150-Q48

INPUT CHARACTERIST	CS					
Parameter	Conditions	Min.	Тур.	Max.	Units	
Voltago rango	Continuous operation, 24V input types	18	24	36	V	
Voltage range	Continuous operation, 48V input types	36	48	75	v	

OUTPUT CHARACTERIS	TICS				
Parameter	Conditions	Min.	Тур.	Max.	Units
Voltage set point error	50% load			0.5	%
Overall voltage error	Case temperature -40°C to 110°C Load 0% - 100% Input specified range			2.5	%
Temperature coefficient of output voltage (slope)				250	ppm⁰C
Deviation of output voltage	Temperature MIN-MAX		0.5	1	%
Line regulation	Operating voltage range, 50% load			0.1	%
Load Regulation	0% - 100% rated load			0.5	%
Ripple	rms		70		mV

ISOLATION CHARACT	ERISTICS				
Parameter	Conditions	Min.	Тур.	Max.	Units
Isolation test voltage	Flash tested for 1 second	1500			VDC
Resistance	VISO = 500VDC	1			GΩ

1. Parts ending with EiC have optional TRIM and SS pins fitted.

2. Calculated using MIL-HDBK-217F with nominal input voltage at full load.

- 3. Absolute maximum value for 30 seconds. Prolonged operation may damage the product.
- All specifications typical at TA=25°C, nominal input voltage and rated output current unless otherwise specified.

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CONTROL CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Voltage trimming range ¹	At rated load, trim control at either output	±10			%
Remote switch input (voltage relative to input negative) ¹	Not operating	-15	0	1.5	V
	Operating, open circuit voltage	9	10	11	V
Start delay	Time from application of valid input voltage to output being in specifi- cation		25	50	ms
Synchronisation ¹	Specified drive signal	320		440	kHz
Switching frequency		330	350	395	kHz

TEMPERATURE CHARACTERISTICS					
Parameter	Conditions	Min.	Тур.	Max.	Units
Case temperature	Full load	-40		110	۰ ۲
Storage	Absolute Max. internal temperature	-40		125	0
Relative humidity	Non condensing 85°C			85	%
Thermal protection	Operates at case temperature	110			°C

ABSO	LUTE MAXIMUM RATINGS	

Wave Solder	Wave Solder profile not to exceed the profile recommended in IEC 61760-1 Section 6.1.3. Please refer to <u>application notes</u> for further information.
Input voltage, 24V input types	-0.5V to 40V ³
Input voltage, 48V input types	-0.5V to 80V ³
Output voltage	-0.3V to regulated voltage
Output trim control	-1V to +30V
Synchronisation/shutdown control	±15V relative to input return

THERMAL CHARACTERISTICS

UL 1950 recognition: Max. permissable	loads for a given	ambient temperature for	any NPH15S model.			
Temperature (°C)		Power (W)	Temperature (°	C)	Power (W)	
40		15.0	70		11.7	
50		15.0	80		ç	9.8
53		15.0	85		ł	3.8
60		13.7	90		-	7.8
Max. power rating with case temperature	e maintained by e	external means (e.g. force	d air cooling). Case Temperature			
Part Number		100°C	105°C	110ºC		Units
NPH15S2403XXX		19	16	12		
NPH15S2405XXX		22	19	15		W
NPH15S2412XXX		25	22	19		
NPH15S2415XXX		26	24	21		
NPH15S4803XXX		20	17	13		
NPH15S4805XXX		23	20	16		14/
NPH15S4812XXX		28	26	23		W
NPH15S4815XXX		30	28	25		

1. Optional - where fitted.

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TECHNICAL NOTES

ISOLATION VOLTAGE

'Hi Pot Test', 'Flash Tested', 'Withstand Voltage', 'Proof Voltage', 'Dielectric Withstand Voltage' & 'Isolation Test Voltage' are all terms that relate to the same thing, a test voltage, applied for a specified time, across a component designed to provide electrical isolation, to verify the integrity of that isolation.

Murata Power Solutions NPH15S series of DC-DC converters are all 100% production tested at their stated isolation voltage. This is 1500 VDC for 1 second.

A question commonly asked is, "What is the continuous voltage that can be applied across the part in normal operation?"

The NPH15S series has been recognised by Underwriters Laboratory, both input and output should normally be maintained within SELV limits i.e. less than 42.4V peak, or 60VDC. The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with several hundred volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

REPEATED HIGH-VOLTAGE ISOLATION TESTING

It is well known that repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment. While manufactured parts can withstand several times the stated test voltage, the isolation capability does depend on the wire insulation. Any material, including this enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltages thus implying that the number of tests should be strictly limited. We therefore strongly advise against repeated high voltage isolation testing, but if it is absolutely required, that the voltage be reduced by 20% from specified test voltage.

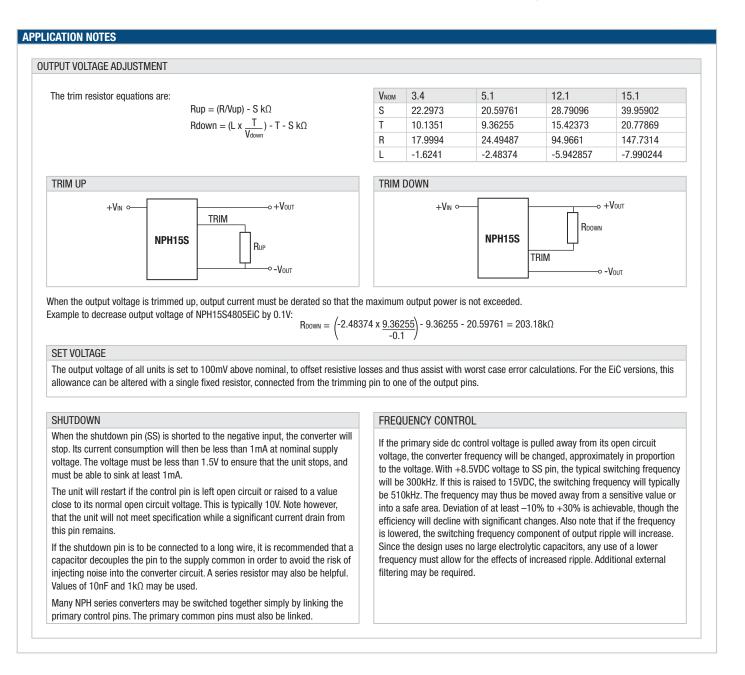
RoHS COMPLIANCE INFORMATION



This series is compatible with RoHS soldering systems with a peak wave solder temperature of 260°C for 10 seconds. Please refer to application notes for further information. The pin termination finish on this product series is a Gold flash (0.05-0.10 micron) over Nickel Preplate. The series is backward compatible with Sn/Pb soldering systems. For further information, please visit www.murata-ps.com/rohs

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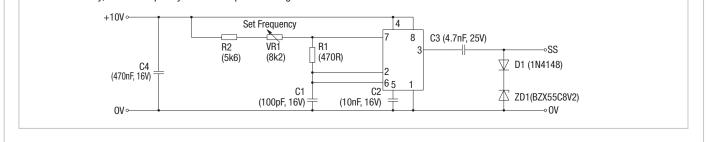
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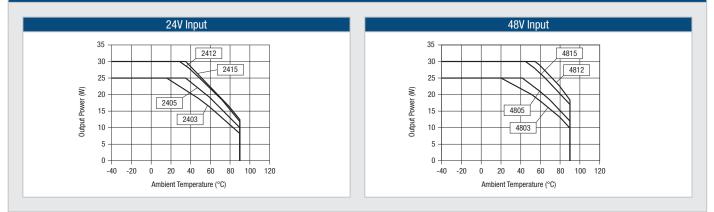
APPLICATION NOTES (Continued)

SYNCHRONIZATION

The converter frequency may be synchronised to an external frequency by connecting a negative going pulse to the SS pin. The drive signal is typically 8V to 12V amplitude and 100ns to 200ns duration. A suitable circuit consists of a CMOS timer (TLC555) connected as an oscillator or as a pulse shaper. Its logic output (not the discharge output) should be connected via a 4.7nF capacitor to the converter pin. The synchronised frequency is above the free running value. However, the free running frequency can be lowered, so that synchronisation may include frequencies near or below the natural value. An example of a practical circuit is shown below, which uses a zener diode to lower the natural frequency. Several converters of this family may be synchronised from the same reference provided the waveform can be maintained by the use of an adequate driver circuit. If the rise time is more than 20ns, for example, synchronisation may not be achieved over the specified frequency range. For best efficiency, set the frequency within the specified range of its natural state.



THERMAL PERFORMANCE



NPH15S Series

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EMC FILTERING AND SPECTRA

FILTERING

The module includes a basic level of filtering, sufficient for many applications. Where lower noise levels are desired, filters can easily be added to achieve any required noise performance.

A DC-DC converter generates noise in two principle forms: that which is radiated from its body and that conducted on its external connections. There are three separate modes of conducted noise: input differential, output differential and input-output.

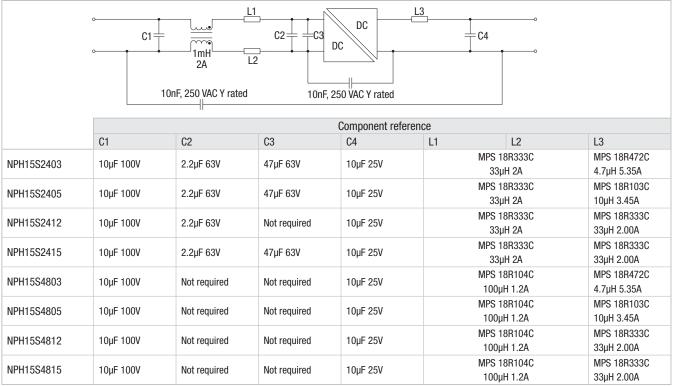
This last appears as common mode at the input and the output, and cannot therefore be removed by filtering at the input or output alone. The first level of filtering is to connect a capacitor between input and output returns, to reduce this form of noise. It typically contains high harmonics of the switching frequency, which tend to appear as spikes on surrounding circuits. The voltage rating of this capacitor must match the required isolation voltage. (Due to the great variety in isolation voltage and required noise performance, this capacitor has not been included within the converter).

Input ripple is a voltage developed across the internal Input decoupling capacitor. It is therefore measured with a defined supply source impedance. Although simple series inductance will provide filtering, on its own it can degrade the stability. A shunt capacitor is therefore recommended across the converter input terminals, so that it is fed from a low impedance.

If no filtering is required, the inductance of long supply wiring could also cause a problem, requiring an input decoupling capacitor for stability. An electrolytic will perform well in these situations. The input-output filtering is performed by the common-mode choke on the primary. This could be placed on the output, but would then degrade the regulation and produce less benefit for a given size, cost, and power loss.

Radiated noise is present in magnetic and electrostatic forms. The latter is suppressed by the metal case, which is connected to the output return, typically a zero-volt point. Thanks to the small size of these units, neither form of noise will be radiated "efficiently", so will not normally cause a problem. Any question of this kind usually better repays attention to conducted signals.

EMC FILTER AND VALUES TO OBTAIN SPECTRA AS SHOWN



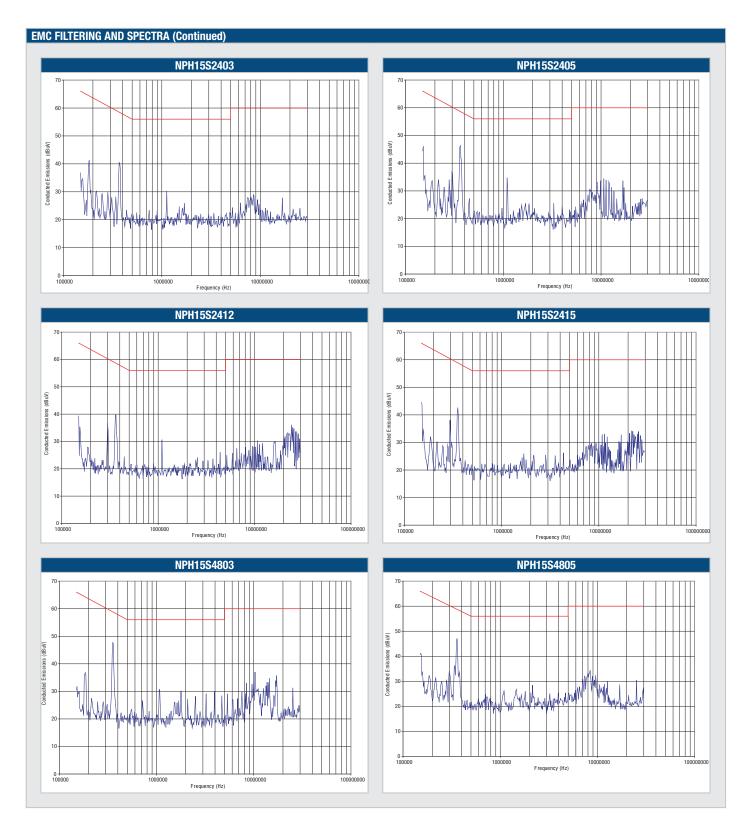
C1, C2 & C4 : Electrolytic capacitors

C3 : Polyester or ceramic capacitor

EMC Spectra red limit line is EN 55022 curve B Quasi-peak average limit.

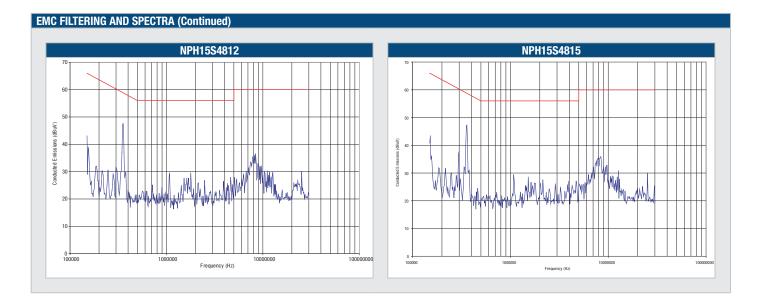
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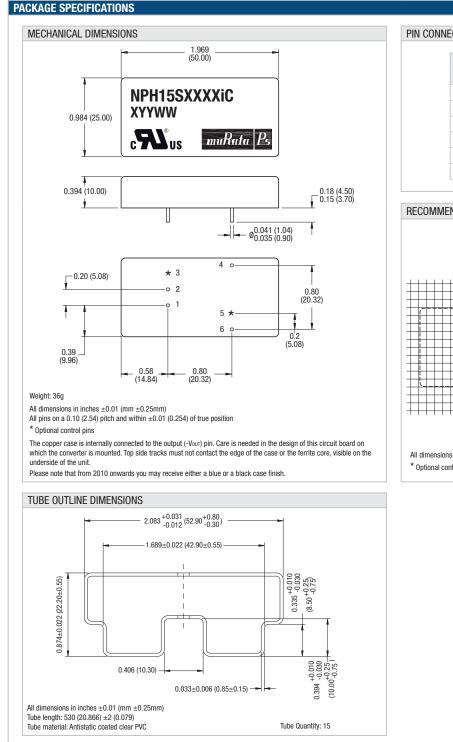
NPH15S Series

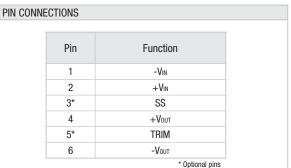
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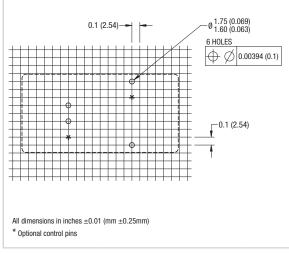
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RECOMMENDED FOOTPRINT DETAILS



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- Medical equipment
- Transportation equipment (automobiles, trains, ships, etc.)
- Traffic signal equipment
- Disaster prevention / crime prevention equipment
- Data Processing equipment

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