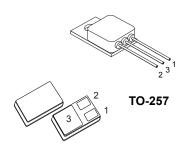
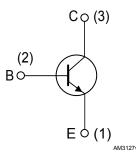


Rad-Hard 80 V, 5 A NPN transistor



SMD.5



Base and Emitter are inverted for 2N5154RSRHRx and 2N5154SRHRx series. For TO-257, the case is not connected to any pin. For SMD.5, the lid is not connected to any pin.

Product status link

2N5154HR

Features

| V _{CEO} | I _C (max.) | h _{FE} at 5 V, 2.5 A | Operating temperature range |
|------------------|-----------------------|----------------------------------|-----------------------------------|
| 80 V | 5 A | > 70 | -65 °C to +200 °C |

- Hermetic package
- · ESCC qualified
- Up to 100 krad(Si) low dose rate

Description

The 2N5154HR is a bipolar transistor able to operate under severe environment conditions and radiation exposure. It provides high reliability performance and immunity to the total ionizing dose (TID).

Qualified as per ESCC 5203/010 specification and available in SMD.5 and TO-257 hermetic packages, it is specifically recommended for space and harsh environment applications and suitable for power suppliers, battery switch and linear bias supply circuits.

In case of discrepancies between this datasheet and the relevant agency specification, the latter takes precedence.

Product summary

| Product summary | | | | |
|-----------------|----------------------|----------------------|---------|--------------------------------|
| Device | Qualification system | Agency specification | Package | Remarks |
| 2N5154ESYHRx | ESCC | 5203/010 | TO-257 | - |
| 2N5154RESYHRx | ESCC | 5203/010 | TO-257 | 100 krad ESCC LDR |
| 2N5154RSHRx | ESCC | 5203/010 | SMD.5 | 100 krad LDR, emitter on pin 1 |
| 2N5154SHRx | ESCC | 5203/010 | SMD.5 | Emitter on pin 1 |
| 2N5154RSRHRx | ESCC | 5203/010 | SMD.5 | 100 krad LDR, emitter on pin 1 |
| 2N5154SRHRx | ESCC | 5203/010 | SMD.5 | Emitter on pin 2 |

Note: See Table 7 for ordering information.



1 Electrical ratings

Table 1. Absolute maximum ratings

| Symbol | Parameter | Value | Unit |
|------------------|---|------------|------|
| V_{CBO} | V _{CBO} Collector-base voltage (I _E = 0) | | V |
| V _{CEO} | Collector-emitter voltage (I _B = 0) | 80 | V |
| V _{EBO} | Emitter-base voltage (I _C = 0) | 6 | V |
| I _C | Collector current | 5 | Α |
| D | Total dissipation at T _{amb} ≤ 25 °C for TO-257 and SMD.5 | 3.3 | W |
| P _{TOT} | Total dissipation at T _{case} ≤ 25 °C for TO-257 and SMD.5 | 35 | W |
| T _{OP} | Operating temperature range | -65 to 200 | °C |
| T _J | Max. operating junction temperature | 200 | °C |

Table 2. Thermal data

| Symbol | Parameter | TO-257 and SMD.5 value | Unit |
|--|--|---------------------------|------|
| R _{thj-case} | R _{thj-case} Thermal resistance junction-case | | °C/W |
| R _{thj-amb} Thermal resistance junction-ambient | | 53 | °C/W |

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2 Electrical characteristics

Table 3. Electrical characteristics (T_{amb} = 25 °C unless otherwise specified)

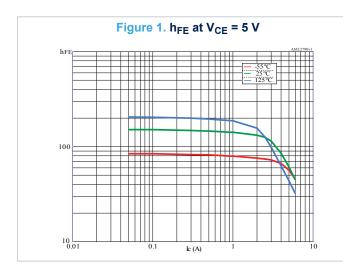
| Symbol | Parameter | Test conditions | Min. | Max. | Unit |
|--------------------------|---|--|------|------|------|
| | | V _{CB} = 60 V, I _E = 0 A | | 1 | |
| I _{CES} | Collector cut-off current | V _{CB} = 60 V, I _E = 0 A, | | 40 | μA |
| | | T _{amb} = 150 °C | | 10 | |
| I _{CEO} | Collector cut-off current | V _{CE} = 40 V, I _B = 0 A | | 50 | μA |
| I | Emitter cut-off current | V _{EB} = 5 V, I _C = 0 A | | 1 | μA |
| I _{EBO} | Emitter cut-off current | V _{EB} = 6 V, I _C = 0 A | | 1 | mA |
| V _{(BR)CEO} (1) | Collector-emitter breakdown voltage | I _C = 100 mA, I _B = 0 A | 80 | | V |
| V (1) | 0-11-4-2-2-244-2-4-4-4-4-4-4-4-4-4-4-4-4- | I _C = 2.5 A, I _B = 0.25 A | | 1.45 | |
| V _{CE(sat)} (1) | Collector-emitter saturation voltage | I _C = 5 A, I _B = 0.5 A | | 1.5 | V |
| V (1) | Door one the continuation well as | I _C = 2.5 A, I _B = 0.25 A | | 1.45 | V |
| V _{BE(sat)} (1) | Base-emitter saturation voltage | I _C = 5 A, I _B = 0.5 A | | 2.2 | |
| | | I _C = 50 mA, V _{CE} = 5 V | 50 | | |
| ı. (1) | DO summed main | I _C = 2.5 A, V _{CE} = 5 V | 70 | 200 | |
| h _{FE} (1) | DC current gain | I _C = 5 A, V _{CE} = 5 V | 40 | | |
| | | I _C = 2.5 A, V _{CE} = 5 V, T _{amb} = -55 °C | 35 | | |
| h _{fe} | AC forward current transfer ratio | I _C = 500 mA, f = 20 MHz, V _{CE} = 5 V | 3.5 | | |
| C _{obo} | Output capacitance | I _E = 0 A, f = 1 MHz, V _{CB} = 10 V | | 250 | pF |
| t _{on} | Turn-on time | V _{CC} = 30 V, V _{BB} = -4 V, | | 0.5 | μs |
| t | Turn-off time | $V_{in} \approx 51 \text{ V, } I_C = 5 \text{ A,}$ | | 1.3 | |
| t _{off} | iurn-on time | $I_{B1} = -I_{B2} = 0.5 \text{ A}$ | | 1.3 | μs |

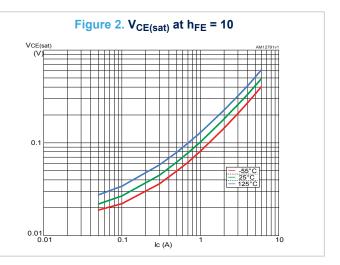
^{1.} Pulsed duration = 300 μs, duty cycle ≤ 2%

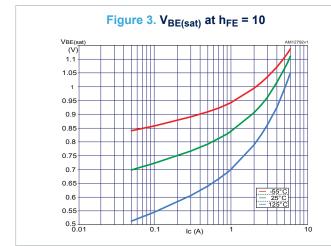
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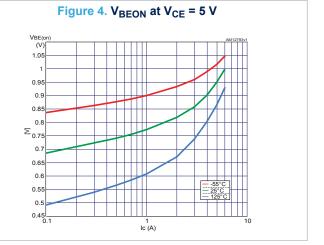


3 Electrical characteristics (curves)







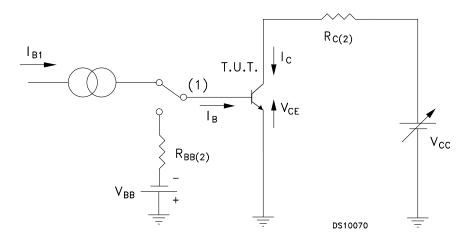


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3.1 Test circuits

Figure 5. ESCC resistive load switching test circuit



Note: (1) Fast electronic switch

Note: (2) Non-inductive resistor

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4 Radiation hardness assurance

This products is guaranteed in radiation as per ESCC 22900 and in compliance with ESCC 5203/010 specification.

Each lot is tested in radiation according to the following procedure:

- · Radiation condition of 0.1 rad (Si)/s
- Test of 11 samples by wafer, 5 biased at 80% of $V_{(BR)CEO}$, 5 unbiased and for reference
- Acceptance criteria of each wafer at 100 krad if all 10 samples comply with the post radiation electrical characteristics as per Table 4.

Table 4. ESCC 5203/010 post radiation electrical characteristics (T_{amb} = 25 °C unless otherwise specified)

| Symbol | Parameter | Test conditions | Min. | Max. | Unit |
|-----------------------------------|---|---|------|-------|------|
| I _{CES} | Collector cut-off current | V _{CB} = 60 V, I _E = 0 A | | 1 | μA |
| I _{CEO} | Collector cut-off current | V _{CE} = 40 V, I _B = 0 A | | 50 | μA |
| l | Emitter out off ourrent | V _{EB} = 5 V, I _C = 0 A | | 1 | μA |
| I _{EBO} | Emitter cut-off current | V _{EB} = 6 V, I _C = 0 A | | 1 | mA |
| V _{(BR)CEO} (1) | Collector-emitter breakdown voltage | I _C = 100 mA, I _B = 0 A | 80 | | V |
| V (1) | Callacter emitter acturation voltage | I _C = 2.5 A, I _B = 0.25 A | | 1.45 | V |
| VCE(sat) | $V_{CE(sat)}^{(1)}$ Collector-emitter saturation voltage $I_C = 5 \text{ A}, I_B = 0$ | I _C = 5 A, I _B = 0.5 A | | 1.5 | V |
| V (1) | Dage emitter esturation valtage | I _C = 2.5 A, I _B = 0.25 A | | 1.45 | V |
| V _{BE(sat)} (1) | Base-emitter saturation voltage | I _C = 5 A, I _B = 0.5 A | | 2.2 V | V |
| | | I _C = 50 mA, V _{CE} = 5 V | [25] | | |
| [h _{FE}] ⁽¹⁾ | Post irradiation gain calculation ⁽²⁾ | I _C = 2.5 A, V _{CE} = 5 V | [35] | 200 | |
| | | I _C = 5 A, V _{CE} = 5 V | [20] | | 1 |

^{1.} Pulsed duration = 300 μ s, duty cycle \leq 2%

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^{2. [}hFE] calculated according to method 1019 of MIL-STD-750.

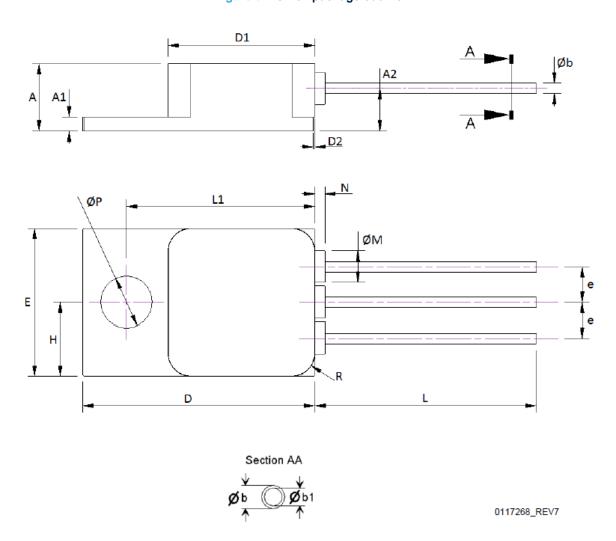


5 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

5.1 TO-257 package information

Figure 6. TO-257 package outline



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Table 5. TO-257 package mechanical data

| Dim. | mm | | | |
|--------|-------|-------|-------|--|
| Dilli. | Min. | Тур. | Max. | |
| А | 4.83 | 4.95 | 5.08 | |
| A1 | 0.89 | 1.02 | 1.14 | |
| A2 | 2.91 | 3.05 | 3.18 | |
| b | 0.64 | - | 1.02 | |
| b1 | 0.64 | 0.76 | 0.89 | |
| D | 16.51 | 16.64 | 16.76 | |
| D1 | 10.41 | 10.54 | 10.67 | |
| D2 | - | - | 0.97 | |
| е | 2.41 | 2.54 | 2.67 | |
| E | 10.41 | 10.54 | 10.67 | |
| Н | 5.13 | 5.25 | 5.38 | |
| L | 15.24 | 15.88 | 16.51 | |
| L1 | 13.39 | 13.51 | 13.64 | |
| M | 2.16 | 2.29 | 2.41 | |
| N | - | - | 0.71 | |
| Р | 3.56 | 3.68 | 3.81 | |
| R | - | 1.65 | - | |

Note: The case is not connected to any pin.

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5.2 SMD.5 package information

Figure 7. SMD.5 package outline

7386434_REV7

Top view

В

Table 6. SMD.5 package mechanical data

□.004 C> ←

| Dim. | mm | | | |
|--------|-------|-------|-------|--|
| Dilli. | Min. | Тур. | Max. | |
| Α | 2.84 | | 3.30 | |
| A1 | 0.25 | 0.38 | 0.51 | |
| b | 7.13 | 7.26 | 7.39 | |
| b1 | 5.58 | 5.72 | 5.84 | |
| b2 | 2.28 | 2.41 | 2.54 | |
| b3 | 2.92 | 3.05 | 3.18 | |
| D | 10.03 | 10.16 | 10.28 | |
| D1 | 0.76 | | | |
| E | 7.39 | 7.52 | 7.64 | |
| е | | 1.91 | | |

Note:

Pad 1: emitter

Pad 2: base

Pad 3: collector

- base and emitter are inverted for 2N5154RSRHRx and 2N5154SRHRx series
- the lid is not connected to any pin

b

Bottom view

⊕ .014 M C A

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6

Ordering information

Contact ST sales office for information about specific conditions for products in die form.

^{1.} Specific marking only. The full marking includes in addition: For the Engineering Models: ST logo, date code; country of origin (FR). For ESCC flight parts: ST logo, date code, country of origin (FR), ESA logo, serial number of the part within the assembly lot.

^{2.} EM: Engineering model



7 Other information

7.1 Date code

Date code information is described in the table below.

Table 8. Date codes

| Model | Date codes1 |
|-------|-------------|
| EM | 3yywwN |
| ESCC | yywwN |

^{1.} yy = year, ww = week number, N = lot index in the week.

7.2 Documentation

Documentation is provided for each product as per below table.

Table 9. Documentation summary

| Quality level | Radiation level | Documentation | |
|-------------------|---|---|--|
| Engineering model | Engineering model - Certificate of conformance. | | |
| ESCC | ESCC - Certificate of conformance. ESCC qualification maintenance lot reference. | | |
| ESCC | 100 Krad | Certificate of conformance. ESCC qualification maintenance lot reference. Radiation data at 30 / 50 / 70 / 100 krad at 0.1 rad / s. | |

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Revision history

Table 10. Document revision history

| Date | Version | Changes |
|-------------|---------|--|
| 08-Jan-2009 | 1 | Initial release. |
| 08-Jan-2010 | 2 | Modified Table 1: Device summary. |
| 22-Jul-2011 | 3 | Updated marking for the order code 2N5154ESYHRB in Table 1: Device summary. |
| 12-Sep-2012 | 4 | Added: Section 2.1: Electrical characteristics (curves) on page 5. |
| 29-Jan-2014 | 5 | Added Section 3: Radiation hardness assurance and Section 5: Ordering information. Updated Table 1: Device summary. |
| 08-Apr-2014 | 6 | Updated Table 1: Device summary and Table 10: Order codes. Updated Section 4: Package mechanical data. Added Figure 2.: Safe operating area. Minor text changes. |
| 29-Jan-2016 | 7 | Updated Figure 2.: Safe operating area. Minor text changes. |
| 05-Apr-2016 | 8 | Added part number 2N5154RSRHRG. Document updated accordingly. Updated Section 4: Package information. |
| 02-May-2016 | 9 | Updated package silhouette, Figure 1: Internal schematic diagram and Table 1: Device summary in cover page. Updated Section 4: Package information and Section 5: Ordering information. Inserted Section 6: Shipping details. Minor text changes. |
| 28-Oct-2016 | 10 | Added Table 6: Product mass summary. |
| 27-Jul-2020 | 11 | Removed TO-39 package information. Updated Section 5.1 TO-257 package information, Section 5.2 SMD.5 package information and Ordering information. Minor text changes. |
| 25-Mar-2021 | 12 | Updated Table 1, Figure 1 and Table 7. |
| 04-Oct-2021 | 13 | Updated Table 7 and Description. |

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