

P0102DN

Sensitive 0.8 A SCR thyristor

Datasheet - production data

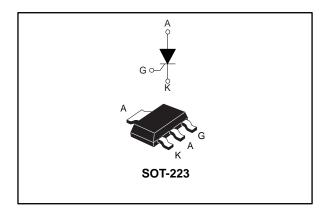
Unit

А

V

mΑ

°C



Symbol

I_{T(RMS)}

Vdrm/Vrrm

lgт

T_i max.

Description

0.8 A P0102DN SCR thyristor is suitable for all applications where available gate current is limited. This device offers a high blocking voltage of 400 V, ideal for applications like interrupters circuits in 110 V mains regions.

Thanks to highly sensitive triggering levels, the

The surface mount SOT-223 package allows compact, SMD based designs for automated manufacturing.

Table 1: Device summary

Value

0.8

400

0.2

125

- I_{T(RMS)} 0.8 A
- 125 °C max T_i
- Low 0.2 mA gate current
- 400 V V_{DRM}/V_{RRM}
- ECOPACK[®]2 compliant component

Applications

- Proximity sensors
- Gate driver for large Thyristors
- Overvoltage crowbar protection
- Ground fault circuit interrupters
- Arc fault circuit interrupter
- Solid state relay pilot circuit
- Standby mode power supplies
- Residual current detector

DocID031119 Rev 1

This is information on a product in full production.

1

Characteristics

Table 2: Absolute maximum ratings (limiting values), T_j = 25 °C unless otherwise specified

Symbol	Parameter	Value	Unit		
I _{T(RMS)}	RMS on-state current (180 ° conduction	Tamb = 70 °C	0.8	٨	
I _{T(AV)}	Average on-state current (180 ° conduc	tion angle)	$T_{amb} = 70^{\circ}C$	0.5	A
	Non repetitive surge peak on-state curr	t _p = 8.3 ms	8	٨	
ITSM	$(T_j initial = 25 °C$			7	A
l²t	I ² t value for fusing	t _p = 10 ms	0.24	A ² s	
dl/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100 \text{ ns}$	f = 60 Hz	T _j = 125 °C	50	A/µs
Vdrm/Vrrm	Repetitive peak off-state voltage		T _j = 125 °C	400	V
Igм	Peak gate current t _p = 20 µs		T _j = 125 °C	1	А
P _{G(AV)}	Average gate power dissipation	T _j = 125 °C	0.1	W	
T _{stg}	Storage junction temperature range	-40 to +150	°C		
Tj	Operating junction temperature	-40 to +125	°C		

Table 3: Electrical characteristics (T_j = 25 °C unless otherwise specified)

Symbol	Test conditions		Value	Unit	
lgт	V- 12 V D = 140 O	Max.	200	μΑ	
Vgt	$V_{D} = 12 V, R_{L} = 140 \Omega$	Max.	0.8	V	
V_{GD}	$V_D = V_{DRM}, R_L = 3.3 \text{ k}\Omega, R_{GK} = 1000 \Omega$ $T_j = 125 \text{ °C}$			0.1	V
V _{RG}	I _{RG} = 10 μA	Min.	8	V	
Ι _Η	I_T = 50 mA, R_{GK} = 1000 Ω	Max.	5	mA	
١L	I _G = 1.2 x I _{GT} , R _{GK} = 1000 Ω	Max.	6	mA	
dV/dt	$V_D = 67 \ \% \ V_{DRM}, \ R_{GK} = 1000 \ \Omega $ $T_j = 125 \ ^\circ C$		Min.	75	V/µs

Table 4: Static characteristics

Symbol	Test conditio	Value	Unit			
Vтм	I _{TM} = 1.6 A, t _p = 380 μs	T _j = 25 °C	Max.	1.95	V	
Vto	Threshold voltage	T _j = 125 °C	Max.	0.95	v	
R _D	Dynamic resistance	T _j = 125 °C	Max.	600	mΩ	
Idrm/Irrm	$V_D = V_{DRM}$; $V_R = V_{RRM}$, $R_{GK} = 1000 \ \Omega$	T _j = 25 °C	Maria	1	μA	
		T _j = 125 °C	Max.	100		

Table 5: Thermal parameters

Symbol	Parameter	Value	Unit	
Rth(j-t)	Junction to tab (DC)	30		
Rth(j-a)	Junction to ambient (DC)	$S^{(1)} = 5 \text{ cm}^2$	60	°C/W

Notes:

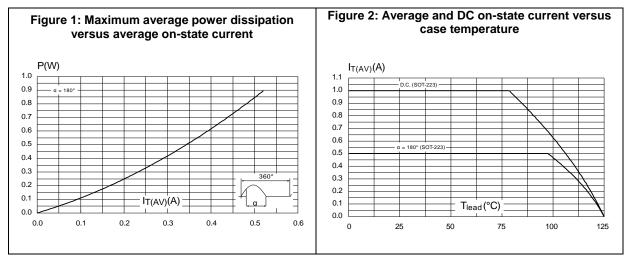
 $^{(1)}S$ = copper surface under tab.

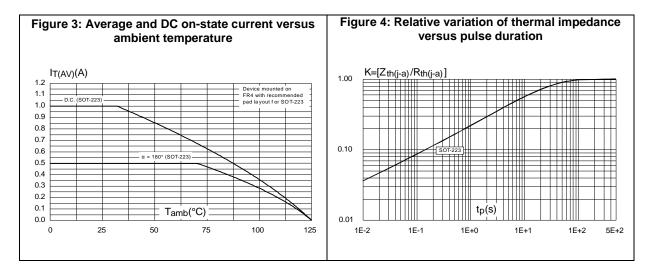


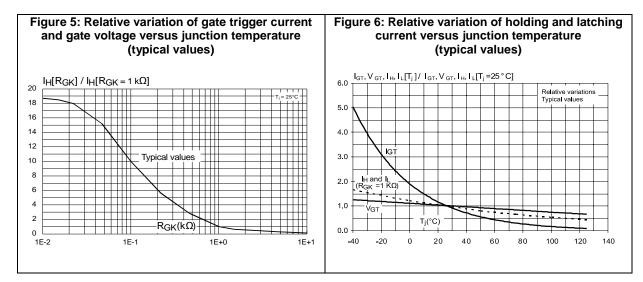
P0102DN

51

1.1 Characteristics (curves)



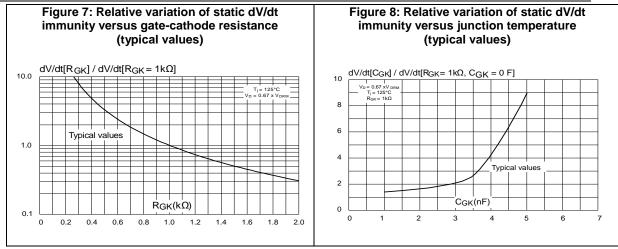


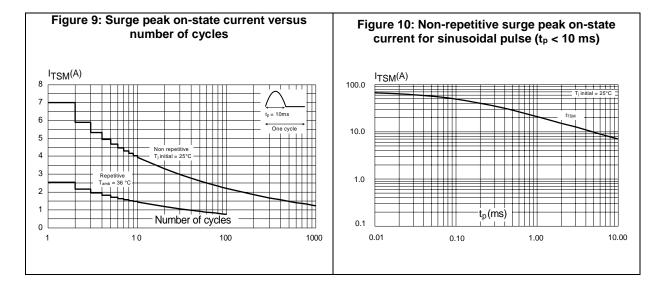


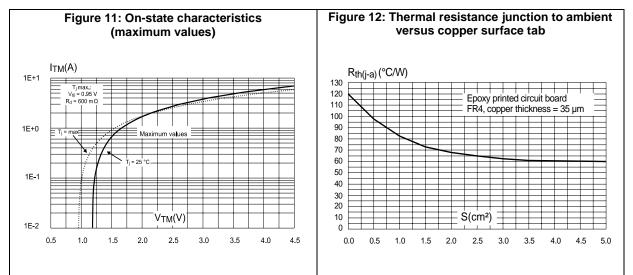
DocID031119 Rev 1

Characteristics

P0102DN







DocID031119 Rev 1



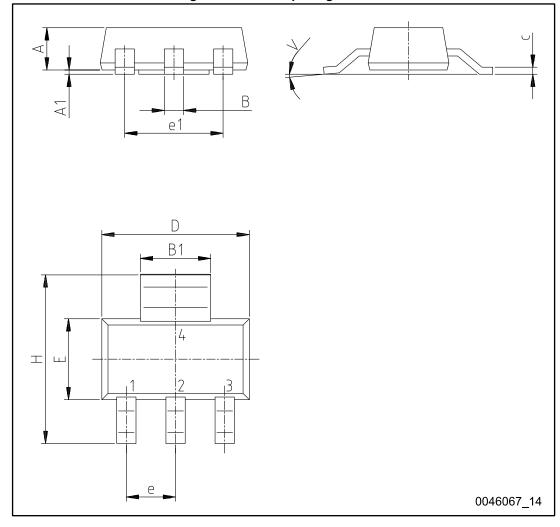
2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

- Lead-free package
- Halogen free molding resin
- Epoxy meets UL94, V0

2.1 SOT-223 package information

Figure 13: SOT-223 package outline





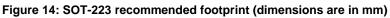
Package information

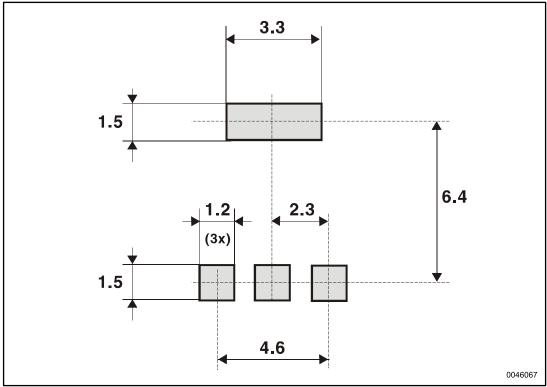
	Table 6: SOT-223 package mechanical data							
Dim		Millimeters			Inches ⁽¹⁾			
Dim.	Min.	Тур.	Max.	Min.	Тур.	Max.		
А			1.8			0.0709		
A1	0.02		0.1	0.0008		0.0039		
В	0.6	0.7	0.85	0.0236	0.0276	0.0335		
B1	2.9	3	3.15	0.1142	0.1181	0.1240		
с	0.24	0.26	0.35	0.0094	0.0102	0.0138		
D ⁽²⁾	6.3	6.5	6.7	0.2480	0.2559	0.2638		
е		2.3			0.0906			
e1		4.6			0.1811			
E	3.3	3.5	3.7	0.1299	0.1378	0.1457		
Н	6.7	7.0	7.3	0.2638	0.2756	0.2874		
V			10º			10°		

Notes:

⁽¹⁾Inches dimensions given only for reference

⁽²⁾Does not include mold flash or protusions. Mold flash or protusions must not exceed 0.15 mm (0.006 inches)







3 Ordering information

Series P = sensitive SCR, high immunity	P01 02	D 	N 	- xx	xx	
Gate sensitivity						
02 = 200 μA						
Voltage D = 400 V						
Package						
N = SOT-223						
Delivery mode (Packing)						
5AA4 = Tape and reel 7"						

Figure 15: Ordering information scheme

Table 7: Ordering information

Order code	Marking	ing Package Weight		Base qty.	Delivery mode	
P0102DN 5AA4	P2D	SOT-223	0.12 g	1000	Tape and reel 7"	

4 Revision history

Table 8: Document revision history

Date	Revision	Changes
26-Oct-2017	1	Initial release.



IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2017 STMicroelectronics - All rights reserved

