





#### 1 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

### **Features**

- IEC 61000-4-2 (ESD): Air ±15kV, Contact ±8kV
- 1 Channel of ESD protection
- Low Channel Input Capacitance of 0.85pF Typical
- Typically Used at High Speed Ports such as USB 2.0, IEEE1394, Serial ATA, DVI, HDMI, PCI
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

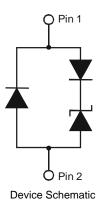
#### **Mechanical Data**

- Case: SOD323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.004 grams (Approximate)





Top View



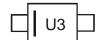
### Ordering Information (Note 4)

Part Number	Case	Packaging
D1213A-01WS-7	SOD323	3,000/Tape & Reel

Notes:

- $1.\ No\ purposely\ added\ lead.\ Fully\ EU\ Directive\ 2002/95/EC\ (RoHS)\ \&\ 2011/65/EU\ (RoHS\ 2)\ compliant.$
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

# **Marking Information**



U3 = Product Type Marking Code Line Denotes Pin 1



### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current	Ipp	5	Α	8/20µs, Per Figure 3
ESD Protection – Contact Discharge	V <sub>ESD_Contact</sub>	±8	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V <sub>ESD_Air</sub>	±15	kV	Standard IEC 61000-4-2

### **Thermal Characteristics**

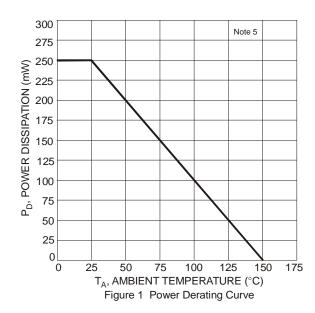
Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P <sub>D</sub>	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	500	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

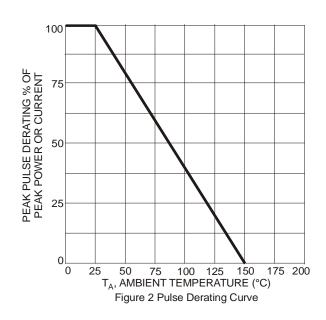
### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse working voltage	VRWM	_	ı	3.3	V	_
Reverse current (Note 6)	I <sub>R</sub>	_	0.1	1.0	μA	$V_R = V_{RWM} = 3.3V$
Reverse breakdown voltage	$V_{BR}$	6.0	_	1	V	$I_R = 1mA$
Forward voltage	$V_{F}$	0.6	0.8	0.95	V	$I_F = 8mA$
Reverse clamping voltage, Positive Transients	V <sub>CL1</sub>		10.0	-	V	$I_{PP} = 1A, t_p = 8/20 \mu s$
Reverse clamping voltage, Negative Transients	V <sub>CL2</sub>		-1.7		V	$I_{PP} = -1A, t_p = 8/20 \mu s$
Dynamic resistance	$R_{DYN}$		0.9		Ω	$I_R = 1A$ , $t_p = 8/20 \mu s$
Capacitance	Ст	_	0.85	1.2	pF	V <sub>R</sub> = 1.65V, f = 1MHz

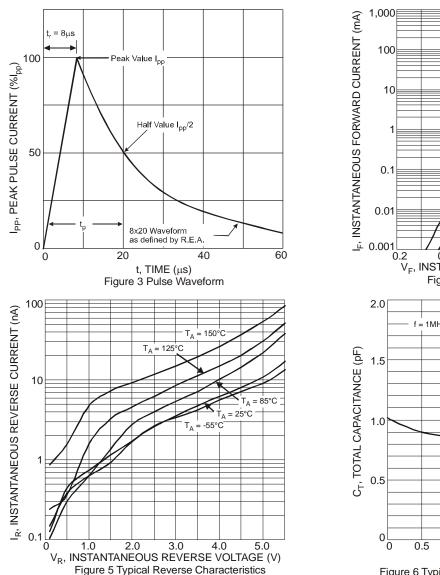
Notes:

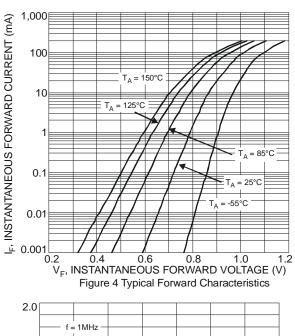
- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.
- 6. Short duration pulse test used to minimize self-heating effect.
  7. For information on the impact of Diodes' USB 2.0 compatible ESD protectors on signal integrity including eye diagram plots, please refer to AN77 at the following URL: http://www.diodes.com/destools/appnote\_dnote.html.











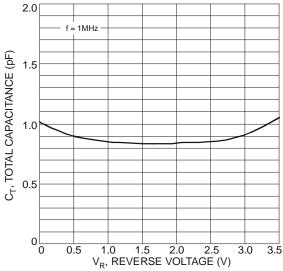
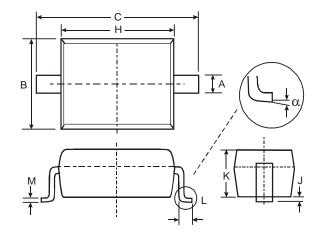


Figure 6 Typical Total Capacitance vs. Reverse Voltage

# **Package Outline Dimensions**

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

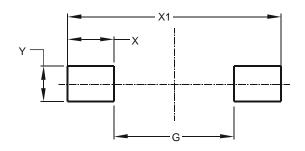


SOD323					
Dim	Min	Max			
Α	0.25	0.35			
В	1.20	1.40			
C	2.30	2.70			
Η	1.60	1.80			
7	0.00	0.10			
K	1.0	1.1			
L	0.20	0.40			
М	0.10	0.15			
α	0°	8°			
All Dimensions in mm					



## **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
G	1.520
Х	0.590
X1	2.700
Υ	0.450

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