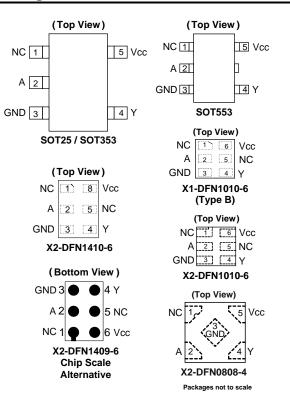


## Description

The 74LVC1G06 is a single inverter gate with an open drain output. The device is designed for operation with a power supply range of 1.65V to 5.5V. The input is tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output preventing damaging current backflow when the device is powered down. The open-drain output can be connected to other open drain outputs to implement active-low wired-OR or active-high wired-AND functions. The maximum sink current is 32mA.

## **Pin Assignments**



## Features

- Wide Supply Voltage Range from 1.65 to 5.5V
- 24mA Sink Current at 3.3V
- CMOS Low Power Consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs Accept up to 5.5V
- ESD Protection Tested per JESD 22
  - Exceeds 200-V Machine Model (A115)
  - Exceeds 2000-V Human Body Model (A114)
  - Exceeds 1000-V Charged Device Model (C101)
- Latch-Up Exceeds 100mA per JESD 78, Class I
- Range of Package Options
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products Such as:
  - PCs, Networking, Notebooks, Netbooks, PDAs
  - Tablet Computers, E-readers
  - Computer Peripherals, Hard Drives, CD/DVD ROM
  - TV, DVD, DVR, Set Top Box
  - Cell Phones, Personal Navigation / GPS
  - MP3 players ,Cameras, Video Recorders
- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



## Ordering Information (Note 4)

Logic Device 74 : Logic Prefix LVC : 1.65 to 5.5 V Logic Family 1G : One Gate		Function 06 : 1-Input Inverter Buffer with open drai output		-7 : 7" Ta -4 0-6 (Type B) -6 9-6	acking ıpe & Reel	
Part Number	Package Code	Package	Package	7" Tape and Reel		
	· · ······go · · ···	(Notes 5 & 6)	Size	Quantity	Part Number Suffix	
74LVC1G06W5-7	W5	SOT25	3.0mm x 2.8mm x 1.2mm 0.95 mm lead pitch	3,000/Tape & Reel	-7	
74LVC1G06SE-7	SE	SOT353	2.0mm x 2.0mm x 1.1mm 0.65 mm lead pitch	3,000/Tape & Reel	-7	
74LVC1G06Z-7	Z	SOT553	1.6mm x 1.6 mm x 0.62mm 0.5 mm lead pitch	4,000/Tape & Reel	-7	
74LVC1G06FS3-7	FS3	X2-DFN0808-4	0.8mm x 0.8 mm x 0.35mm 0.5 mm pad pitch (diamond)	5,000/Tape & Reel	-7	
74LVC1G06FW5-7	FW5	X1-DFN1010-6 (Type B)	1.0mm x 1.0mm x 0.5mm 0.35 mm pad pitch	5,000/Tape & Reel	-7	
74LVC1G06FW4-7	FW4	X2-DFN1010-6	1.0mm x 1.0mm x 0.4mm 0.35 mm pad pitch	5,000/Tape & Reel	-7	
74LVC1G06FX4-7	FX4	X2-DFN1409-6 Chip scale alternative	1.4mm x 0.9mm x 0.4mm 0.5 mm pad pitch	5,000/Tape & Reel	-7	
74LVC1G06FZ4-7	FZ4	X2-DFN1410-6	1.4mm x 1.0mm x 0.4mm 0.5 mm pad pitch	5,000/Tape & Reel	-7	

For packaging details, go to our website at http://www.diodes.com/products/packages.html.
Pad layout as shown on Diodes Inc. suggested pad layout which can be found on our website at http://www.diodes.com/package-outlines.html.
The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.

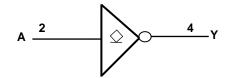
## **Pin Descriptions**

Pin Name	Description
NC	No Connection
A	Data Input
GND	Ground
Y	Data Output
V <sub>CC</sub>	Supply Voltage

# **Function Table**

Inputs	Output
Α	Y
Н	L
L	Z

# Logic Diagram





Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State.	-0.5 to 6.5	V
I <sub>IK</sub>	Input Clamp Current VI < 0	-50	mA
loк	Output Clamp Current	-50	mA
lo	Continuous Output Current	±50	mA
I <sub>CC</sub> , I <sub>GN</sub> Continuous Current Through V <sub>CC</sub> or GND		±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	°C

## Absolute Maximum Ratings (Notes 7 & 8) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Notes: 7. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

8. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

## Recommended Operating Conditions (Note 9) (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Symbol		Parameter	Min	Max	Unit	
M		Operating	1.65	5.5	V	
V <sub>CC</sub>	Operating Voltage	Data retention only	1.5	—	V	
		V <sub>CC</sub> = 1.65V to 1.95V	0.65 x V <sub>CC</sub>	—		
	Ligh Lovel Input ) (altage	V <sub>CC</sub> = 2.3V to 2.7V	1.7	—	V	
VIH	High-Level Input Voltage	$V_{CC} = 3V$ to 3.6V	2	—	v	
		V <sub>CC</sub> = 4.5V to 5.5V	0.7 x V <sub>CC</sub>	—		
		V <sub>CC</sub> = 1.65V to 1.95V	—	0.35 x V <sub>CC</sub>		
		V <sub>CC</sub> = 2.3V to 2.7V	_	0.7	N	
VIL	Low-Level Input Voltage	V <sub>CC</sub> = 3V to 3.6V	—	0.8	V	
		V <sub>CC</sub> = 4.5V to 5.5V	_	0.3 x V <sub>CC</sub>		
VI	Input Voltage		0	5.5	V	
Vo	Output Voltage		0	5.5	V	
		V <sub>CC</sub> = 1.65V	—	4		
		V <sub>CC</sub> = 2.3V	_	8		
	Low-Level Output Current	$V_{CC} = 2.7 V$	_	12	mA	
I <sub>OL</sub>			_	16	ША	
		$V_{CC} = 3V$	—	24		
		$V_{CC} = 4.5V$	—	32		
		$V_{CC} = 1.8V \pm 0.15V, 2.5V \pm 0.2V$	—	20		
$\Delta t / \Delta V$	Input Transition Rise or Fall Rate	$V_{CC} = 3.3 V \pm 0.3 V$	—	10	ns/V	
		$V_{\rm CC} = 5V \pm 0.5V$	_	5		
T <sub>A</sub>	Operating Free-Air Temperature	_	-40	+125	°C	

Note: 9. Unused inputs should be held at  $V_{CC}$  or Ground.



Sumbol	Parameter	Test Conditions	N N	-40°C to +85°C			-40°C to	Unit	
Symbol	Symbol	Test Conditions	Vcc	Min	Тур	Max	Min	Max	Unit
		I <sub>OL</sub> = 100μA	1.65V to 5.5V	—	_	0.1	—	0.1	
		I <sub>OL</sub> = 4mA	1.65V	—	—	0.45	—	0.7	
	V <sub>OL</sub> Low Level Output Voltage	I <sub>OL</sub> = 8mA	2.3V	—	—	0.3	—	0.45	
Vol		I <sub>OL</sub> = 12mA	2.7	—	—	0.4	—	0.6	V
		$I_{OL} = 16 \text{mA}$	3V	—	—	0.4	—	0.6	
		$I_{OL} = 24mA$	37	—	—	0.55	—	0.8	1
		I <sub>OL</sub> = 32mA	4.5V	—	—	0.55	—	0.8	
h	Input Current	$V_I = 5.5 V \text{ or GND}$	0 to 5.5V	—	± 0.1	±5	—	± 100	μA
IOFF	Power Down Leakage Current	$V_{I}$ or $V_{O} = 5.5V$	0V	_	_	±10	_	±200	μA
Icc	Supply Current	$V_I = 5.5V$ or GND $I_O = 0$	5.5V	—	0.1	10	—	200	μA
ΔIcc	Additional Supply Current	Input at V <sub>CC</sub> – 0.6 V	3V to 5.5V	_	_	500	_	5,000	μA
Cı	Input Capacitance	$V_I = V_{CC} - or GND$	3.3V	_	5	_	_	_	pF

## **Electrical Characteristics** (All typical values are at $V_{CC} = 3.3V$ , $T_A = +25^{\circ}C$ )



Symbol	Parameter	Test Conditions	Vcc	Min	Тур.	Max	Unit
		SOT25		—	204	_	
		SOT353		—	371	_	
		SOT553		—	231	_	
0	Thermal Resistance	X2-DFN0808-4	$(N_{oto}, 10)$	—	400	_	°C M
θ <sub>JA</sub>	Junction-to-Ambient	X1-DFN1010-6 (Type B)	(Note 10)	—	435	_	°C/W
		X2-DFN1010-6		—	445	_	
		X2-DFN1409-6		—	470	_	
		X2-DFN1410-6	X2-DFN1410-6		460	_	
		SOT25		—	52	_	
		SOT353		—	143	-	
		SOT553		—	105	-	
0	Thermal Resistance	X2-DFN0808-4	(Nata 10)	—	225	_	°C/W
θյς	Junction-to-Case	X1-DFN1010-6 (Type B)	(Note 10)	—	250	_	°C/vv
		X2-DFN1010-6		—	250	_	
		X2-DFN1409-6		—	275	_	
		X2-DFN1410-6			265	_	1

## **Package Characteristics** (All typical values are at $V_{CC} = 3.3V$ , $T_A = +25^{\circ}C$ )

Note: 10. Test condition for each of the 8 package types: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

## **Switching Characteristics**

Figure 1 Typical Values at  $T_A = +25^{\circ}C$  and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

Parameter	From	То	Var	TA	= -40°C to +8	5°C	T <sub>A</sub> = -40°C	to +125°C	Unit			
Falameter	Input	Output	V <sub>cc</sub>	Min	Тур	Max	Min	Max	Unit			
			1.8V ± 0.15V	1.0	3.0	6.5	1.0	8.5				
			2.5V ± 0.2V	0.5	1.9	4.0	0.5	5.5				
t <sub>pd</sub>	A or B	Y	2.7V	0.5	2.5	4.5	0.5	6.0	ns			
			3.3 V ± 0.3V	0.5	2.3	4.0	0.5	5.5				
							$5.0V \pm 0.5V$	0.5	1.7	3.0	0.5	4.0

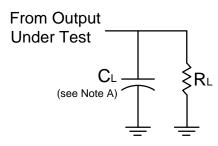
# **Operating Characteristics**

 $T_A = +25^{\circ}C$ 

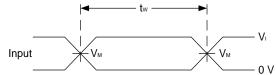
Parameter		Test Conditions	V <sub>CC</sub> = 1.8V Typ	V <sub>CC</sub> = 2.5V Typ	V <sub>CC</sub> = 3.3V Typ	V <sub>CC</sub> = 5V Typ	Unit
C <sub>pd</sub>	Power Dissipation Capacitance	f = 10MHz	3	3	4	6	pF



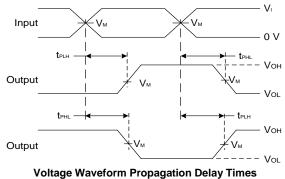
## **Parameter Measurement Information**



V	Inputs		V	V	6	P	
V <sub>cc</sub>	VI	t <sub>r</sub> /t <sub>f</sub>	V <sub>M</sub>	V <sub>LOAD</sub>	C∟	RL	VΔ
1.8V±0.15V	Vcc	≤2ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	30pF	1KΩ	0.15V
2.5V±0.2V	Vcc	≤2ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	30pF	500Ω	0.15V
2.7V	2.7V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
3.3V±0.3V	3V	≤2.5ns	1.5V	6V	50pF	500Ω	0.3V
5V±0.5V	V <sub>CC</sub>	≤2.5ns	V <sub>CC</sub> /2	2 X V <sub>CC</sub>	50pF	500Ω	0.3V



Voltage Waveform Pulse Duration



Inverting and Non Inverting Outputs

#### Figure 1 Load Circuit and Voltage Waveforms

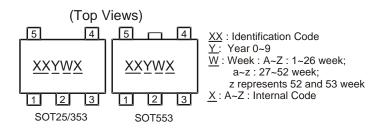
Notes:

- A. Includes test lead and test apparatus capacitance.
- B. All pulses are supplied at pulse repetition rate  $\leq$  10 MHz C. The inputs are measured one at a time with one transition per measurement.
- D. For the open drain device  $t_{\mathsf{PLZ}}$  and  $t_{\mathsf{PZL}}$  are the same as  $t_{\mathsf{PD}}$
- E. t<sub>PZL</sub> is measured at V<sub>M</sub>.
- F. t<sub>PLZ</sub> is measured at V<sub>OL</sub> +V\_ $\Delta$



## **Marking Information**

### (1) SOT25, SOT353 and SOT553



Part Number	Package	Identification Code
74LVC1G06W5-7	SOT25	UM
74LVC1G06SE-7	SOT353	UM
74LVC1G06Z-7	SOT553	UM

#### (2) DFN Packages



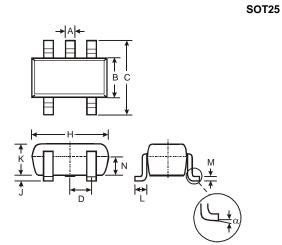
XX : Identification Code Y : Year 0~9 W : Week : A~Z : 1~26 week

 $\frac{1}{\underline{W}}: Week: A-Z: 1-26 week;$ a-z: 27-52 week;z represents 52 and 53 week $\underline{X}: A-Z: Internal Code$ 

Part Number	Package	Identification Code
74LVC1G06FS3-7	X2-DFN0808-4	WM
74LVC1G06FW5-7	X1-DFN1010-6 (Type B)	V5
74LVC1G06FW4-7	X2-DFN1010-6	UM
74LVC1G06FX4-7	X2-DFN1409-6	MD
74LVC1G06FZ4-7	X2-DFN1410-6	UM



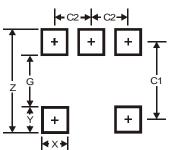
Please see http://www.diodes.com/package-outlines.html for the latest version.



	SOT25				
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	-	-	0.95		
н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
κ	1.00	1.30	1.10		
L	0.35	0.55	0.40		
М	0.10	0.20	0.15		
Ν	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95

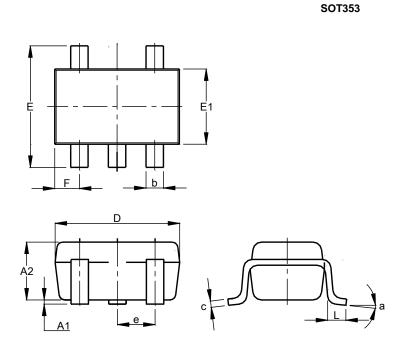
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74LVC1G06 Document number: DS32272 Rev. 11 - 2
Downloaded from Arrow.com.



Please see http://www.diodes.com/package-outlines.html for the latest version.

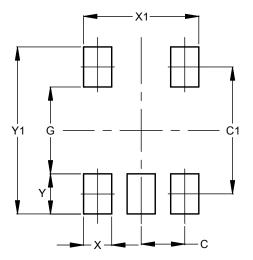


SOT353			
Dim	Min	Max	Тур
A1	0.00	0.10	0.05
A2	0.90	1.00	1.00
b	0.10	0.30	0.25
С	0.10	0.22	0.11
D	1.80	2.20	2.15
Е	2.00	2.20	2.10
E1	1.15	1.35	1.30
е	C	).650 B	SC
F	0.40	0.45	0.425
L	0.25	0.40	0.30
а	0°	8°	
All Dimensions in mm			

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

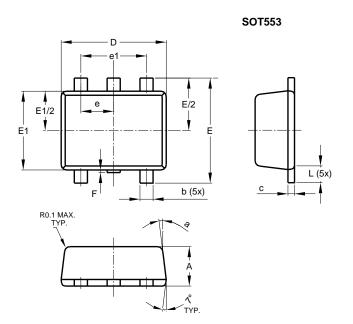
## SOT353



Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Y	0.600
Y1	2.500



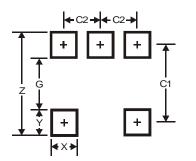
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT553			
Dim	Min	Max	Тур
Α	0.55	0.62	0.60
b	0.15	0.30	0.20
С	0.10	0.18	0.15
D	1.50	1.70	1.60
Е	1.55	1.70	1.60
E1	1.10	1.25	1.20
е	0.50 BSC		
e1	1.00 BSC		
F	0.00	0.10	_
L	0.10	0.30	0.20
а	6°	8°	7°
All Dimensions in mm			

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

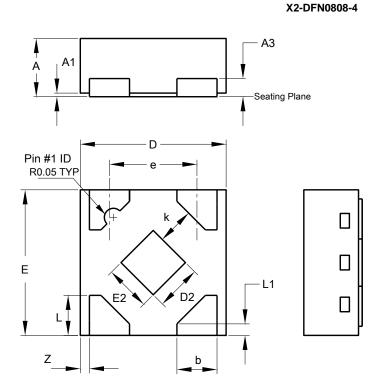


SOT553

Dimensions	Value
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5



Please see http://www.diodes.com/package-outlines.html for the latest version.

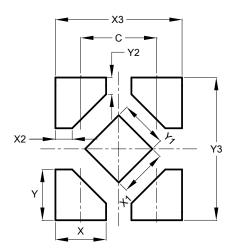


	X2-DFN0808-4				
Dim	Min	Max	Тур		
Α	0.25	0.35	0.30		
A1	0	0.04	0.02		
A3	-	-	0.13		
b	0.17	0.27	0.22		
D	0.75	0.85	0.80		
D2	0.15	0.35	0.25		
Е	0.75	0.85	0.80		
E2	0.15	0.35	0.25		
е	-	-	0.48		
k	0.20	-	-		
L	0.17	0.27	0.22		
L1	0.02	0.12	0.07		
z	-	-	0.05		
A	All Dimensions in mm				

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

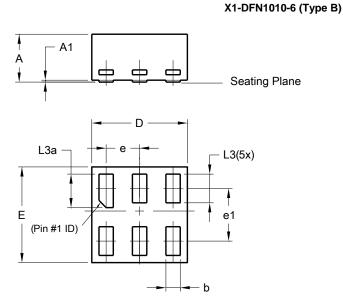
### X2-DFN0808-4



Dimensions	Value
С	0.480
Х	0.320
X1	0.300
X2	0.106
X3	0.800
Y	0.320
Y1	0.300
Y2	0.106
Y3	0.900



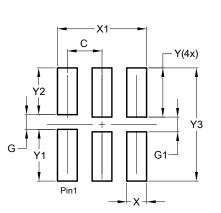
Please see http://www.diodes.com/package-outlines.html for the latest version.



	X1-DFN1010-6 (Type B)			
Dim	Min	Max	Тур	
Α	-	0.50	0.39	
A1	-	0.04	-	
b	0.12	0.20	0.15	
D	0.95	1.050	1.00	
Е	0.95	1.050	1.00	
е	e 0.35 BSC			
e1	0.55 BSC			
L3	0.27	0.30	0.30	
L3a	0.32	0.40	0.35	
All	All Dimensions in mm			

## **Suggested Pad Layout**

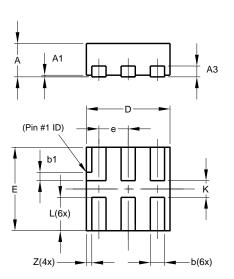
Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)	
С	0.350	
G	0.150	
G1	0.150	
Х	0.200	
X1	0.900	
Y	0.500	
Y1	0.525	
Y2	0.475	
Y3	1.150	



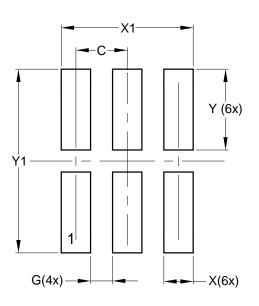
Please see http://www.diodes.com/package-outlines.html for the latest version.



X2-DFN1010-6			
Dim	Min	Max	Тур
Α		0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.14	0.20	0.17
b1	0.05	0.15	0.10
D	0.95	1.05	1.00
ш	0.95	1.05	1.00
e		-	0.35
L	0.35	0.45	0.40
K	0.15		
Z			0.065
All Dimensions in mm			

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



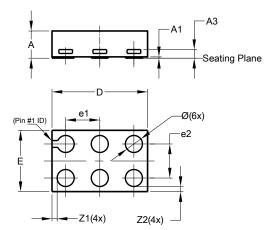
X2-DFN1010-6			
	Dimensions	Value (in mm)	
	С	0.350	
	G	0.150	
	Х	0.200	
	X1	0.900	
	Y	0.550	
	Y1	1.250	

# X2-DFN1010-6



Please see http://www.diodes.com/package-outlines.html for the latest version.

#### X2-DFN1409-6 CHIP SCALE ALTERNATIVE

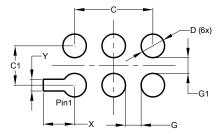


X2-DFN1409-6			
Dim	Min	Max	Тур
Α	-	0.40	0.39
A1	0	0.05	0.02
A3	-	-	0.13
Ø	0.20	0.30	0.25
D	1.35	1.45	1.40
E	0.85	0.95	0.90
e1	-	-	0.50
e2	-	-	0.50
Z1	-	-	0.075
Z2	-	-	0.075
All Dimensions in mm			

# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

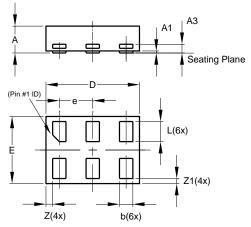
#### X2-DFN1409-6 CHIP SCALE ALTERNATIVE



Dimensions	Value	
Dimensions	(in mm)	
С	1.000	
C1	0.500	
D	0.300	
G	0.200	
G1	0.200	
Х	0.400	
Y	0.150	



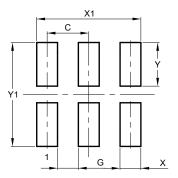
Please see http://www.diodes.com/package-outlines.html for the latest version.



X2-DFN1410-6			
Dim	Min	Max	Тур
Α		0.40	0.39
A1	0.00	0.05	0.02
A3			0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
ш	0.95	1.05	1.00
e			0.50
L	0.25	0.35	0.30
Z		_	0.10
Z1	0.045	0.105	0.075
All Dimensions in mm			

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)	
С	0.500	
G	0.250	
Х	0.250	
X1	1.250	
Y	0.525	
Y1	1.250	

#### X2-DFN1410-6

X2-DFN1410-6



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