LITEON SEMICONDUCTOR

T8M30T800HE



ELECTRICAL CHARACTERISTICS (Tj = 25°C, unless otherwise specified.)

Absolute Ratings

PARAMETER	SYMBOL	VALUE	UNIT
Peak repetitive off-state voltage (Tj = -40 to 150°C, Full sine wave, 50 to 60 Hz; Gate open) (Note 1)	V _{DRM} V _{RRM}	800 800	V
On-stage RMS current (Full sine wave, Tc = 110°C)	I _{T(RMS)}	8	А
Peak non-repetitive surge current (one full cycle 60 HZ, Tj = 25°C)	I _{TSM}	80	А
Circuit fusing consideration (t = 10ms)		21	A ² S
Peak gate current	I _{GM}	2	А
Peak gate power	P_{GM}	5	W
Average gate power		0.5	W
Operating junction temperature range		-40 to +150	°C
Storage temperature range T _{STG}		-40 to +150	°C
		REV. 0, MAR-2017, K	TXC43

Note :

(1) V_{DRM} and V_{RRM} for all types can be applied on a continuous basis.

Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

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Thermal Characteristics

PARAMETER		SYMBOL		UNIT	
Junction to case with heatsink		Rth(j-c)	Max	5	°C/W
Junction to ambient (DC) S ⁽¹⁾ =0.8		Rth(j-a)	Тур	55	°C/W
Maximum lead temperature for soldering purposes (1/8" form case for 10 seconds)		TL	Max	260	°C

1. S= Copper surface under tab.

Static Characteristics

PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT
Threshold Voltage ⁽²⁾ @ Tj = 150°C		V _{to}	-		1	V
Dynamic resistors ⁽²⁾ @ Tj = 150°C		R₀	1	-	37	mΩ
Peak repetitive forward or reverse blocking current	Tj = 25°C	I _{DRM}	-	1	5	uA
(V_{AK} = rated V_{DRM} and V_{RRM} , gate open)	Tj = 150°C	I _{RRM}			2	mA

2. For both polarities of A2 referenced to A1.

ON Characteristics

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Peak forward on-state voltage ($I_{TM} = 12A @ Tj = 25^{\circ}C$)	V _{TM}			1.5	V
$V_D = V_{DRM}$, $R_L = 100\Omega$, Tj=150°C		0.2			V
Gate trigger current (V_{AK} = 12V, R_L =100 Ω)	I _{GT1} I _{GT2} I _{GT3}	 	 	35	mA
Gate trigger voltage (V_{AK} = 12V, R_L =100 Ω)	V _{GT1} V _{GT2} V _{GT3}			1	V
Holding current (V_{AK} = 12V, R_L =100 Ω)				35	m۸
				50	
	I _{L1}	-	-	35	
Latching current (V_{AK} = 12V, R_L =100 Ω)	I _{L2}			45	mA
				35	

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Dynamic Characteristics

PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT
Critical rate of rise of off-stage voltage (V_{AK} = 67% rated V_{DRM} , @ Tj = 150°C, gate open)		dv/dt	1000			V/us
Critical rate of rise of on-state current $(V_{DRM}=maximum V_{DRM}, Tj = 150^{\circ}C)$		di/dt(s)			100	A/us
Rate of change of commutating current	Without snubber, VD=400V, Tj=150°C	di/dt(c)	3			A/ms



Symbol	Parameter
VDRM	Peak Repetitive Forward Off State Voltage
IDRM	Peak Forward Blocking Current
VRRM	Peak Repetitive Reverse Off State Voltage
IRRM	Peak Reverse Blocking Current
V _{TM}	Maximum On State Voltage
ΙΗ	Holding Current





All polarities are referenced to MT1 With in -phase signal (using standard AC lines) quadrants I and III are used





VT , INSTANTANEOUS ON-STATE VOLTAGE(V)

IT , ON-STATE CURRENT(A)





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