



60V N-CHANNEL SELF PROTECTED ENHANCEMENT MODE IntelliFET MOSFET

## Product Summary

- Continous Drain-Source Voltage: 60V
- On-State Resistance: 200mΩ
- Nominal Load Current (V<sub>IN</sub> = 5V): 1.8A
- Clamping Energy: 120mJ

### Description

The ZXMS6005DN8Q is a dual self-protected low side IntelliFET<sup>TM</sup> MOSFET with logic level input. It integrates overtemperature, overcurrent, overvoltage (active clamp) and ESD protected logic level functionality. The ZXMS6005DN8Q is ideal as a general purpose switch driven from 3.3V or 5V microcontrollers in harsh environments where standard MOSFETs are not rugged enough.

# Applications

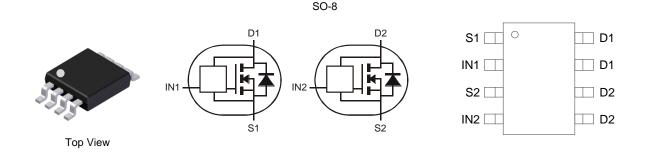
- Lamp Driver
- Motor Driver
- Relay Driver
- Solenoid Driver

### **Features and Benefits**

- Low Input Current
- Logic Level Input (3.3V and 5V)
- Short Circuit Protection with Auto Restart
- Overvoltage Protection (Active Clamp)
- Thermal Shutdown with Auto Restart
- Overcurrent Protection
- Input Protection (ESD)
- High Continuous Current Rating
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

# **Mechanical Data**

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish @3
- Weight: 79.1mg (Approximate)



### Ordering Information (Note 5)

Part Number	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
ZXMS6005DN8Q-13	6005DN8	13	12	2,500 units

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

YY: Year

WW: Week: 01~52:

) | | = Manufacturer's Marking

YYWW = Date Code Marking

52 Represents 52 and 53 Week

6005DN8 = Product Type Marking Code

4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product\_compliance\_definitions.html.

5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

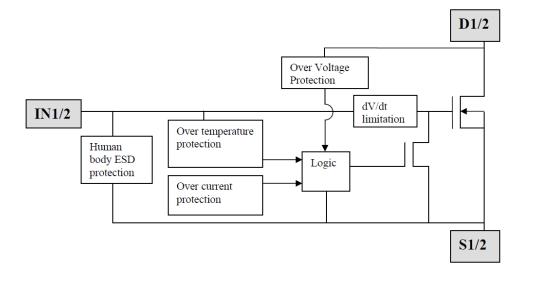
# **Marking Information**

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## **Functional Block Diagram**



# Application Information

- Two Completely Isolated Independent Channels •
- Especially Suited for Loads with a High In-rush Current Such as Lamps and Motors
- All Types of Resistive, Inductive and Capacitive Loads in Switching Applications
- $\mu C$  Compatible Power Switch for 12V and 24V DC Applications
- **Replaces Electromechanical Relays and Discrete Circuits**
- Linear Mode Capability the current-limiting protection circuitry is designed to deactivate at low V<sub>DS</sub> to minimize on-state power dissipation The maximum DC operating current is therefore determined by the thermal capability of the package or board combination, rather than by the protection circuitry. This does not compromise the product's ability to self-protect at low V<sub>DS</sub>.

#### Characteristic Symbol Value Unit Continuous Drain-Source Voltage 60 V VDS Drain-Source Voltage For Short Circuit Protection 16 V V<sub>DS(SC)</sub> V Continuous Input Voltage -0.5 to +6 VIN Continuous Input Current @-0.2V $\leq V_{IN} \leq 6V$ No Limit IIN mΑ Continuous Input Current @VIN< -0.2V or VIN> 6V | I<sub>IN</sub> | ≤ 2 Pulsed Drain Current @VIN = 3.3V 5 $I_{DM}$ А Pulsed Drain Current @VIN = 5V $I_{DM}$ 6 А Continuous Source Current (Body Diode) (Note 6) 2.5 А ls Pulsed Source Current (Body Diode) I<sub>SM</sub> 10 А Unclamped Single Pulse Inductive Energy, 120 Eas mJ $T_J = +25^{\circ}C, I_D = 0.5A, V_{DD} = 24V$ Electrostatic Discharge (Human Body Model) V 4,000 V<sub>HBM</sub> Charged Device Model 1,000 V VCDM

Absolute Maximum Ratings (@TA = +25°C, unless otherwise stated.)

Note: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



# **Recommended Operating Conditions**

The ZXMS6005DN8Q is optimized for use with  $\mu$ C operating from 3.3V and 5V supplies.

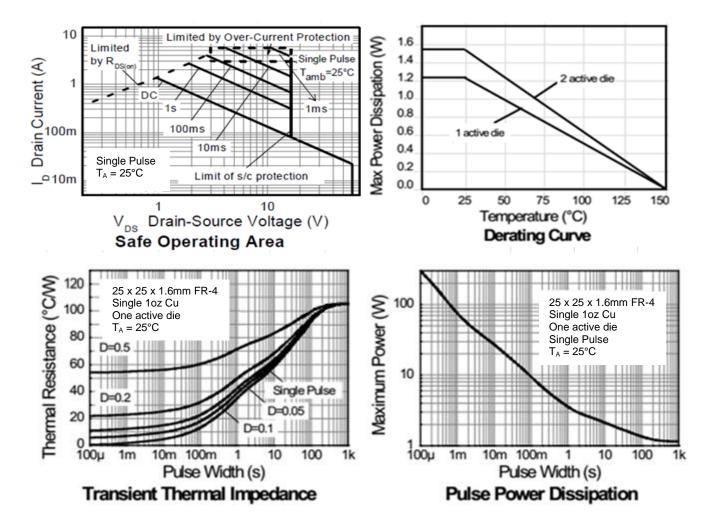
Characteristic	Symbol	Min	Max	Unit
Input Voltage Range	V <sub>IN</sub>	0	5.5	V
Ambient Temperature Range	T <sub>A</sub>	-40	+125	°C
High Level Input Voltage for MOSFET to be On	VIH	3	5.5	V
Low Level Input Voltage for MOSFET to be Off	VIL	0	0.7	V
Peripheral Supply Voltage (Voltage to which load is referred)	VP	0	16	V

## **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation at $T_A = +25^{\circ}C$ (Note 6) Linear Derating Factor	PD	1.21 9.7	W mW/°C
Power Dissipation at $T_A = +25^{\circ}C$ (Note 7) Linear Derating Factor	PD	1.56 12.5	W mW/°C
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>0JA</sub>	103	°C/W
Thermal Resistance, Junction to Ambient (Note 7)	R <sub>0JA</sub>	81	°C/W
Thermal Resistance, Junction to Case (Note 8)	R <sub>θJC</sub>	13.5	°C/W
Operating Temperature Range	TJ	-40 to +150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

Notes:

Device mounted on FR-4 substrate PC board, 2oz copper, with 1-inch square copper plate.
Thermal resistance between junction and the mounting surfaces of drain and source pins.





# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

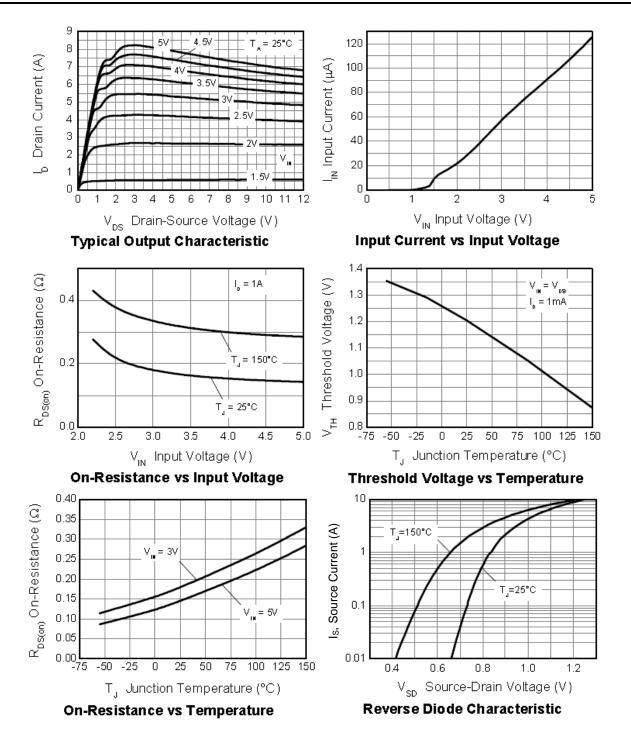
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Static Characteristics			•		•	•
Drain-Source Clamp Voltage	V <sub>DS(AZ)</sub>	60	65	70	V	I <sub>D</sub> = 10mA
Off-State Drain Current		—	—	1	μA	$V_{DS} = 12V, V_{IN} = 0V$
On-State Drain Current	I <sub>DSS</sub>	_	—	2		$V_{DS} = 36V, V_{IN} = 0V$
Input Threshold Voltage	VIN(TH)	0.7	1	1.5	V	$V_{DS} = V_{GS}, I_D = 1mA$
lanut Current		_	60	100	μA	V <sub>IN</sub> = 3V
Input Current	I <sub>IN</sub>		120	200		$V_{IN} = 5V$
Input Current while Over-Temperature Active	—	_	_	300	μA	$V_{IN} = 5V$
Quality During Quantum Quality Destinations	_	_	170	250	mΩ	V <sub>IN</sub> = 3V, I <sub>D</sub> = 1A
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	_	150	200		V <sub>IN</sub> = 5V, I <sub>D</sub> = 1A
	– I <sub>D</sub>	1.4	_	_	A	V <sub>IN</sub> = 3V; T <sub>A</sub> = +25°C
Continuous Drain Current (Note 6)		1.6	_	_		V <sub>IN</sub> = 5V; T <sub>A</sub> = +25°C
		1.7	_	_		V <sub>IN</sub> = 3V; T <sub>A</sub> = +25°C
Continuous Drain Current (Note 7)		1.8	_	_		V <sub>IN</sub> = 5V; T <sub>A</sub> = +25°C
	I <sub>D(LIM)</sub>	2.2	5	_	A	V <sub>IN</sub> = 3V
Current Limit (Note 9)		3.3	7	_		$V_{IN} = 5V$
Dynamic Characteristics	•		•	•	•	•
Turn On Delay Time	t <sub>D(ON)</sub>	_	6	—	μs	
Rise Time	t <sub>R</sub>	—	14	—	μs	V <sub>DD</sub> = 12V, I <sub>D</sub> = 0.5A, V <sub>GS</sub> = 5
Turn Off Delay Time	tD(OFF)	—	34	—	μs	
Fall Time	tF	_	19	_	μs	7
Over-Temperature Protection	•	•		•		•
Thermal Overload Trip Temperature (Note 10)	T <sub>JT</sub>	+150	+175	—	°C	_
Thermal Hysteresis (Note 10)	$\Delta T_{JT}$		+10	_	°C	_

9. The drain current is restricted only when the device is in saturation (see graph "Typical Output Characteristic"). This allows the device to be used in the onstate without interference from the current limit. The device is fully protected at all drain currents, as the low power dissipation generated outside Notes: saturation makes current limit unnecessary. 10. Over-temperature protection is designed to prevent device destruction under fault conditions. Fault conditions are considered as "outside" normal

operating range, so this part is not designed to withstand over-temperature for extended periods.

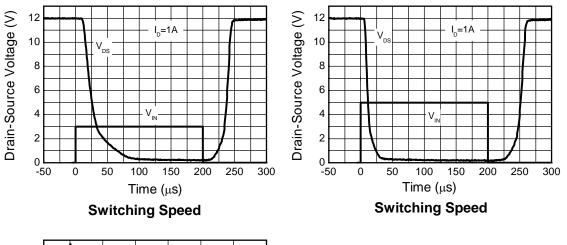


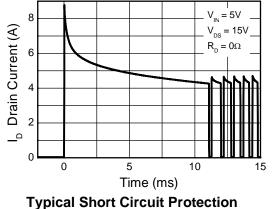






# Typical Characteristics (Cont.)

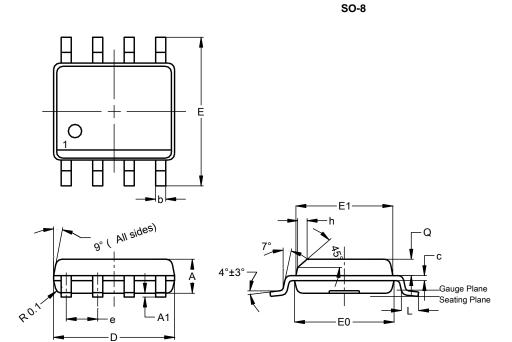






# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

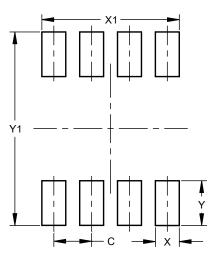


	SO-8					
Dim	Min	Max	Тур			
Α	1.40	1.50	1.45			
A1	0.10	0.20	0.15			
b	0.30	0.50	0.40			
С	0.15	0.25	0.20			
D	4.85	4.95	4.90			
Ε	5.90	6.10	6.00			
E1	3.80	3.90	3.85			
E0	3.85	3.95	3.90			
е			1.27			
h	-	-	0.35			
L	0.62	0.82	0.72			
Q	0.60	0.70	0.65			
All	All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)		
С	1.27		
Х	0.802		
X1	4.612		
Y	1.505		
Y1	6.50		



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