

### SiGe: C NPN RF bipolar transistor









# **Product description**

The BFP740FESD is a wideband NPN RF heterojunction bipolar transistor (HBT) with an integrated ESD protection.



### **Feature list**

- Unique combination of high end RF performance and robustness:
   21 dBm maximum RF input power, 2 kV ESD robustness (HBM) due to integrated protection circuits
- NF<sub>min</sub> = 0.6 dB at 2.4 GHz and 0.8 dB at 5.5 GHz, 3 V, 6 mA
- High gain  $G_{ms}$  = 26 dB at 2.4 GHz and  $G_{ma}$  = 20.5 dB at 5.5 GHz, 3 V, 25 mA
- OIP<sub>3</sub> = 23.5 dBm at 5.5 GHz, 3 V, 25 mA

## **Product validation**

Qualified for industrial applications according to the relevant tests of JEDEC47/20/22.

# **Potential applications**

- Wireless communications: WLAN, WiMax, UWB
- Satellite communication systems: GNSS navigation systems (GPS, GLONASS, BeiDou, Galileo), satellite radio (SDARs, DAB) and C-band LNB
- Multimedia applications such as portable TV, CATV and FM radio
- ISM applications like RKE, AMR and Zigbee, as well as for emerging wireless applications

### **Device information**

#### Table 1 Part information

Product name / Ordering code	Package	Pin co	nfigurat	tion	Marking	Pieces / Reel	
BFP740FESD / BFP740FESDH6327XTSA1	TSFP-4-1	1 = B	2 = E	3 = C	4 = E	T7s	3000

Attention: ESD (Electrostatic discharge) sensitive device, observe handling precautions

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**Absolute maximum ratings** 

#### **Absolute maximum ratings** 1

Table 2 Absolute maximum ratings at  $T_A = 25$  °C (unless otherwise specified)

Parameter	Symbol	Va	lues	Unit	Note or test condition	
		Min.	Min. Max.			
Collector emitter voltage	$V_{\sf CEO}$	_	4.2	V	Open base	
			3.7		T <sub>A</sub> = -55 °C, open base	
Collector base voltage 1)	$V_{CBO}$		4.9		Open emitter	
			4.4		$T_{\rm A}$ = -55 °C, open emitter	
Collector emitter voltage <sup>2)</sup>	$V_{CES}$		4.2		E-B short circuited	
			3.7		T <sub>A</sub> = -55 °C,	
					E-B short circuited	
Base current <sup>3)</sup>	I <sub>B</sub>	-10	5	mA		
Collector current	I <sub>C</sub>	_	45			
RF input power	$P_{RFin}$		21	dBm		
ESD stress pulse <sup>4)</sup>	$V_{ESD}$	-2	2	kV	HBM, all pins, acc. to JESD22-A114	
Total power dissipation <sup>5)</sup>	P <sub>tot</sub>	_	160	mW	<i>T</i> <sub>S</sub> ≤ 100 °C	
Junction temperature	TJ		150	°C		
Storage temperature	$T_{\mathrm{Stg}}$	-55				

Attention: Stresses above the max. values listed here may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Exceeding only one of these values may cause irreversible damage to the integrated circuit.

<sup>1</sup> Low  $V_{CBO}$  due to design.

<sup>2</sup>  $V_{\text{CES}}$  is similar to  $V_{\text{CEO}}$  due to design.

<sup>3</sup> Sustainable reverse bias current is high due to integrated protection circuits.

ESD robustness is high due to design.

 $T_S$  is the soldering point temperature.  $T_S$  is measured on the emitter lead at the soldering point of the PCB.



**Thermal characteristics** 

# 2 Thermal characteristics

Table 3 Thermal resistance

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Max.		
Junction - soldering point	R <sub>thJS</sub>	_	315	_	K/W	

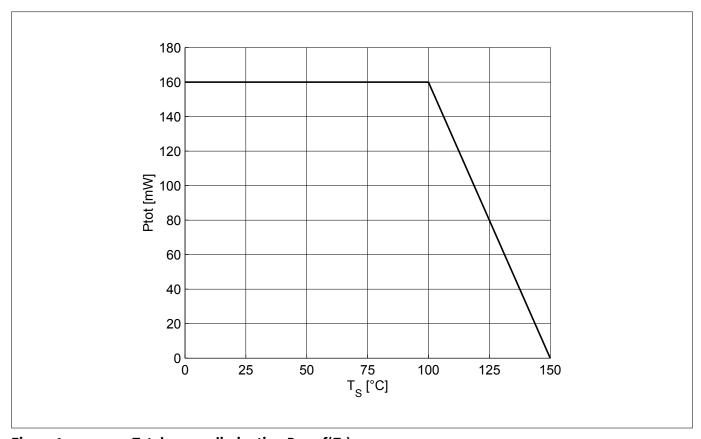


Figure 1 Total power dissipation  $P_{\text{tot}} = f(T_S)$ 

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## **Electrical characteristics**

# **3** Electrical characteristics

#### 3.1 DC characteristics

Table 4 DC characteristics at  $T_A = 25$  °C

Parameter	Symbol	l Values			Unit	Note or test condition	
		Min.	Тур.	Max.			
Collector emitter breakdown voltage	V <sub>(BR)CEO</sub>	4.2	4.7	-	V	$I_C = 1 \text{ mA}, I_B = 0,$ open base	
Collector emitter leakage current	I <sub>CES</sub>	_	_	400 1)	nA	$V_{CE} = 2 \text{ V}, V_{BE} = 0,$ E-B short circuited	
Collector base leakage current	I <sub>CBO</sub>			400 <sup>1)</sup>		$V_{\text{CB}} = 2 \text{ V}, I_{\text{E}} = 0,$ open emitter	
Emitter base leakage current	I <sub>EBO</sub>			10 <sup>1)</sup>	μΑ	$V_{\rm EB}$ = 0.5 V, $I_{\rm C}$ = 0, open collector	
DC current gain	h <sub>FE</sub>	160	250	400		$V_{CE} = 3 \text{ V}, I_{C} = 25 \text{ mA},$ pulse measured	

#### 3.2 General AC characteristics

Table 5 General AC characteristics at  $T_A = 25 \,^{\circ}\text{C}$ 

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Max.		
Transition frequency	$f_{T}$	_	47	_	GHz	$V_{CE} = 3 \text{ V}, I_{C} = 25 \text{ mA},$ f = 1  GHz
Collector base capacitance	C <sub>CB</sub>		0.08		pF	$V_{CB} = 3 \text{ V}, V_{BE} = 0,$ f = 1  MHz, emitter grounded
Collector emitter capacitance	C <sub>CE</sub>		0.4			$V_{CE} = 3 \text{ V}, V_{BE} = 0,$ f = 1  MHz, base grounded
Emitter base capacitance	C <sub>EB</sub>		0.5			$V_{\text{EB}} = 0.4 \text{ V}, V_{\text{CB}} = 0,$ f = 1  MHz, collector grounded

<sup>&</sup>lt;sup>1</sup> Maximum values not limited by the device but by the short cycle time of the 100% test.

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#### **Electrical characteristics**

# **3.3** Frequency dependent AC characteristics

Measurement setup is a test fixture with Bias-T's in a 50  $\Omega$  system,  $T_{\rm A}$  = 25 °C.

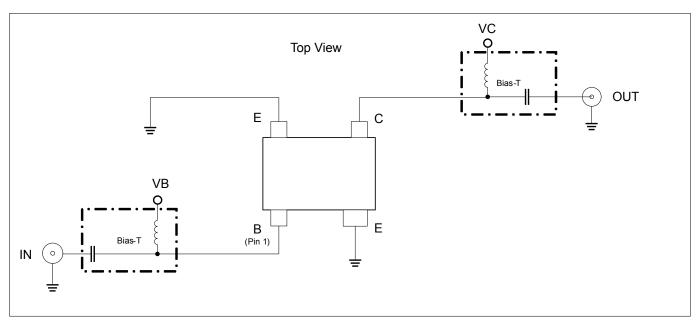


Figure 2 Testing circuit

Table 6 AC characteristics,  $V_{CE} = 3 \text{ V}, f = 150 \text{ MHz}$ 

Parameter	Symbol	Symbol Values		Unit	Note or test condition	
		Min.	Тур.	Max.		
Power gain		_		_	dB	
Maximum power gain	G <sub>ms</sub>		39			$I_{\rm C} = 25  {\rm mA}$
• Transducer gain	$ S_{21} ^2$		34			
Noise figure						
<ul> <li>Minimum noise figure</li> </ul>	NF <sub>min</sub>		0.5			$I_{\rm C} = 6  \rm mA$
Associated gain	G <sub>ass</sub>		31			
Linearity					dBm	
3rd order intercept point at output	OIP <sub>3</sub>		23.5			$Z_{\rm S} = Z_{\rm L} = 50 \Omega, I_{\rm C} = 25 \rm mA$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		10			

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# **Electrical characteristics**

Table 7 AC characteristics,  $V_{CE} = 3 \text{ V}, f = 450 \text{ MHz}$ 

Parameter	Symbol		Values	i	Unit	Note or test condition
		Min.	Тур.	Max.		
Power gain		_		_	dB	
<ul> <li>Maximum power gain</li> </ul>	G <sub>ms</sub>		34			$I_{\rm C} = 25  {\rm mA}$
<ul> <li>Transducer gain</li> </ul>	$ S_{21} ^2$		32.5			
Noise figure						
<ul> <li>Minimum noise figure</li> </ul>	NF <sub>min</sub>		0.5			$I_{\rm C} = 6  \text{mA}$
<ul> <li>Associated gain</li> </ul>	G <sub>ass</sub>		29			
Linearity					dBm	
<ul> <li>3rd order intercept point at output</li> </ul>	OIP <sub>3</sub>		23.5			$Z_{\rm S} = Z_{\rm L} = 50 \Omega, I_{\rm C} = 25 {\rm m/s}$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		10			

# Table 8 AC characteristics, $V_{CE} = 3 \text{ V}, f = 900 \text{ MHz}$

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Max.		
Power gain		_		_	dB	
Maximum power gain	G <sub>ms</sub>		30.5			$I_{\rm C} = 25  {\rm mA}$
• Transducer gain	$ S_{21} ^2$		29.5			
Noise figure						
<ul> <li>Minimum noise figure</li> </ul>	NF <sub>min</sub>		0.55			$I_{\rm C} = 6  \text{mA}$
<ul> <li>Associated gain</li> </ul>	G <sub>ass</sub>		26.5			
Linearity					dBm	
3rd order intercept point at output	OIP <sub>3</sub>		24			$Z_{\rm S} = Z_{\rm L} = 50 \Omega, I_{\rm C} = 25 \rm mA$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		10			

# Table 9 AC characteristics, $V_{CE} = 3 \text{ V}, f = 1.5 \text{ GHz}$

Parameter	Symbol		Values		Unit	Note or test condition
		Min.	Тур.	Max.		
Power gain		_		-	dB	
Maximum power gain	G <sub>ms</sub>		28			$I_{\rm C} = 25  {\rm mA}$
• Transducer gain	$ S_{21} ^2$		26			
Noise figure						
<ul> <li>Minimum noise figure</li> </ul>	NF <sub>min</sub>		0.55			$I_{\rm C} = 6  \text{mA}$
Associated gain	G <sub>ass</sub>		24			
Linearity					dBm	
3rd order intercept point at output	OIP <sub>3</sub>		24			$Z_{\rm S} = Z_{\rm L} = 50 \Omega, I_{\rm C} = 25 \rm mA$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		10			

# SiGe:C NPN RF bipolar transistor



# **Electrical characteristics**

Table 10 AC characteristics,  $V_{CE} = 3 \text{ V}$ , f = 1.9 GHz

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Мах.		
Power gain		_		_	dB	
<ul> <li>Maximum power gain</li> </ul>	G <sub>ms</sub>		27			$I_{\rm C} = 25  {\rm mA}$
<ul> <li>Transducer gain</li> </ul>	$ S_{21} ^2$		24.5			
Noise figure						
<ul> <li>Minimum noise figure</li> </ul>	NF <sub>min</sub>		0.55			$I_{\rm C}$ = 6 mA
<ul> <li>Associated gain</li> </ul>	G <sub>ass</sub>		22			
Linearity					dBm	
<ul> <li>3rd order intercept point at output</li> </ul>	OIP <sub>3</sub>		24.5			$Z_{\rm S} = Z_{\rm L} = 50 \Omega, I_{\rm C} = 25 {\rm mA}$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		10			

# Table 11 AC characteristics, $V_{CE} = 3 \text{ V}$ , f = 2.4 GHz

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Max.		
Power gain		_		-	dB	
Maximum power gain	G <sub>ms</sub>		26			$I_{\rm C} = 25  {\rm mA}$
• Transducer gain	$ S_{21} ^2$		22.5			
Noise figure						
Minimum noise figure	NF <sub>min</sub>		0.6			$I_{\rm C}$ = 6 mA
<ul> <li>Associated gain</li> </ul>	G <sub>ass</sub>		20.5			
Linearity					dBm	
3rd order intercept point at output	OIP <sub>3</sub>		24.5			$Z_{\rm S} = Z_{\rm L} = 50 \Omega, I_{\rm C} = 25 \rm mA$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		10			

# Table 12 AC characteristics, $V_{CE} = 3 \text{ V}, f = 3.5 \text{ GHz}$

Parameter	Symbol Values				Unit	Note or test condition
		Min.	Тур.	Max.		
Power gain		_		_	dB	
<ul> <li>Maximum power gain</li> </ul>	G <sub>ms</sub>		24			$I_{\rm C} = 25  {\rm mA}$
<ul> <li>Transducer gain</li> </ul>	$ S_{21} ^2$		19.5			
Noise figure						
<ul> <li>Minimum noise figure</li> </ul>	NF <sub>min</sub>		0.65			$I_{\rm C}$ = 6 mA
<ul> <li>Associated gain</li> </ul>	G <sub>ass</sub>		17			
Linearity					dBm	
<ul> <li>3rd order intercept point at output</li> </ul>	OIP <sub>3</sub>		24.5			$Z_{\rm S} = Z_{\rm L} = 50 \Omega, I_{\rm C} = 25 {\rm mA}$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		10			

#### SiGe:C NPN RF bipolar transistor



#### **Electrical characteristics**

Table 13 AC characteristics,  $V_{CE} = 3 \text{ V}$ , f = 5.5 GHz

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Max.		
Power gain		_		_	dB	
<ul> <li>Maximum power gain</li> </ul>	G <sub>ma</sub>		20.5			$I_{\rm C} = 25  {\rm mA}$
<ul> <li>Transducer gain</li> </ul>	$ S_{21} ^2$		15.5			
Noise figure						
<ul> <li>Minimum noise figure</li> </ul>	NF <sub>min</sub>		0.8			$I_{\rm C}$ = 6 mA
<ul> <li>Associated gain</li> </ul>	G <sub>ass</sub>		14.5			
Linearity					dBm	
<ul> <li>3rd order intercept point at output</li> </ul>	OIP <sub>3</sub>		23.5			$Z_{\rm S} = Z_{\rm L} = 50 \Omega, I_{\rm C} = 25 {\rm m}$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		10			

Table 14 AC characteristics,  $V_{CE} = 3 \text{ V}, f = 10 \text{ GHz}$ 

Parameter	Symbol	Values			Unit	Note or test condition
		Min.	Тур.	Мах.		
Power gain		-		_	dB	
<ul> <li>Maximum power gain</li> </ul>	G <sub>ma</sub>		14			$I_{\rm C} = 25  {\rm mA}$
<ul> <li>Transducer gain</li> </ul>	$ S_{21} ^2$		9			
Noise figure						
<ul> <li>Minimum noise figure</li> </ul>	NF <sub>min</sub>		1.45			$I_{\rm C} = 6  \text{mA}$
<ul> <li>Associated gain</li> </ul>	G <sub>ass</sub>		9			
Linearity					dBm	
<ul> <li>3rd order intercept point at output</li> </ul>	OIP <sub>3</sub>		21			$Z_{\rm S} = Z_{\rm L} = 50 \ \Omega, I_{\rm C} = 25 \ {\rm mA}$
• 1 dB gain compression point at output	OP <sub>1dB</sub>		8			

Note:

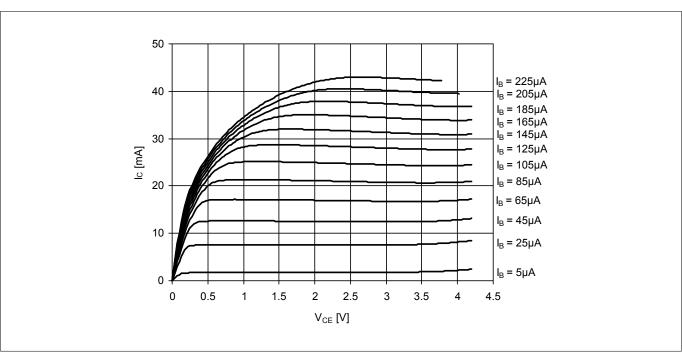
 $G_{\rm ms}$  =  $IS_{21}/S_{12}I$  for k < 1;  $G_{\rm ma}$  =  $IS_{21}/S_{12}I$  (k-( $k^2$ -1) $^{1/2}$ ) for k > 1. In order to get the NF<sub>min</sub> values stated in this chapter, the test fixture losses have been subtracted from all measured results. OIP<sub>3</sub> value depends on termination of all intermodulation frequency components. Termination used for this measurement is 50  $\Omega$  from 0.2 MHz to 12 GHz.

## **BFP740FESD** SiGe:C NPN RF bipolar transistor



#### **Electrical characteristics**

#### **Characteristic DC diagrams** 3.4



Collector current vs. collector emitter voltage  $I_C = f(V_{CE})$ ,  $I_B = parameter$ Figure 3

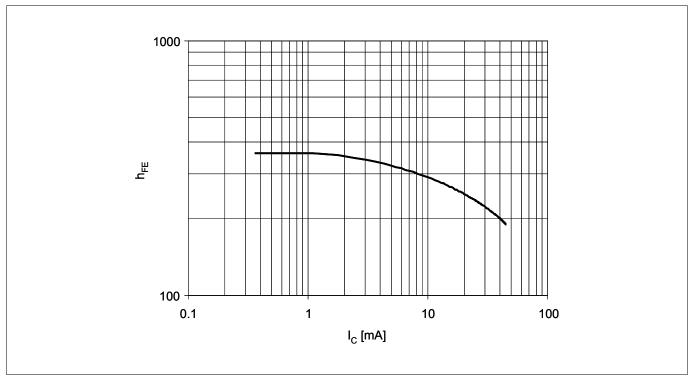


Figure 4 DC current gain  $h_{FE} = f(I_C)$ ,  $V_{CE} = 3 \text{ V}$ 



v2.0

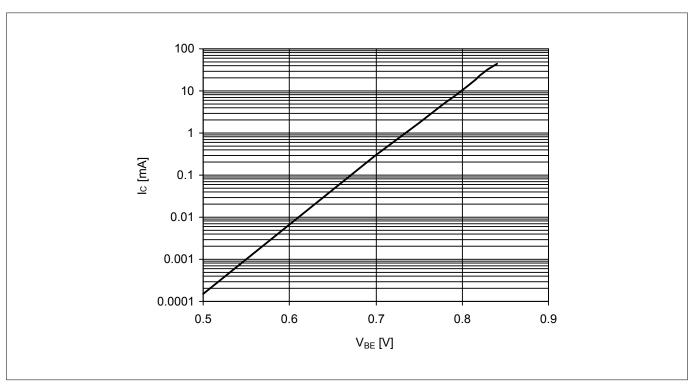


Figure 5 Collector current vs. base emitter voltage  $I_C = f(V_{BE})$ ,  $V_{CE} = 2 \text{ V}$ 

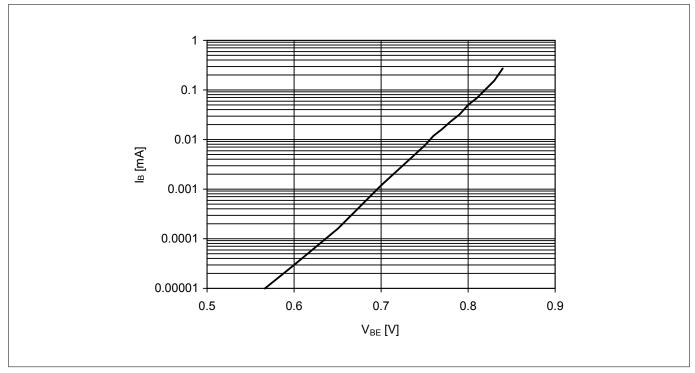


Figure 6 Base current vs. base emitter forward voltage  $I_B = f(V_{BE})$ ,  $V_{CE} = 2 \text{ V}$ 



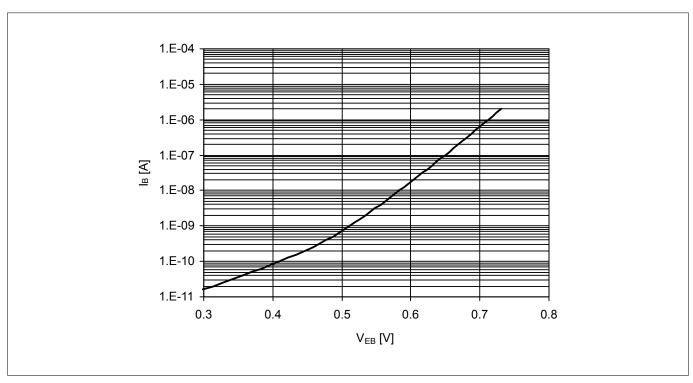


Figure 7 Base current vs. base emitter reverse voltage  $I_B = f(V_{EB})$ ,  $V_{CE} = 2 \text{ V}$ 



# 3.5 Characteristic AC diagrams

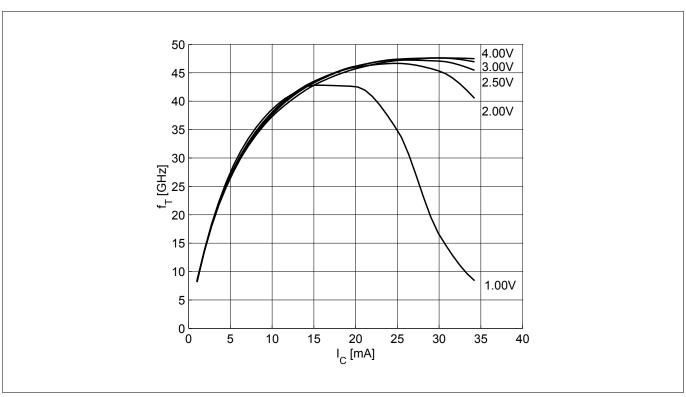


Figure 8 Transition frequency  $f_T = f(I_C)$ , f = 1 GHz,  $V_{CE} =$  parameter

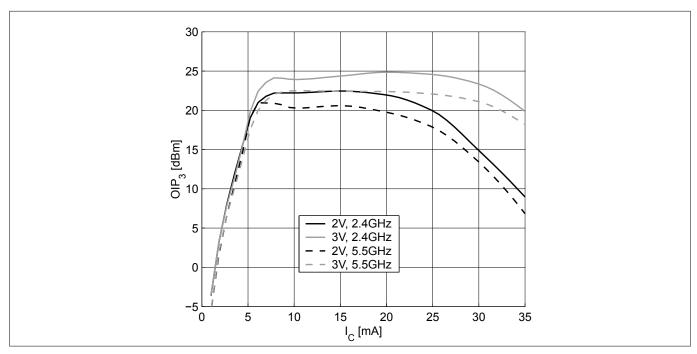


Figure 9 3rd order intercept point  $OIP_3 = f(I_C)$ ,  $Z_S = Z_L = 50 \Omega$ ,  $V_{CE}$ , f = parameters



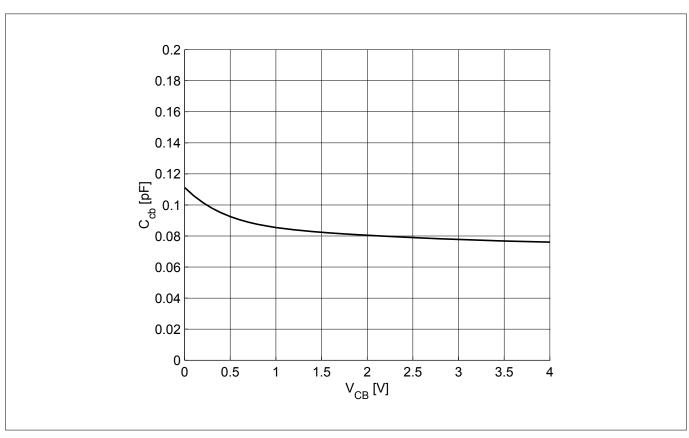


Figure 10 Collector base capacitance  $C_{CB} = f(V_{CB}), f = 1 \text{ MHz}$ 

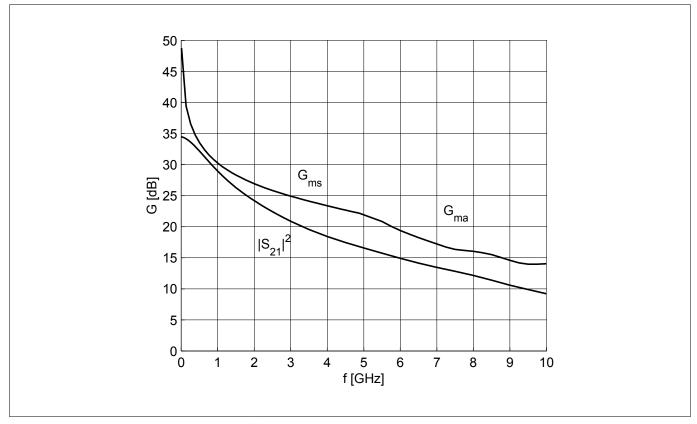


Figure 11 Gain  $G_{ma}$ ,  $G_{ms}$ ,  $IS_{21}I^2 = f(f)$ ,  $V_{CE} = 3 \text{ V}$ ,  $I_C = 25 \text{ mA}$ 



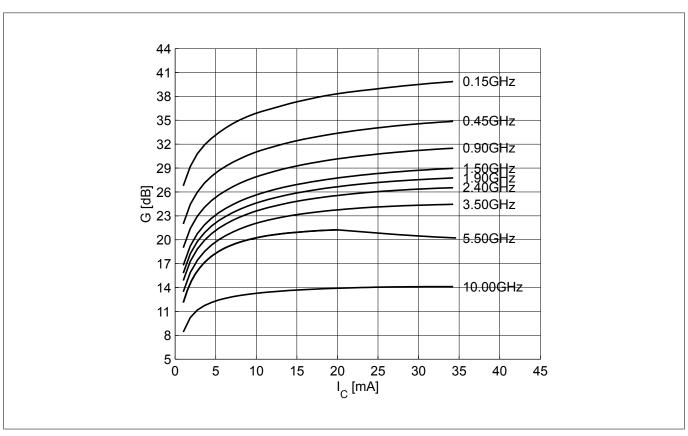


Figure 12 Maximum power gain  $G_{\text{max}} = f(I_{\text{C}})$ ,  $V_{\text{CE}} = 3 \text{ V}$ , f = parameter in GHz

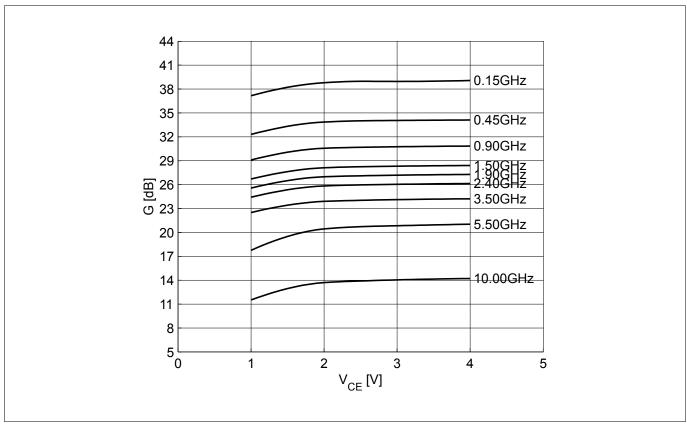


Figure 13 Maximum power gain  $G_{\text{max}} = f(V_{\text{CE}})$ ,  $I_{\text{C}} = 25 \text{ mA}$ , f = parameter in GHz

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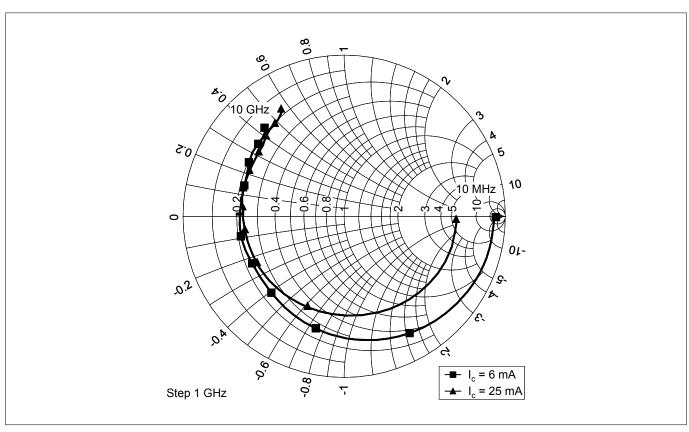


Figure 14 Input reflection coefficient  $S_{11} = f(f)$ ,  $V_{CE} = 3 \text{ V}$ ,  $I_{C} = 6 / 25 \text{ mA}$ 

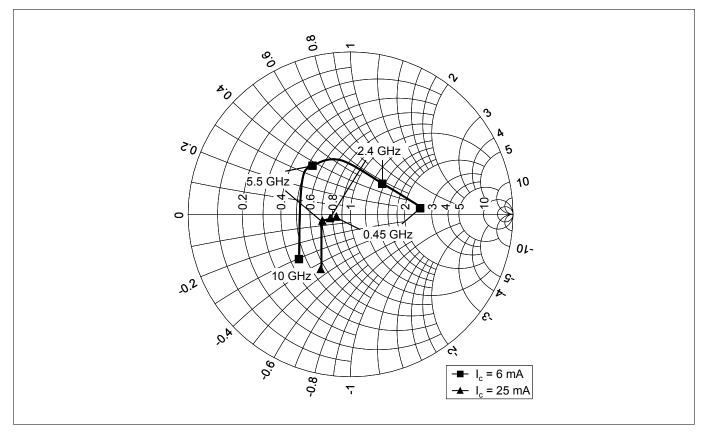


Figure 15 Source impedance for minimum noise figure  $Z_{S,opt} = f(f)$ ,  $V_{CE} = 3 \text{ V}$ ,  $I_C = 6 / 25 \text{ mA}$ 



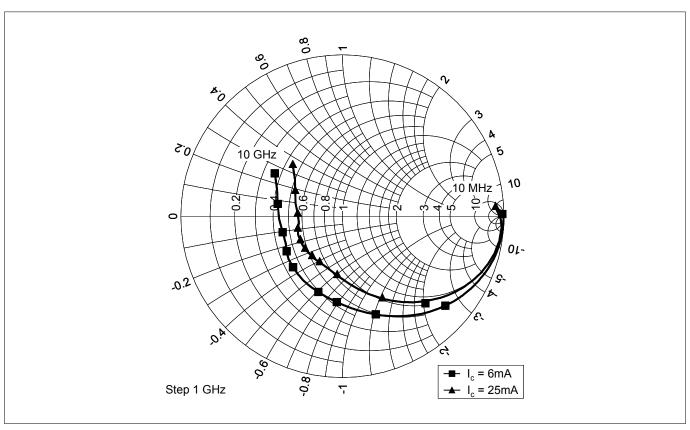


Figure 16 Output reflection coefficient  $S_{22} = f(f)$ ,  $V_{CE} = 3 \text{ V}$ ,  $I_C = 6 / 25 \text{ mA}$ 

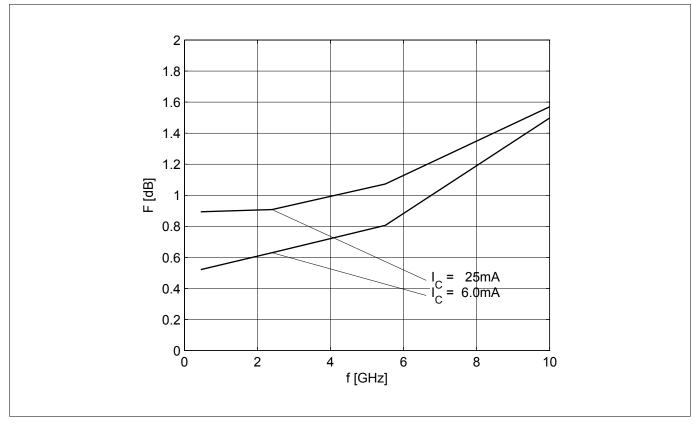


Figure 17 Noise figure  $NF_{min} = f(f)$ ,  $V_{CE} = 3 \text{ V}$ ,  $Z_S = Z_{S,opt}$ ,  $I_C = 6 / 25 \text{ mA}$ 

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#### **Electrical characteristics**

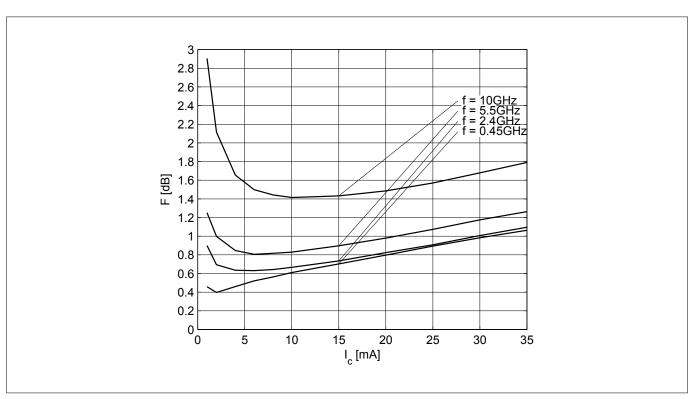


Figure 18 Noise figure  $NF_{min} = f(I_C)$ ,  $V_{CE} = 3 \text{ V}$ ,  $Z_S = Z_{S,opt}$ , f = parameter in GHz

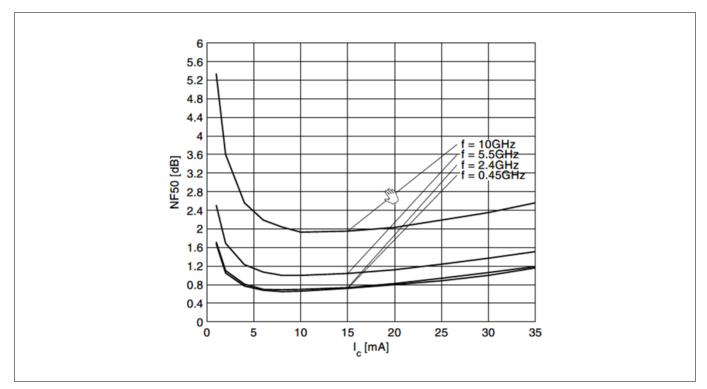


Figure 19 Noise figure  $NF_{50} = f(I_C)$ ,  $V_{CE} = 3 \text{ V}$ ,  $Z_S = 50 \Omega$ , f = parameter in GHz

Note: The curves shown in this chapter have been generated using typical devices but shall not be considered as a guarantee that all devices have identical characteristic curves.  $T_A = 25 \,^{\circ}\text{C}$ .



Package information TSFP-4-1

# 4 Package information TSFP-4-1

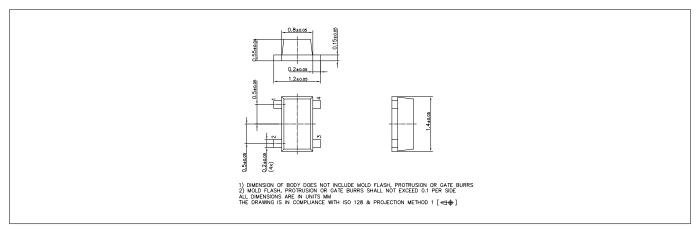


Figure 20 Package outline

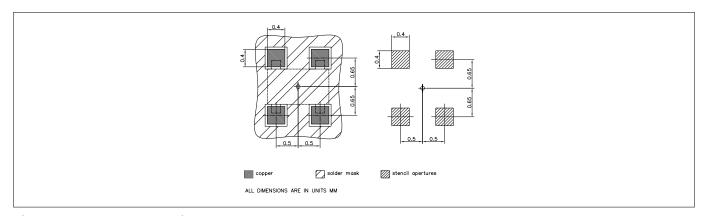


Figure 21 Foot print

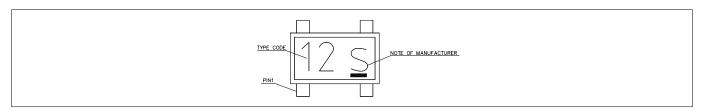


Figure 22 Marking layout example

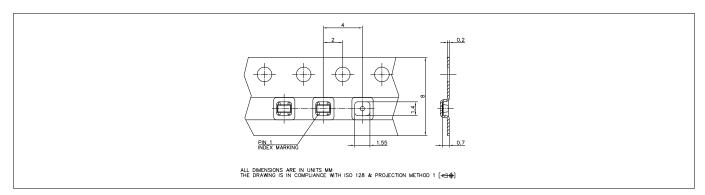


Figure 23 Tape dimensions

## SiGe:C NPN RF bipolar transistor



**Revision history** 

# **Revision history**

Document version	Date of release	Description of changes
2.0	2018-09-26	New datasheet layout.

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