74HC151; 74HCT151

8-input multiplexer Rev. 8 — 18 March 2021

1. General description

The 74HC151; 74HCT151 are 8-bit multiplexer with eight binary inputs (I0 to I7), three select inputs (S0 to S2) and an enable input (\overline{E}). One of the eight binary inputs is selected by the select inputs and routed to the complementary outputs (Y and \overline{Y}). A HIGH on \overline{E} forces the output Y LOW and output \overline{Y} HIGH. Inputs also include clamp diodes that enable the use of current limiting resistors to interface inputs to voltages in excess of V_{CC}.

2. Features and benefits

- Specified in compliance with JEDEC standard no. 7A
- Input levels:
 - For 74HC151: CMOS level
 - For 74HCT151: TTL level
- Low-power dissipation
- Non-inverting data path
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

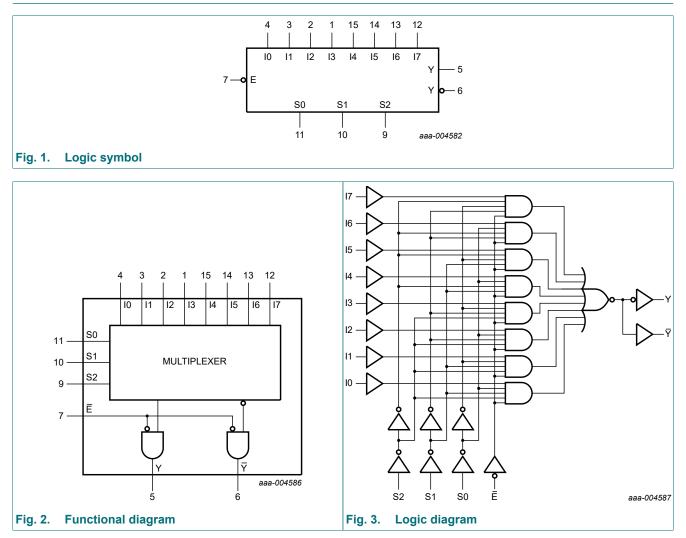
3. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74HC151D	-40 °C to +125 °C	SO16	plastic small outline package; 16 leads;	SOT109-1
74HCT151D			body width 3.9 mm	
74HC151DB	-40 °C to +125 °C	SSOP16	plastic shrink small outline package; 16 leads; body width 5.3 mm	SOT338-1
74HC151PW	-40 °C to +125 °C	TSSOP16	plastic thin shrink small outline package;	SOT403-1
74HCT151PW			16 leads; body width 4.4 mm	

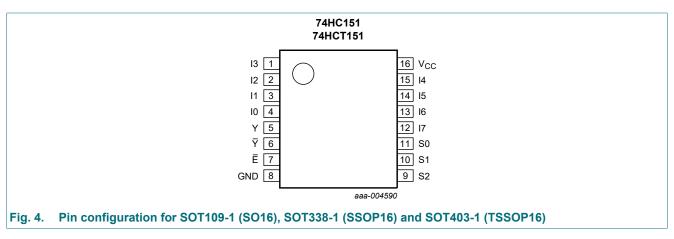


4. Functional diagram



5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description		
Symbol	Pin	Description
10 to 17	4, 3, 2, 1, 15, 14, 13, 12	data inputs
Y	5	multiplexer output
Y	6	complementary multiplexer output
Ē	7	enable input (active LOW)
GND	8	ground (0 V)
S0, S1, S2	11, 10, 9	common data select inputs
V _{CC}	16	supply voltage

6. Functional description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care.

Input												Outp	ut
E	S2	S1	S0	10	I1	12	13	14	15	16	17	Y	Y
Н	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х	Х	Н	L
L	L	L	L	L	Х	Х	Х	Х	Х	Х	Х	Н	L
L	L	L	L	Н	Х	Х	Х	Х	Х	Х	Х	L	Н
L	L	L	Н	Х	L	Х	Х	Х	Х	Х	Х	Н	L
L	L	L	Н	Х	Н	Х	Х	Х	Х	Х	Х	L	Н
L	L	Н	L	Х	Х	L	Х	Х	Х	Х	Х	Н	L
L	L	Н	L	Х	Х	Н	Х	Х	Х	Х	Х	L	Н
L	L	Н	Н	Х	Х	Х	L	Х	Х	Х	Х	Н	L
L	L	Н	Н	Х	Х	Х	Н	Х	Х	Х	Х	L	Н
L	Н	L	L	Х	Х	Х	Х	L	Х	Х	Х	Н	L
L	Н	L	L	Х	Х	Х	Х	Н	Х	Х	Х	L	Н
L	Н	L	Н	Х	Х	Х	Х	Х	L	Х	Х	Н	L
L	Н	L	Н	Х	Х	Х	Х	Х	Н	Х	Х	L	Н
L	н	Н	L	Х	Х	Х	Х	Х	Х	L	Х	Н	L
L	Н	Н	L	Х	Х	Х	Х	Х	Х	Н	Х	L	Н
L	н	Н	Н	Х	Х	Х	Х	Х	Х	Х	L	Н	L
L	Н	Н	Н	Х	Х	Х	Х	Х	Х	Х	Н	L	Н

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7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7	V
l _{IK}	input clamping current	$V_{I} < -0.5 V \text{ or } V_{I} > V_{CC} + 0.5 V$	-	±20	mA
I _{OK}	output clamping current	$V_{\rm O}$ < -0.5 V or $V_{\rm O}$ > $V_{\rm CC}$ + 0.5 V	-	±20	mA
lo	output current	$V_{O} = -0.5 V$ to ($V_{CC} + 0.5 V$)	-	±25	mA
I _{CC}	supply current		-	+50	mA
I _{GND}	ground current		-50	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C [1]	-	500	mW

For SOT109-1 (SO16) package: P_{tot} derates linearly with 12.4 mW/K above 110 °C.
 For SOT338-1 (SSOP16) package: P_{tot} derates linearly with 8.5 mW/K above 91 °C.
 For SOT403-1 (TSSOP16) package: P_{tot} derates linearly with 8.5 mW/K above 91 °C.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V)

Symbol	Parameter	Conditions	74HC151 74HCT151			1	Unit		
			Min	Тур	Max	Min	Тур	Max	
V _{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V _{CC}	0	-	V _{CC}	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 2.0 V	-	-	625	-	-	-	ns/V
		V _{CC} = 4.5 V	-	1.67	139	-	1.67	139	ns/V
		V _{CC} = 6.0 V	-	-	83	-	-	-	ns/V

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C to	o +85 °C	-40 °C to	+125 °C	Unit
			Min	Тур	Мах	Min	Max	Min	Max	
74HC15 ⁴	1									
V _{IH}	HIGH-level	V _{CC} = 2.0 V	1.5	1.2	-	1.5	-	1.5	-	V
	input voltage	V _{CC} = 4.5 V	3.15	2.4	-	3.15	-	3.15	-	V
		V _{CC} = 6.0 V	4.2	3.2	-	4.2	-	4.2	-	V
V _{IL}	LOW-level	V _{CC} = 2.0 V	-	0.8	0.5	-	0.5	-	0.5	V
	input voltage	V _{CC} = 4.5 V	-	2.1	1.35	-	1.35	-	1.35	V
		V _{CC} = 6.0 V	-	2.8	1.8	-	1.8	-	1.8	V

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Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	1
V _{OH}	HIGH-level	V _I = V _{IH} or V _{IL}								
	output voltage	I _O = -20 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -20 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -20 μA; V _{CC} = 6.0 V	5.9	6.0	-	5.9	-	5.9	-	V
		I _O = -4.0 mA; V _{CC} = 4.5 V	3.98	4.32	-	3.84	-	3.7	-	V
		I _O = -5.2 mA; V _{CC} = 6.0 V	5.48	5.81	-	5.34	-	5.2	-	V
V _{OL}	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}$								
	output voltage	I _O = 20 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 6.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 4.5 V	-	0.15	0.26	-	0.33	-	0.4	V
		I _O = 5.2 mA; V _{CC} = 6.0 V	-	0.16	0.26	-	0.33	-	0.4	V
l _l	input leakage current	$V_1 = V_{CC}$ or GND; $V_{CC} = 6.0$ V	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0$ V	-	-	8.0	-	80	-	160	μA
CI	input capacitance		-	3.5	-	-	-	-	-	pF
74HCT1	51									
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	1.6	-	2.0	-	2.0	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	1.2	0.8	-	0.8	-	0.8	V
V _{OH}	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = -20 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -4 mA	3.98	4.32	-	3.84	-	3.7	-	V
V _{OL}	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = 20 μA	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA	-	0.15	0.26	-	0.33	-	0.4	V
lı	input leakage current	$V_1 = V_{CC}$ or GND; $V_{CC} = 5.5 V$	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	8.0	-	80	-	160	μA
ΔI _{CC}	additional supply current	$\label{eq:VI} \begin{array}{l} V_{I} = V_{CC} - 2.1 \; V;\\ \text{other inputs at } V_{CC} \; \text{or GND};\\ V_{CC} = 4.5 \; V \; \text{to } 5.5 \; V; \; I_{O} = 0 \; A \end{array}$								
		per input pin; In inputs	-	45	162	-	203	-	221	μA
		per input pin; E input	-	30	108	-	135	-	147	μA
		per input pin; Sn input	-	150	540	-	675	-	735	μA
Cı	input capacitance		-	3.5	-	-	-	-	-	pF

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10. Dynamic characteristics

Table 7. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); C_L = 50 pF unless otherwise specified; for test circuit see Fig. 7.

Symbol	Parameter	Conditions			25 °C		-40 °C t	o +85 °C	-40 °C te	o +125 °C	Unit
				Min	Тур	Мах	Min	Max	Min	Max	1
74HC15 [,]	1								•		
t _{pd}	propagation	In to Y; see Fig. 5	[1]								
	delay	V _{CC} = 2.0 V		-	52	170	-	215	-	255	ns
		V _{CC} = 4.5 V		-	19	34	-	43	-	51	ns
		V _{CC} = 5 V; C _L = 15 pF		-	17	-	-	-	-	-	ns
		V _{CC} = 6.0 V		-	15	29	-	37	-	43	ns
		In to Y; see <u>Fig. 5</u>	[1]								
		V _{CC} = 2.0 V		-	58	185	-	230	-	280	ns
		V _{CC} = 4.5 V		-	21	37	-	46	-	56	ns
		V _{CC} = 5 V; C _L = 15 pF		-	17	-	-	-	-	-	ns
		V _{CC} = 6.0 V		-	17	31	-	39	-	48	ns
		Sn to Y; see <u>Fig. 6</u>	[1]								<u> </u>
		V _{CC} = 2.0 V		-	61	185	-	230	-	280	ns
		V _{CC} = 4.5 V		-	22	37	-	46	-	56	ns
		V _{CC} = 5 V; C _L = 15 pF		-	19	-	-	-	-	-	ns
		V _{CC} = 6.0 V		-	18	31	-	39	-	48	ns
		Sn to \overline{Y} ; see Fig. 6	[1]								
		V _{CC} = 2.0 V		-	61	205	-	255	-	310	ns
		V _{CC} = 4.5 V		-	22	41	-	51	-	62	ns
		V _{CC} = 5 V; C _L = 15 pF		-	19	-	-	-	-	-	ns
		V _{CC} = 6.0 V		-	18	35	-	43	-	53	ns
		Ē to Y; see <u>Fig. 6</u>									
		V _{CC} = 2.0 V		-	41	125	-	155	-	190	ns
		V _{CC} = 4.5 V		-	15	25	-	31	-	38	ns
		V _{CC} = 5 V; C _L = 15 pF		-	12	-	-	-	-	-	ns
		V _{CC} = 6.0 V		-	12	21	-	26	-	32	ns
		Ē to Ϋ; see <u>Fig. 6</u>									
		V _{CC} = 2.0 V		-	47	145	-	180	-	220	ns
		V _{CC} = 4.5 V		-	17	29	-	36	-	44	ns
		V _{CC} = 5 V; C _L = 15 pF		-	14	-	-	_	-	-	ns
		V _{CC} = 6.0 V		-	14	25	-	31	-	38	ns
t _t	transition	Y, ∏ ; see <u>Fig. 5</u>	[2]								
	time	V _{CC} = 2.0 V		-	19	75	-	95	-	110	ns
		V _{CC} = 4.5 V		-	7	15	-	19	-	22	ns
		V _{CC} = 6.0 V		-	6	13	-	16	-	19	ns
C _{PD}	power dissipation capacitance	$C_L = 50 \text{ pF}; \text{ f} = 1 \text{ MHz};$ $V_I = \text{GND to } V_{CC}$	[3]	-	40	-	-	-	-	-	pF

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Symbol	Parameter	Conditions			25 °C		-40 °C t	o +85 °C	-40 °C t	o +125 °C	Unit
				Min	Тур	Max	Min	Мах	Min	Max	
74HCT1	51	1					I				1
t _{pd}	propagation	In to Y; see Fig. 5	[1]								
	delay	V _{CC} = 4.5 V		-	22	38	-	48	-	57	ns
		V _{CC} = 5 V; C _L = 15 pF		-	19	-	-	-	-	-	ns
		In to \overline{Y} ; see <u>Fig. 5</u>	[1]								
		V _{CC} = 4.5 V		-	22	38	-	48	-	57	ns
		V _{CC} = 5 V; C _L = 15 pF		-	19	-	-	-	-	-	ns
		Sn to Y; see Fig. 6	[1]								
		V _{CC} = 4.5 V		-	23	41	-	51	-	62	ns
		V _{CC} = 5 V; C _L = 15 pF		-	20	-	-	-	-	-	ns
		Sn to ႃ∀; see <u>Fig. 6</u>	[1]								
		V _{CC} = 4.5 V		-	25	43	-	54	-	65	ns
		V _{CC} = 5 V; C _L = 15 pF		-	20	-	-	-	-	-	ns
		Ē to Y; see Fig. 6	[1]								
		V _{CC} = 4.5 V		-	16	29	-	36	-	44	ns
		V _{CC} = 5 V; C _L = 15 pF		-	13	-	-	-	-	-	ns
		\overline{E} to \overline{Y} ; see <u>Fig. 6</u>	[1]								
		V _{CC} = 4.5 V		-	21	36	-	45	-	54	ns
		V _{CC} = 5 V; C _L = 15 pF		-	18	-	-	-	-	-	ns
t _t	transition	Y, Y ; see <u>Fig. 5</u>	[2]								
	time	V _{CC} = 4.5 V		-	7	15	-	19	-	22	ns
C _{PD}	power dissipation capacitance	C_L = 50 pF; f = 1 MHz; V _I = GND to V _{CC} - 1.5 V	[3]	-	40	-	-	-	-	-	pF

f_i = input frequency in MHz;

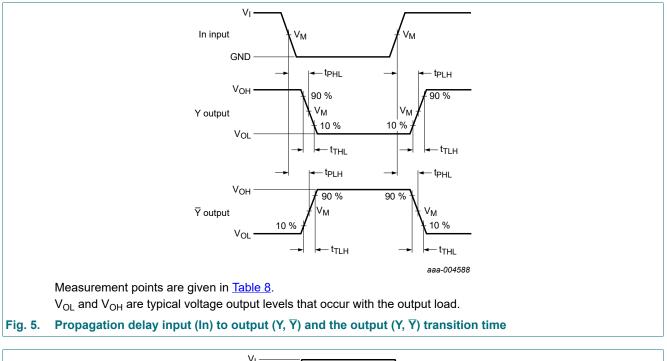
f_o = output frequency in MHz;

C_L = output load capacitance in pF;

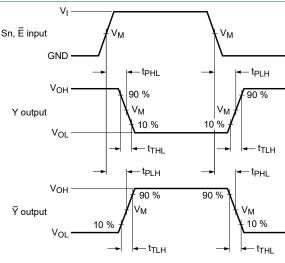
V_{CC} = supply voltage in V;

N = number of inputs switching; $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of outputs.

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10.1. Waveforms and test circuit



Measurement points are given in Table 8.

 V_{OL} and V_{OH} are typical voltage output levels that occur with the output load.

Fig. 6. Propagation delay input (Sn, \overline{E}) to output (Y, \overline{Y}) and output (Y, \overline{Y}) transitions time

Table 8. Measurement points

Туре	Input	Output
	V _M	V _M
74HC151	0.5V _{CC}	0.5V _{CC}
74HCT151	1.3 V	1.3 V

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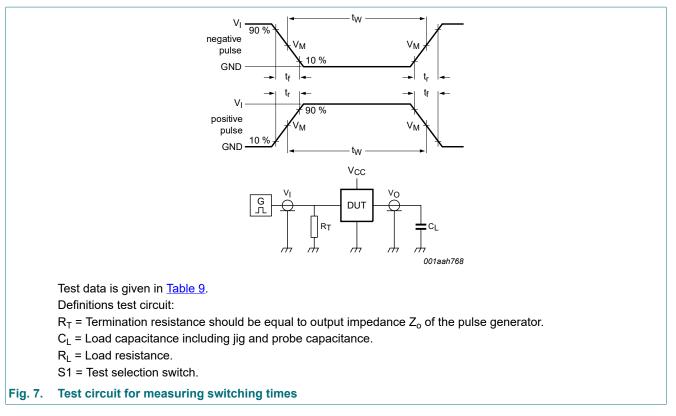


Table 9. Test data

Туре	Input Loa		Load	Test
	VI	t _r , t _f	CL	
74HC151	V _{CC}	6.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}
74HCT151	3.0 V	6.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}

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11. Package outline

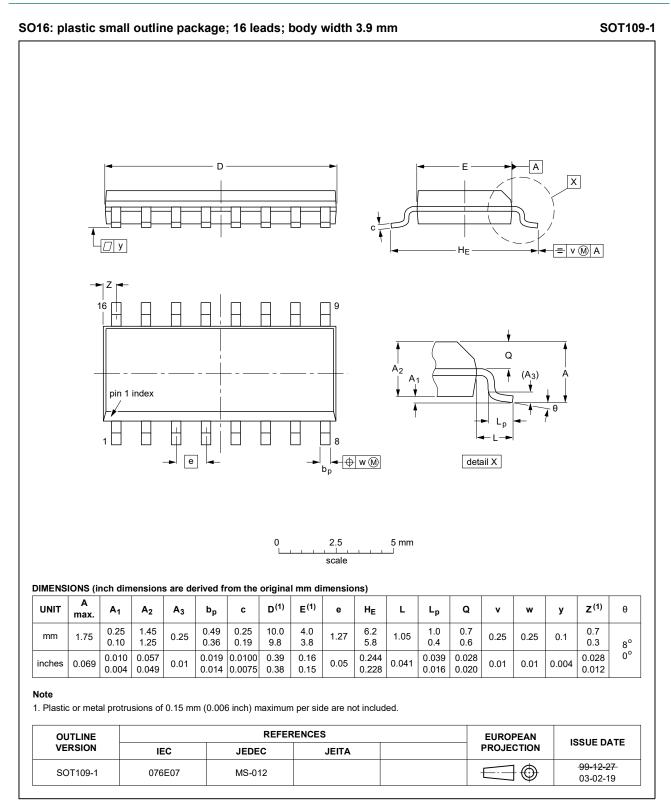


Fig. 8. Package outline SOT109-1 (SO16)

74HC_HCT151

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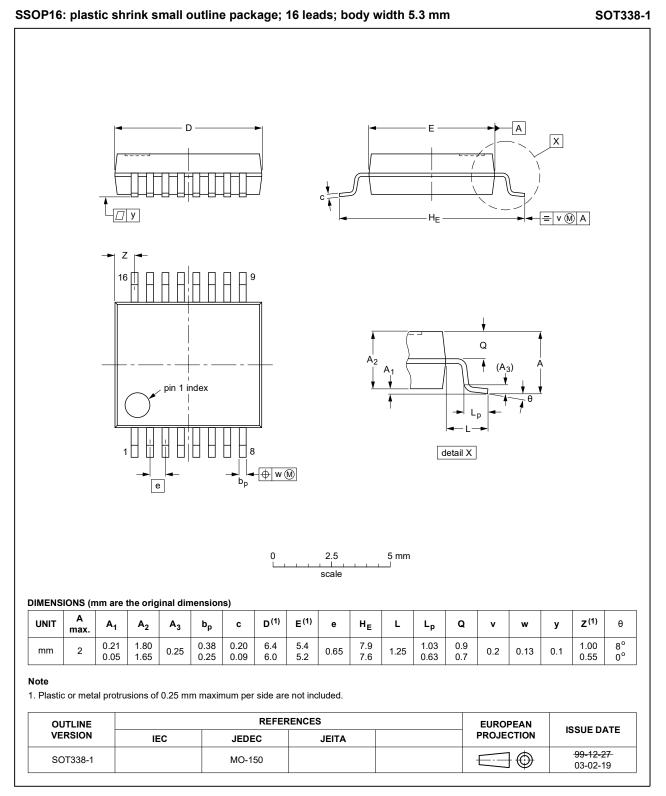


Fig. 9. Package outline SOT338-1 (SSOP16)

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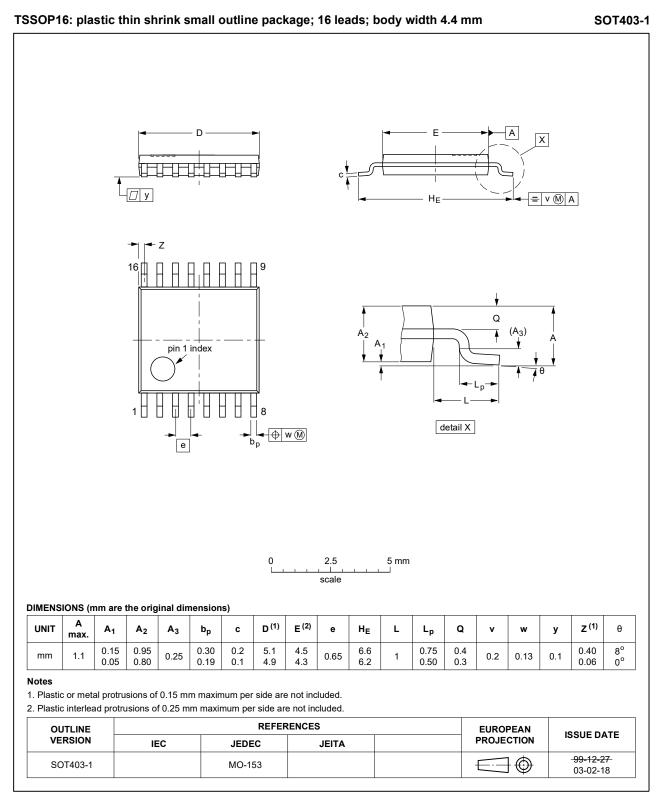


Fig. 10. Package outline SOT403-1 (TSSOP16)

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12. Abbreviations

Acronym	Description
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

13. Revision history

Table 11. Revision history						
Document ID	Release date	Data sheet status	Change notice	Supersedes		
74HC_HCT151 v.8	20210318	Product data sheet	-	74HC_HCT151 v.7		
Modifications:	Type number	Type number 74HC151DB (SOT338-1 / SSOP16) added.				
74HC_HCT151 v.7	20210114	Product data sheet	-	74HC_HCT151 v.6		
Modifications:	guidelines o Legal texts Type number	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type numbers 74HC151DB and 74HCT151DB (SOT338-1 / SSOP16) removed. <u>Section 7</u>: Derating values for P_{tot} total power dissipation have been updated. 				
74HC_HCT151 v.6	20151228	Product data sheet	-	74HC_HCT151 v.5		
Modifications:	Type numbers 74HC151N and 74HCT151N (SOT38-4) removed.					
74HC_HCT151 v.5	20150126	Product data sheet	-	74HC_HCT151 v.4		
Modifications:	• <u>Table 7</u> : Power dissipation capacitance condition for 74HCT151 is corrected.					
74HC_HCT151 v.4	20130211	Product data sheet	-	74HC_HCT151 v.3		
Modifications:	New descriptive title (errata).					
74HC_HCT151 v.3	20120919	Product data sheet	-	74HC_HCT151_CNV v.2		
74HC_HCT151_CNV v.2	19970827	Product specification	-			

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14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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