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Orderable Parts for: MT29C2G24MAABAKAKD-5 IT							SEE ALL NAND	-BASED MCP PARTS
	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
MT29C2G24MAABAKAKD-5 IT	Production	N/A	JW617	N/A	N/A	No		N/A

Detailed Specifications

	Part Status Code	Production	NAND Density	2Gb	DRAM Type	LPDDR	DRAM Density	1Gb
	Bus Width	x16	Secondary Bus Width	x32	RoHS	Yes	Voltage	1.7V-1.9V
Dow	Package	TFBGA .com.	Pin Count	137-ball	Clock Rate	200 MHz	Operating Temp	-40C to +85C

Sim Models & Software

Sim	Model	S
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IBIS		
IBIS (ZIP)		Multichip Packages NAND LPDDR
NAND Flash + LPD	DR MCP (Rev 1.7)	
File Type: ZIP	Updated: 07/31/2014	DOWNLOAD
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HSpice: J41L (ZIF		Multichip Packages NAND LPDDR
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RoHS Certificates

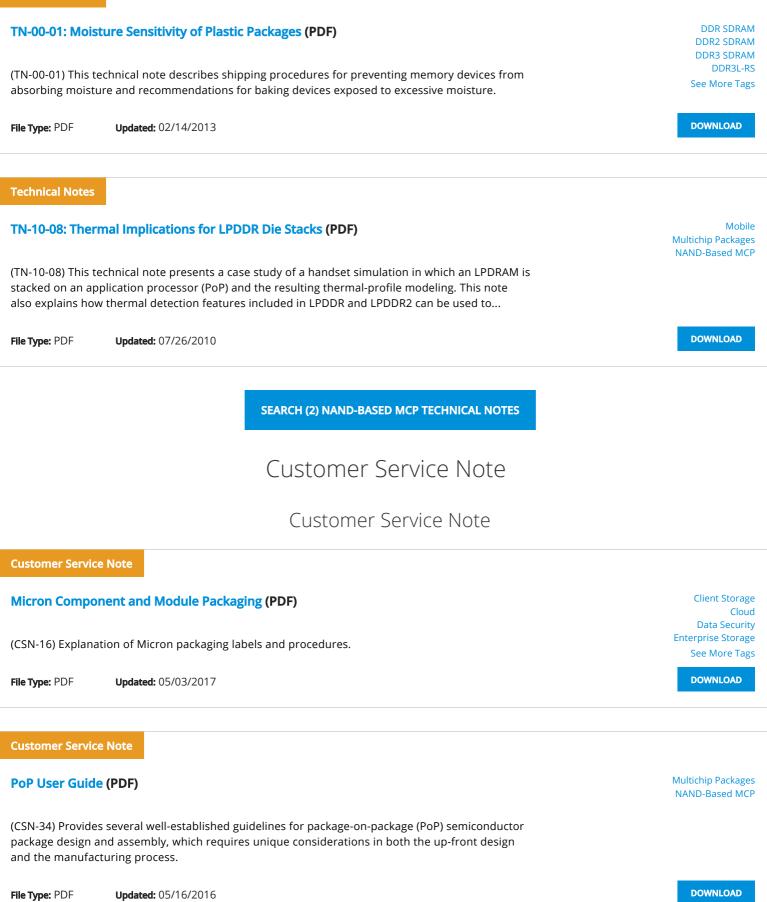
RoHS Certificates		
RoHS Certificat	e of Compliance (PDF)	MT29C2G24MAABAKAKD-5 IT
	fication of how this product meets the requirements of the current DIRECTIVE . Restriction of Hazardous Substances (RoHS) Directive.	
File Type: (PDF)	Updated: 05/2017	DOWNLOAD
RoHS Certificates		
China RoHS Cer	tificate (PDF)	MT29C2G24MAABAKAKD-5 IT
Part-specific certi	fication as required by China's Management Methods for Controlling Pollution by ation Products.	
Electronic Inform		

See All NAND-Based MCP Documentation

Technical Notes

SEARCH (2) NAND-BASED MCP TECHNICAL NOTES

Technical Notes



Parts with the same Data Sheet (2)

MT29C2G24MAABAKAKD-5 I T (Current)		MT29C2G24MAAAAKAKD-5 I T	MT29C2G24MAAAAKAHH-5 AIT	
Part Status Code	Production	Production	Production	
NAND Density	2Gb	2Gb	2Gb	
DRAM Type	LPDDR	LPDDR	LPDDR	
DRAM Density	1Gb	1Gb	1Gb	
Bus Width	x16	x8	x8	
Secondary Bus Width	x32	x32	x32	
RoHS	Yes	Yes	Yes	
Voltage	1.7V-1.9V	1.7V-1.9V	1.7V-1.9V	
Package	TFBGA	TFBGA	TFBGA	
Pin Count	137-ball	137-ball	137-ball	
Clock Rate	200 MHz	200 MHz	200 MHz	
Operating Temp	-40C to +85C	-40C to +85C	-40C to +85C	

MT29C2G24MAABAKAKD-5 I

T (Current)

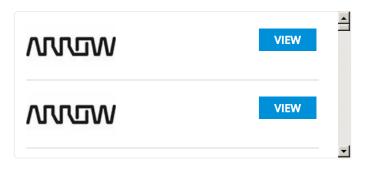
Part Status Code	Production		V	Vhere to	o Buy				
NAND Density	2Gb								
DRAM Type	LPDDR								
DRAM Density	1Gb	Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
Bus Width	x16	Production	N/A	JW617	N/A	N/A	No		N/A
Secondary Bus Width	x32								
RoHS	Yes								

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Voltage	1.7V-1.9V	
Package	TFBGA	
Pin Count	137-ball	
Clock Rate	200 MHz	
Operating Temp	-40C to +85C	

- OR - Check with Distributors

Your Region: Americas



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