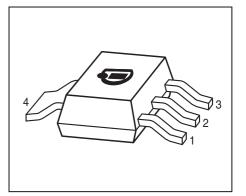


## NPN Silicon Darlington Transistor

- For general AF applications
- High collector current
- High current gain
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101





Туре	Marking	Pin Configuration					Package	
PZTA14	PZTA14	1=B	2=C	3=E	4=C	-	-	SOT223

#### Maximum Ratings

Parameter	Symbol	Value	Unit	
Collector-emitter voltage	V <sub>CES</sub>	30	V	
Collector-base voltage	V <sub>CBO</sub>	30		
Emitter-base voltage	V <sub>EBO</sub>	10		
Collector current	I <sub>C</sub>	300	mA	
Peak collector current, $t_p \le 10 \text{ ms}$	/ <sub>CM</sub>	500		
Base current	I <sub>B</sub>	100		
Peak base current	/ <sub>BM</sub>	200		
Total power dissipation-	P <sub>tot</sub>	1.5	W	
<i>T</i> <sub>S</sub> ≤ 124 °C				
Junction temperature	Ti	150	°C	
Storage temperature	T <sub>stq</sub>	-65 150		

#### Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point <sup>1)</sup>	R <sub>thJS</sub>	≤ 17	K/W

<sup>1</sup>For calculation of  $R_{\text{thJA}}$  please refer to Application Note AN077 (Thermal Resistance Calculation)



Parameter	Symbol		Values		
		min.	typ.	max.	
DC Characteristics					
Collector-base breakdown voltage	V <sub>(BR)CBO</sub>	30	-	-	V
$I_{\rm C}$ = 100 µA, $I_{\rm E}$ = 0					
Collector-emitter breakdown voltage	V <sub>(BR)CES</sub>	30	-	-	
$I_{\rm C}$ = 100 µA, $V_{\rm BE}$ = 0					
Emitter-base breakdown voltage	V <sub>(BR)EBO</sub>	10	-	-	
$I_{\rm E}$ = 10 µA, $I_{\rm C}$ = 0					
Collector-base cutoff current	I <sub>CBO</sub>				μA
$V_{\rm CB} = 30 \text{ V}, I_{\rm E} = 0$		-	-	0.1	
$V_{\rm CB}$ = 30 V, $I_{\rm E}$ = 0 , $T_{\rm A}$ = 150 °C		-	-	10	
Emitter-base cutoff current	I <sub>EBO</sub>	-	-	100	nA
$V_{\rm EB} = 10 \text{ V}, I_{\rm C} = 0$					
DC current gain <sup>1)</sup>	h <sub>FE</sub>				-
<i>I</i> <sub>C</sub> = 10 mA, <i>V</i> <sub>CE</sub> = 5 V		10000	-	-	
$I_{\rm C}$ = 100 mA, $V_{\rm CE}$ = 5 V		20000	-	-	
Collector-emitter saturation voltage1)	V <sub>CEsat</sub>	-	-	1.5	V
<i>I</i> <sub>C</sub> = 100 mA, <i>I</i> <sub>B</sub> = 0.1 mA					
Base emitter saturation voltage <sup>1)</sup>	V <sub>BEsat</sub>	-	-	2	
<i>I</i> <sub>C</sub> = 100 mA, <i>I</i> <sub>B</sub> = 0.1 mA					
AC Characteristics					
Transition frequency	f <sub>T</sub>	125	-	-	MHz
<i>I</i> <sub>C</sub> = 50 mA, <i>V</i> <sub>CE</sub> = 5 V, <i>f</i> = 20 MHz					
Collector-base capacitance	C <sub>cb</sub>	-	3	-	pF
V <sub>CB</sub> = 10 V, <i>f</i> = 1 MHz					

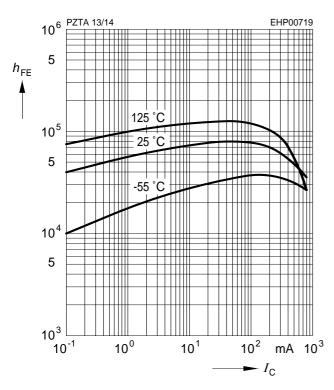
## **Electrical Characteristics** at $T_A = 25^{\circ}$ C, unless otherwise specified

<sup>1</sup>Pulse test: t < 300µs; D < 2%



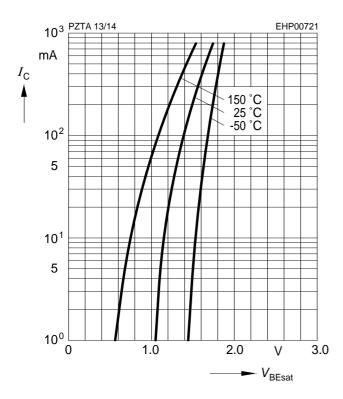
# **DC current gain** $h_{\text{FE}} = f(I_{\text{C}})$

 $V_{CE}$  = 5 V



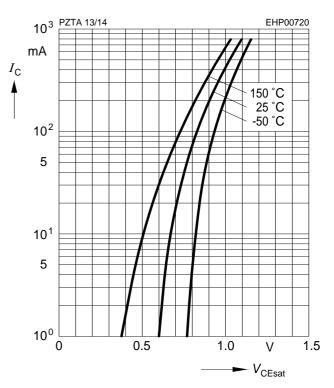
# Base-emitter saturation voltage

 $I_{\rm C} = f(V_{\rm BEsat}), h_{\rm FE} = 1000$ 

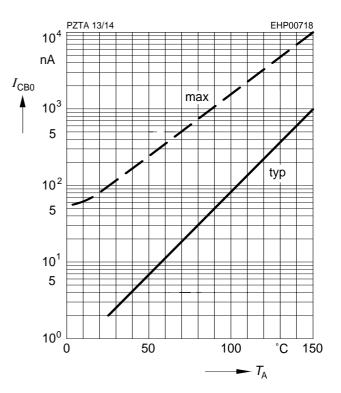


## Collector-emitter saturation voltage

 $I_{\rm C} = f(V_{\rm CEsat}), h_{\rm FE} = 1000$ 



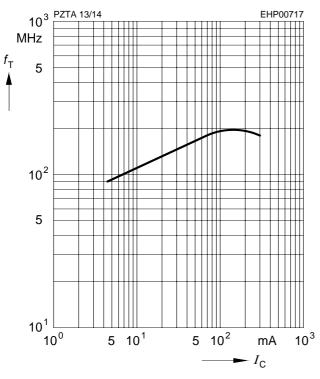
Collector cutoff current  $I_{CBO} = f(T_A)$  $V_{CBO} = 30 \text{ V}$ 



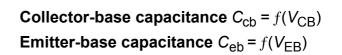


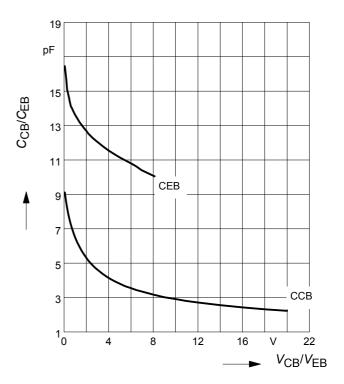
**Transition frequency**  $f_{T} = f(I_{C})$ 

*V*<sub>CE</sub> = 5 V, *f* = 200 MHz



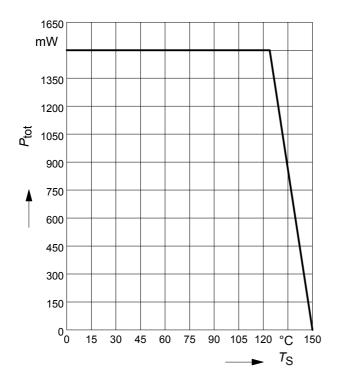
Total power dissipation  $P_{tot} = f(T_S)$ 

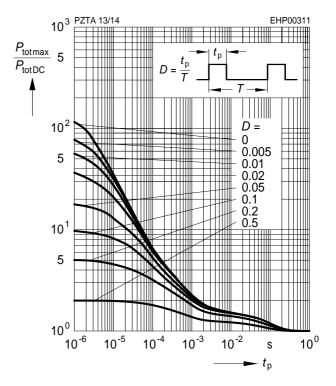




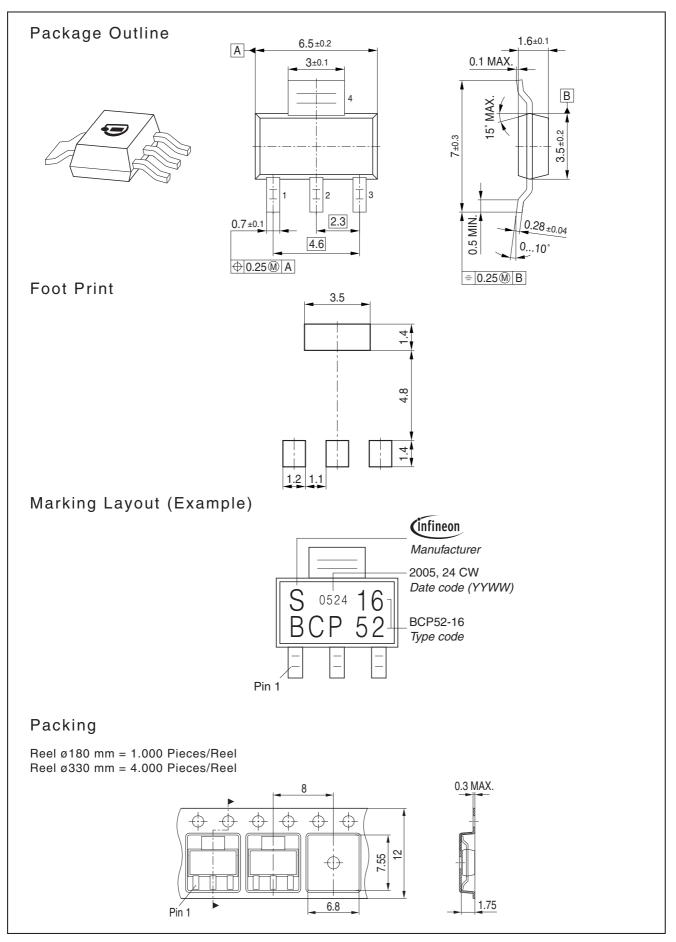
## Permissible Pulse Load

 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$ 











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