## Features

- Near-Zero propagation delay
- $5 \Omega$ switch connects inputs to outputs
- Direct bus connection when switches are ON
- Ultra-low quiescent power ( $0.2 \mu \mathrm{~A}$ typical)
- Ideally suited for notebook applications
- Industrial operating temperature: $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$
- Packaging (Pb-free \& Green available):
- 56-pin 240-mil wide thin plastic TSSOP


## Block Diagram



## Truth Table ${ }^{(1)}$

| $\mathbf{1}_{\mathbf{\mathbf { O E }}}$ | ${ }_{\mathbf{2}} \overline{\mathbf{O E}}$ | ${ }_{\mathbf{1}} \mathbf{A},{ }_{1} \mathbf{B}, \mathbf{I} / \mathbf{O s}$ | ${ }_{2} \mathbf{A},{ }_{2} \mathbf{B}, \mathbf{I} / \mathbf{O s}$ |
| :---: | :---: | :---: | :---: |
| L | L | ${ }_{1} \mathrm{~A}={ }_{1} \mathrm{~B}$ | ${ }_{2} \mathrm{~A}={ }_{2} \mathrm{~B}$ |
| L | H | ${ }_{1} \mathrm{~A}={ }_{1} \mathrm{~B}$ | Z |
| H | L | Z | ${ }_{2} \mathrm{~A}={ }_{2} \mathrm{~B}$ |
| H | H | Z | Z |

Notes:

1. $\mathrm{H}=$ High Voltage Level, $\mathrm{L}=$ Low Voltage Level, Z = High Impedance

## Description

Pericom Semiconductor'sPI5C16211 is a 24-bit bus switch designed with a low On-Resistance allowing inputs to be connected directly to outputs. This device operates as a 24 -bit or a 12 -bit bus switch, which provides high speed bus switching.

## Pin Configuration

| NC 1 | $56{ }_{1} \overline{\mathrm{OE}}$ |
| :---: | :---: |
| ${ }_{1} \mathrm{~A}_{1} \mathrm{C}_{2}$ | $55]{ }_{2} \overline{\mathrm{OE}}$ |
| ${ }_{1} \mathrm{~A}_{2} \mathrm{C}_{3}$ | $54{ }_{1} \mathrm{~B}_{1}$ |
| ${ }_{1} \mathrm{~A}_{3} \mathrm{C}_{4}$ | $53{ }_{1} \mathrm{~B}_{2}$ |
| ${ }_{1} \mathrm{~A}_{4}-5$ | $52{ }_{1} \mathrm{~B}_{3}$ |
| ${ }_{1} A_{5} 6$ | $51]_{1} \mathrm{~B}_{4}$ |
| ${ }_{1} \mathrm{~A}_{6} \mathrm{C}_{7}$ | $50{ }_{1} \mathrm{~B}_{5}$ |
| GND 8 | 49 GND |
| ${ }_{1} \mathrm{~A}_{7} 9$ | $48{ }_{1} \mathrm{~B}_{6}$ |
| ${ }_{1} \mathrm{~A}_{8}-10$ | $47{ }_{1} \mathrm{~B}_{7}$ |
| ${ }_{1} \mathrm{~A}_{9} \mathrm{~A}_{11}$ | $46{ }_{1} \mathrm{~B}_{8}$ |
| ${ }_{1} \mathrm{~A}_{10}$ - 12 | $45{ }_{1} \mathrm{~B}_{9}$ |
| ${ }_{1} \mathrm{~A}_{11}$ ¢ 13 | $44{ }_{1} \mathrm{~B}_{10}$ |
| ${ }_{1} \mathrm{~A}_{12}$ - 14 | $43{ }_{1} \mathrm{~B}_{11}$ |
| ${ }_{2} \mathrm{~A}_{1} \subset 15$ | $42{ }_{1} \mathrm{~B}_{12}$ |
| ${ }_{2} \mathrm{~A}_{2}$ ¢ 16 | $41{ }_{2} \mathrm{~B}_{1}$ |
| $\mathrm{V}_{\mathrm{CC}}$ [17 | $40{ }_{2} \mathrm{~B}_{2}$ |
| ${ }_{2} \mathrm{~A}_{3} \square 18$ | $39{ }_{2} \mathrm{~B}_{3}$ |
| GND 19 | 38 GND |
| ${ }_{2} \mathrm{~A}_{4}$ - 20 | $37 \square_{2} \mathrm{~B}_{4}$ |
| ${ }_{2} \mathrm{~A}_{5}-21$ | $36{ }_{2} \mathrm{~B}_{5}$ |
| ${ }_{2} \mathrm{~A}_{6}-22$ | $35{ }_{2} \mathrm{~B}_{6}$ |
| ${ }_{2} \mathrm{~A}_{7} \mathrm{C}_{23}$ | $34{ }_{2} \mathrm{~B}_{7}$ |
| ${ }_{2} \mathrm{~A}_{8}-24$ | $33{ }_{2} \mathrm{~B}_{8}$ |
| ${ }_{2} \mathrm{~A}_{9} \square 25$ | $32{ }_{2} \mathrm{~B}_{9}$ |
|  | $31-{ }_{2} \mathrm{~B}_{10}$ |
| ${ }_{2} \mathrm{~A}_{11}$ ( $\mathrm{A}_{12}$ | $30{ }_{2}{ }_{2} \mathrm{~B}_{11}$ |
| ${ }_{2} \mathrm{~A}_{12}$ 28 | $29{ }_{2} \mathrm{~B}_{12}$ |

## Pin Description

| Pin Name | I/O | Description |
| :---: | :--- | :--- |
| $\mathrm{x} \overline{\mathrm{OE}}$ | I | Output Enables |
| xAx | $\mathrm{I} / \mathrm{O}$ | Bus A |
| xBx | $\mathrm{I} / \mathrm{O}$ | Bus B |

## Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)


## Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $\mathrm{T}_{\mathrm{A}}=-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V} \pm 10 \%$ )

| Parameters | Description | Test Conditions | Min. | Typ ${ }^{(1)}$ | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{IK}}$ | Clamp Diode Voltage | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{I}}=-18 \mathrm{~mA}$ |  |  | 1.2 | V |
| $\mathrm{I}_{\text {I }}$ | Input LOW Current | $\mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=\mathrm{VCC}^{\text {or GND }}$ |  |  | 10 | $\mu \mathrm{A}$ |
|  |  | $\mathrm{V}_{\mathrm{CC}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=5.5 \mathrm{~V}$ |  |  | $\pm 1$ |  |
| $\mathrm{I}_{\mathrm{CC}}$ | Quiescent Power Supply Current | $\mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=\mathrm{VCC}$ or GND |  |  | 3.0 |  |
| $\Delta \mathrm{I}_{\mathrm{CC}}$ | Supply Current per Control Input @ TTL High | $\mathrm{V}_{\mathrm{CC}}=5.5 \mathrm{~V}$, One input at 3.4 V , other inputs at $\mathrm{V}_{\mathrm{CC}}$ or GND |  |  | 2.5 | mA |
| $\mathrm{C}_{\mathrm{I}}{ }^{(2)}$ | Control Input Capacitance | $\mathrm{V}_{\mathrm{I}}=3 \mathrm{~V}$ or 0 |  |  | 4.5 |  |
| $\mathrm{C}_{\mathrm{IO}}(\mathrm{OFF})^{(2)}$ | A/B Switch OFF Capacitance | $\mathrm{V}_{\mathrm{O}}=3 \mathrm{~V}$ or $0, \mathrm{OE}=\mathrm{V}_{\mathrm{CC}}$ |  |  | 5.5 |  |
| $\mathrm{R}_{\text {ON }}$ | Switch On Resistance ${ }^{(3)}$ | $\mathrm{V}_{\mathrm{CC}}=4.0 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=2.4 \mathrm{~V}, \mathrm{I}_{\mathrm{I}}=15 \mathrm{~mA}$ |  | 14 | 20 | $\Omega$ |
|  |  | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{I}}=64 \mathrm{~mA}$ |  | 5 | 7 |  |
|  |  | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}, \mathrm{~V}_{\mathrm{I}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{I}}=30 \mathrm{~mA}$ |  | 5 | 7 |  |
|  |  | $\mathrm{V}_{\mathrm{CC}}=4.5 \mathrm{~V}, \mathrm{I}=2.4 \mathrm{~V}, \mathrm{I}_{\mathrm{I}}=15 \mathrm{~mA}$ |  | 8 | 12 |  |

## Notes:

1. Typical values are at $\mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ambient and maximum loading.
2. This parameter is determined by device characterization but is not production tested.
3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two $(A, B)$ pins.

## Power Supply Characteristics

| Parameters | Description | Min. | Typ. $^{(\mathbf{1 )}}$ | Max. | Units |
| :---: | :--- | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{CC}}$ | Supply Voltage | 4.0 |  | 5.5 |  |
| $\mathrm{~V}_{\mathrm{IH}}$ | High Level Input Voltage | 2.0 |  |  | V |
| $\mathrm{~V}_{\mathrm{IL}}$ | Low Level Input voltage |  |  | 0.8 |  |
| $\mathrm{~T}_{\mathrm{A}}$ | Operating Free-Air Tempertaure | -40 |  | 85 | ${ }^{\circ} \mathrm{C}$ |

Note:

1. Typical values are at $\mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V},+25^{\circ} \mathrm{C}$ ambient.

## Switching Characteristics over Operating Range

| Parameters | From <br> (Input) | To <br> (Output) | Test <br> Conditions | $\mathrm{V}_{\text {CC }}=5 \mathrm{~V} \pm 0.5 \mathrm{~V}$ |  | $\mathrm{V}_{\mathrm{CC}}=4 \mathrm{~V}$ |  | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | Min. | Max. | Min. | Max. |  |
| $t_{\text {PD }}{ }^{(1)}$ | A or B | B or A | $\begin{aligned} \mathrm{C}_{\mathrm{L}} & =50 \mathrm{pF} \\ \mathrm{R}_{\mathrm{L}} & =500 \Omega \end{aligned}$ |  | 0.25 |  | 0.25 | ns |
| $\mathrm{t}_{\text {EN }}$ | $\overline{\mathrm{OE}}$ | A or B |  | 1.5 | 9.3 |  | 10.1 |  |
| $\mathrm{t}_{\text {DIS }}$ | OE | A or B |  | 1.5 | 8.5 |  | 7.1 |  |

## Notes:

1. This parameter is guaranteed but not tested on Propagation Delays. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.


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## Note:

- For latest package info, please check: http://www.pericom.com/products/packaging/mechanicals.php


## Ordering Information

| Ordering Code | Package Type | Package Description |
| :---: | :---: | :---: |
| PI5C16211AE | A | Pb-free \& Green, 56-pin 240-mil wide TSSOP |

## Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- $\mathrm{E}=\mathrm{Pb}$-free $\&$ Green
- Adding an X suffix $=$ Tape/Reel

