

Product data sheet

## 1. General description

High voltage, high speed planar passivated NPN power switching transistor in a SOT428 (DPAK) surface mountable plastic package.

### 2. Features and benefits

- Fast switching
- Low thermal resistance
- Surface mountable package
- Very high voltage capability
- Very low switching and conduction losses

### 3. Applications

- DC-to-DC converters
- High frequency electronic lighting ballasts
- Inverters
- Motor control systems

### 4. Quick reference data

Table 1. Qui	ck reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CM</sub>	peak collector current	Fig. 1; Fig. 2; Fig. 3	-	-	10	А
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> ≤ 25 °C; <u>Fig. 4</u>	-	-	80	W
V <sub>CESM</sub>	collector-emitter peak voltage	V <sub>BE</sub> = 0 V	-	-	1000	V
Static chara	acteristics	·				-
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 5 mA; V <sub>CE</sub> = 5 V; T <sub>mb</sub> = 25 °C; Fig. 11	10	22	30	
		$I_{C}$ = 500 mA; $V_{CE}$ = 5 V; $T_{mb}$ = 25 °C; Fig. 11	14	25	35	

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# 5. Pinning information

Table 2. Pinning information									
Pin	Symbol	Description	Simplified outline	Graphic symbol					
1	В	base	<u></u>	C					
2	С	collector[1]		в					
3	E	emitter							
mb	С	mounting base; connected to collector		E sym123					
			DPAK (SOT428)						

[1] it is not possible to make a connection to pin 2 of the SOT428 (DPAK) package.

# 6. Ordering information

Table 3. Ordering information							
Type number	Package						
	Name	Description	Version				
BUJ303AD	DPAK	plastic single-ended surface-mounted package (DPAK); 3 leads (one lead cropped)	SOT428				

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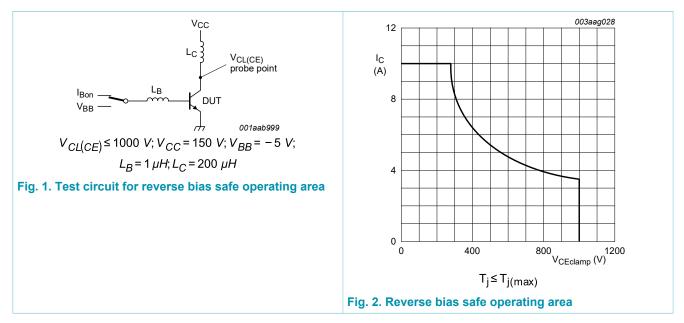
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### 7. Limiting values

### Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CESM</sub>	collector-emitter peak voltage	V <sub>BE</sub> = 0 V	-	1000	V
V <sub>CEO</sub>	collector-emitter voltage	I <sub>B</sub> = 0 A	-	500	V
I <sub>C</sub>	collector current	Fig. 1; Fig. 2; Fig. 3	-	5	А
I <sub>CM</sub>	peak collector current		-	10	А
I <sub>B</sub>	base current		-	2	А
I <sub>BM</sub>	peak base current		-	4	А
P <sub>tot</sub>	total power dissipation	T <sub>mb</sub> ≤ 25 °C; <u>Fig. 4</u>	-	80	W
T <sub>stg</sub>	storage temperature		-65	150	°C
Тј	junction temperature		-	150	°C



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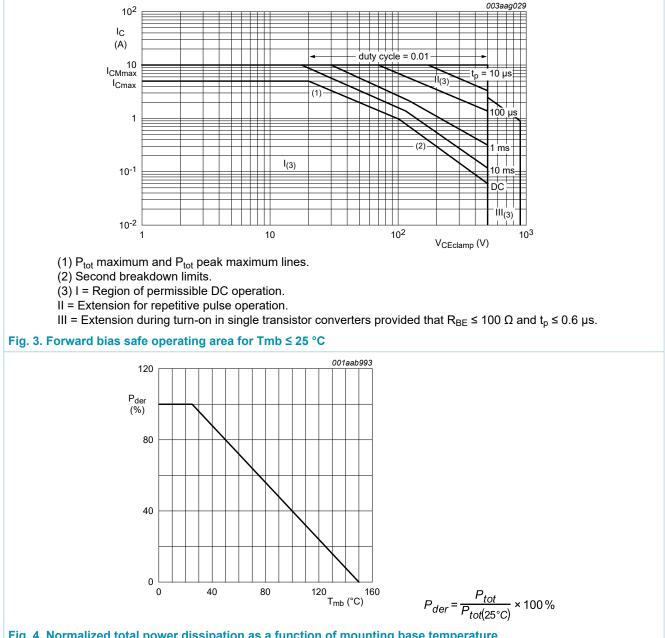
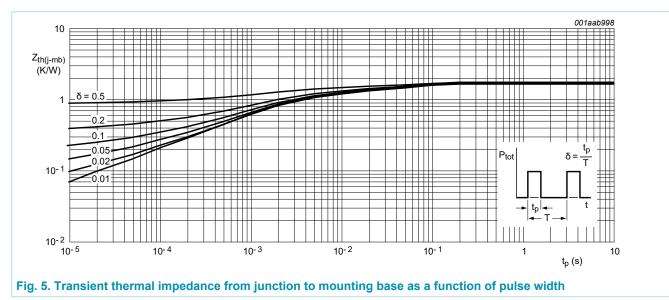


Fig. 4. Normalized total power dissipation as a function of mounting base temperature

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### 8. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-mb)</sub>	thermal resistance from junction to mounting base	<u>Fig. 5</u>	-	-	1.56	K/W
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient free air	printed circuit board (FR4) mounted; minimum footprint	-	75	-	K/W



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## 9. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Static chara	icteristics						
I <sub>CES</sub>	collector-emitter cut-off	V <sub>BE</sub> = 0 V; V <sub>CE</sub> = 1000 V	[1]	-	-	1	mA
	current (base shorted)	V <sub>BE</sub> = 0 V; V <sub>CE</sub> = 1000 V; T <sub>j</sub> = 125 °C	[1]	-	-	2	mA
I <sub>CBO</sub>	collector-base cut-off current (emitter open)	V <sub>CB</sub> = 1000 V; I <sub>E</sub> = 0 A; T <sub>mb</sub> = 25 °C	[1]	-	-	1	mA
I <sub>CEO</sub>	collector-emitter cut-off current (base open)	$V_{CE}$ = 500 V; I <sub>B</sub> = 0 A; T <sub>mb</sub> = 25 °C	[1]	-	-	0.1	mA
I <sub>EBO</sub>	emitter-base cut-off current (collector open)	V <sub>EB</sub> = 9 V; I <sub>C</sub> = 0 A; T <sub>mb</sub> = 25 °C		-	-	0.1	mA
V <sub>CEOsus</sub>	collector-emitter sustaining voltage (base open)	$I_B = 0 \text{ A}; I_C = 100 \text{ mA}; L_C = 25 \text{ mH};$ $T_{mb} = 25 \text{ °C}; Fig. 6; Fig. 7$		500	-	-	V
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 3 A; I <sub>B</sub> = 0.6 A; T <sub>mb</sub> = 25 °C; <u>Fig. 8;</u> <u>Fig. 9</u>		-	0.25	1.5	V
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 3 A; I <sub>B</sub> = 0.6 A; T <sub>mb</sub> = 25 °C; Fig. 10		-	0.97	1.3	V
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 5 mA; V <sub>CE</sub> = 5 V; T <sub>mb</sub> = 25 °C; Fig. 11		10	22	30	
		I <sub>C</sub> = 500 mA; V <sub>CE</sub> = 5 V; T <sub>mb</sub> = 25 °C; Fig. 11		14	25	35	
h <sub>FEsat</sub>	DC saturation current gain	I <sub>C</sub> = 2.5 A; V <sub>CE</sub> = 5 V; T <sub>mb</sub> = 25 °C; Fig. 11		10	13.5	17	
		I <sub>C</sub> = 3 A; V <sub>CE</sub> = 5 V; T <sub>mb</sub> = 25 °C; Fig. 11		-	12	-	
Dynamic ch	aracteristics (switching tir	nes - resistive load)					
t <sub>s</sub>	storage time	I <sub>C</sub> = 2.5 A; I <sub>Bon</sub> = 0.5 A; I <sub>Boff</sub> = -0.5 A;		-	3.4	4	μs
t <sub>f</sub>	fall time	R <sub>L</sub> = 75 Ω; T <sub>mb</sub> = 25 °C; <u>Fig. 12</u> ; <u>Fig. 13</u>		-	0.33	0.45	μs
Dynamic ch	aracteristics (switching tir	nes - inductive load)					
t <sub>s</sub>	storage time	$\begin{split} I_{C} &= 2.5 \text{ A}; \ I_{Bon} = 0.5 \text{ A}; \ V_{BB} = -5 \text{ V}; \\ I_{B} &= 1 \ \mu\text{H}; \ T_{mb} = 25 \ ^{\circ}\text{C}; \ \underline{\text{Fig. 14}}; \ \underline{\text{Fig. 15}} \end{split}$		-	1.4	1.6	μs
				-	1.7	1.9	μs
t <sub>f</sub>	fall time	$I_{C} = 2.5 \text{ A}; I_{Bon} = 0.5 \text{ A}; V_{BB} = -5 \text{ V}; \\ L_{B} = 1 \mu\text{H}; T_{j} = 100 ^\circ\text{C}; \overline{\text{Fig. 14}}; \overline{\text{Fig. 15}}$		-	145	160	ns
				-	160	200	ns

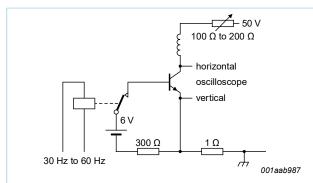
[1] Measured with half-sine wave voltage (curve tracer).

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# Fig. 6. Test circuit for collector-emitter sustaining voltage

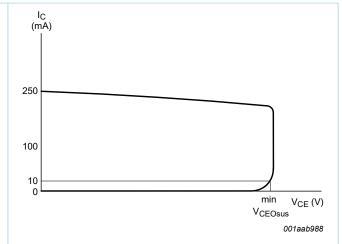
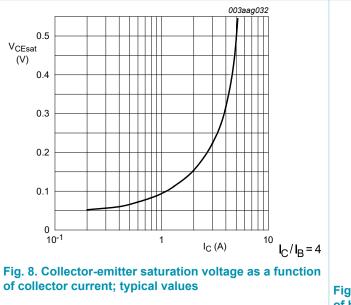


Fig. 7. Oscilloscope display for collector-emitter sustaining voltage test waveform



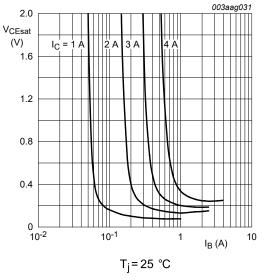
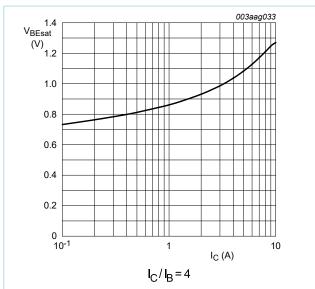


Fig. 9. Collector-emitter saturation voltage as a function of base current; typical values

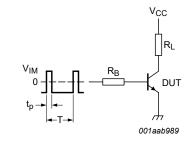
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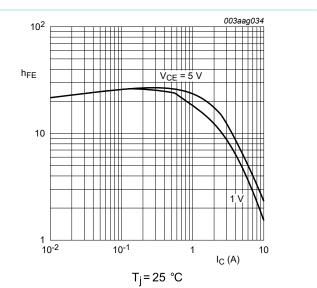




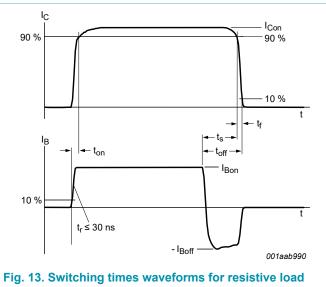


 $V_{IM}$  = -6 to +8 V;  $V_{CC}$  = 250 V;  $t_p$  = 20  $\mu$ s;  $\delta = \frac{t_p}{T}$  = 0.01 R<sub>B</sub> and R<sub>L</sub> calculated from I<sub>Con</sub> and I<sub>Bon</sub> requirements.

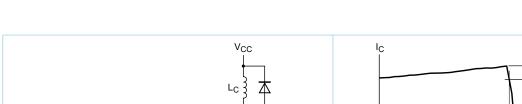
Fig. 12. Test circuit for resistive load switching



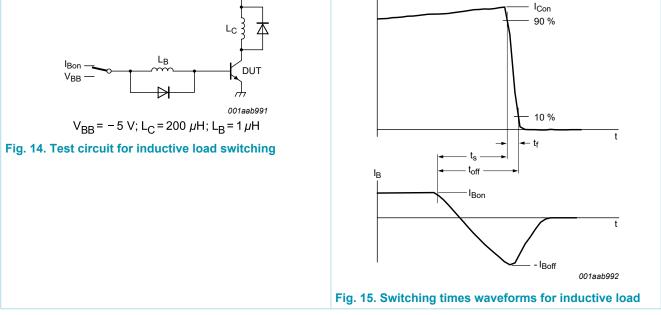




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### 10. Package outline

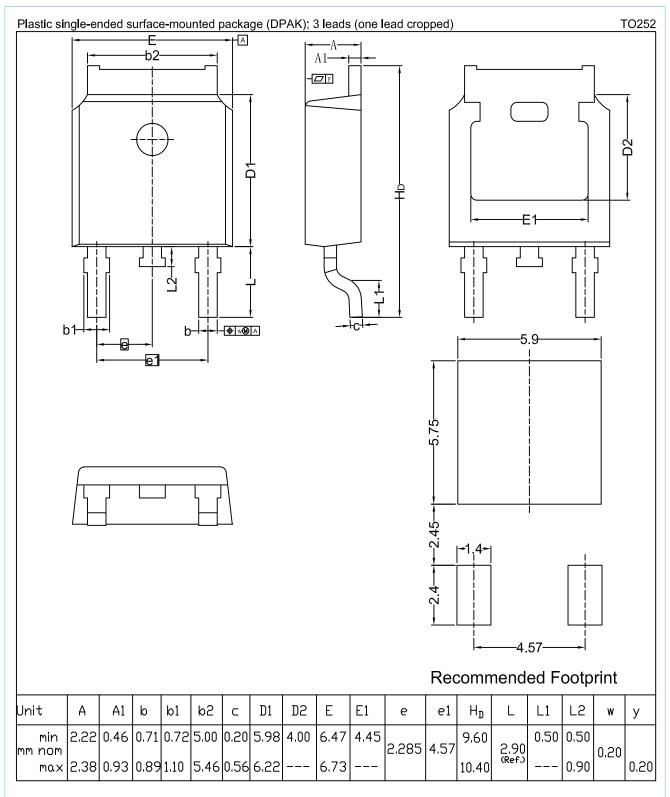


Fig. 16. Package outline DPAK (SOT428)

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Document status [1][2]	Product status [ <u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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