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Team Nexperia

PHP119NQ06T

N-channel TrenchMOS standard level FET

Rev. 02 — 16 April 2010

Product data sheet

1. Product profile

1.1 General description

Standard level N-channel enhancement mode Field-Effect Transistor (FET) in a plastic package using TrenchMOS technology. This product is designed and qualified for use in computing, communications, consumer and industrial applications only.

1.2 Features and benefits

- Low conduction losses due to low on-state resistance
- Suitable for standard level gate drive sources

1.3 Applications

- DC-to-DC convertors
- General industrial applications
- Motors, lamps and solenoids
- Uninterruptible power supplies

1.4 Quick reference data

Table 1. Quick reference data

Cumbal	Daramatar	Canditions	Miss	True	Max	l lmit
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	55	V
I _D	drain current	$T_{mb} = 25 \text{ °C}; V_{GS} = 10 \text{ V};$ see <u>Figure 1</u> ; see <u>Figure 3</u>	-	-	75	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	200	W
Static chara	acteristics					
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 9}}{\text{10}};$ $S_j = \frac{10}{3} \text{ C}$	-	5.8	7.1	mΩ
Dynamic ch	naracteristics					
Q_{GD}	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A};$ $V_{DS} = 44 \text{ V}; T_j = 25 \text{ °C};$ see Figure 11	-	17	-	nC



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		
2	D	drain	mb	D
3	S	source		
mb	D	mounting base; connected to drain		mbb076 S
			SOT78 (TO-220AB)	

3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PHP119NQ06T	TO-220AB	plastic single-ended package; heatsink mounted; 1 mounting hole; 3-lead TO-220AB	SOT78

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Product data sheet

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	55	V
V_{DGR}	drain-gate voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}; R_{GS} = 20 \text{ k}\Omega$	-	-	55	V
V_{GS}	gate-source voltage		-20	-	20	V
I _D	drain current	V _{GS} = 10 V; T _{mb} = 100 °C; see <u>Figure 1</u>	-	-	75	Α
		V_{GS} = 10 V; T_{mb} = 25 °C; see <u>Figure 1</u> ; see <u>Figure 3</u>	-	-	75	Α
I _{DM}	peak drain current	$t_p \le 10 \ \mu s$; pulsed; $T_{mb} = 25 \ ^{\circ}C$; see Figure 3	-	-	240	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	200	W
T _{stg}	storage temperature		-55	-	175	°C
Tj	junction temperature		-55	-	175	°C
Source-drain	diode					
Is	source current	T _{mb} = 25 °C	-	-	75	Α
I _{SM}	peak source current	$t_p \le 10 \ \mu s$; pulsed; $T_{mb} = 25 \ ^{\circ}C$	-	-	240	Α
Avalanche ru	ggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 75 A; $V_{sup} \le$ 55 V; unclamped; t_p = 0.1 ms; R_{GS} = 50 Ω	-	-	280	mJ

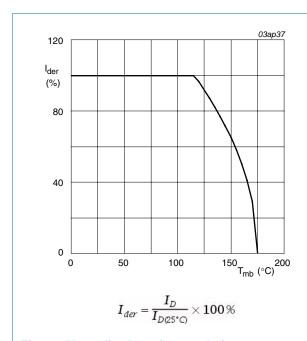


Fig 1. Normalized continuous drain current as a function of mounting base temperature

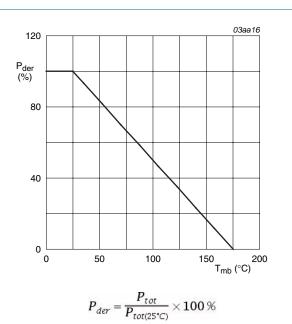
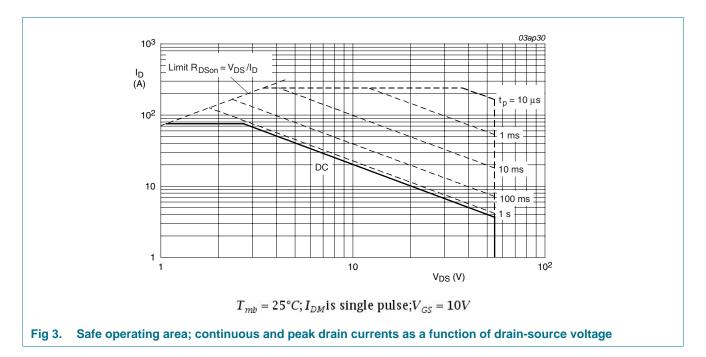


Fig 2. Normalized total power dissipation as a function of mounting base temperature

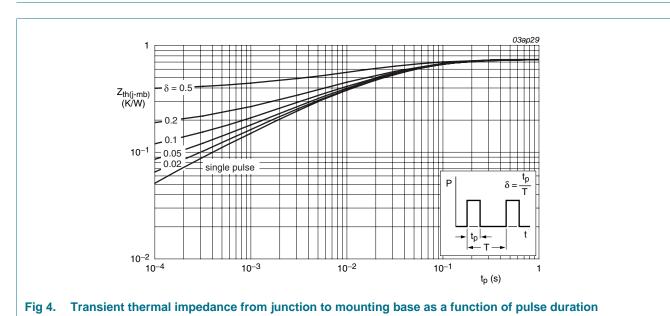
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5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see <u>Figure 4</u>	-	-	0.75	K/W
R _{th(j-a)}	thermal resistance from junction to ambient	vertical in still air	-	60	-	K/W



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6. Characteristics

Table 6. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics					
V _{(BR)DSS}	drain-source	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	50	-	-	V
	breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	55	-	-	V
00(11)	gate-source threshold voltage	$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = -55 \text{ °C}$; see <u>Figure 7</u> ; see <u>Figure 8</u>	-	-	4.4	V
		$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = 175 \text{ °C}$; see <u>Figure 7</u> ; see <u>Figure 8</u>	1	-	-	V
		$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ °C}$; see <u>Figure 7</u> ; see <u>Figure 8</u>	2	3	4	V
I _{DSS}	drain leakage current	$V_{DS} = 55 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	1	μΑ
		$V_{DS} = 55 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175 \text{ °C}$	-	-	500	μΑ
I _{GSS}	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nΑ
		$V_{GS} = -20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
DOON	drain-source on-state resistance	$V_{GS} = 10 \text{ V}$; $I_D = 25 \text{ A}$; $T_j = 175 \text{ °C}$; see Figure 9; see Figure 10	-	10.6	14.2	mΩ
		$V_{GS} = 10 \text{ V}$; $I_D = 25 \text{ A}$; $T_j = 25 \text{ °C}$; see Figure 9; see Figure 10	-	5.8	7.1	mΩ
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	$I_D = 25 \text{ A}; V_{DS} = 44 \text{ V}; V_{GS} = 10 \text{ V};$	-	53	-	nC
Q_{GS}	gate-source charge	T _j = 25 °C; see <u>Figure 11</u>	-	12.3	-	nC
Q_{GD}	gate-drain charge		-	17	-	nC
C _{iss}	input capacitance	$V_{DS} = 25 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$	-	2820	-	pF
C _{oss}	output capacitance	T _j = 25 °C; see <u>Figure 12</u>	-	554	-	pF
C _{rss}	reverse transfer capacitance		-	200	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 30 \text{ V}; R_L = 1.2 \Omega; V_{GS} = 10 \text{ V};$	-	24	-	ns
t _r	rise time	$R_{G(ext)} = 10 \Omega; T_j = 25 °C$	-	52	-	ns
t _{d(off)}	turn-off delay time		-	77	-	ns
t _f	fall time		-	41	-	ns
Source-drai	n diode					
V_{SD}	source-drain voltage	$I_S = 25 \text{ A}$; $V_{GS} = 0 \text{ V}$; $T_j = 25 \text{ °C}$; see Figure 13	-	0.85	1.2	V
t _{rr}	reverse recovery time	$I_S = 20 \text{ A}; dI_S/dt = -100 \text{ A/}\mu\text{s}; V_{GS} = 0 \text{ V};$	-	62	-	ns
Q _r	recovered charge	$V_{DS} = 25 \text{ V}; T_j = 25 \text{ °C}$	_	60	_	nC

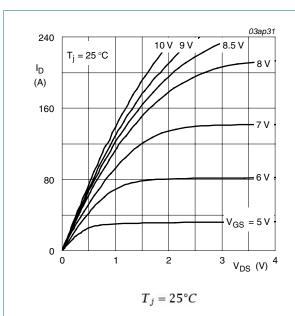


Fig 5. Output characteristics: drain current as a function of drain-source voltage; typical values

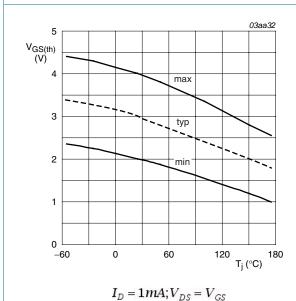
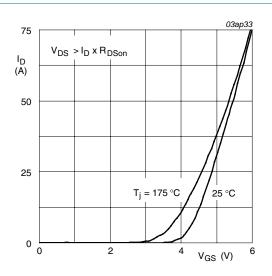
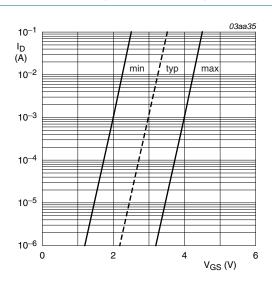


Fig 7. Gate-source threshold voltage as a function of junction temperature



 $T_j = 25^{\circ}C$ and $175^{\circ}C$; $V_{DS} > I_D \times R_{DSon}$

Fig 6. Transfer characteristics: drain current as a function of gate-source voltage; typical values



 $T_j = 25 \,^{\circ}C; V_{DS} = 5V$

Fig 8. Sub-threshold drain current as a function of gate-source voltage

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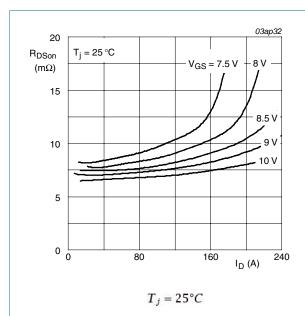


Fig 9. Drain-source on-state resistance as a function of drain current; typical values

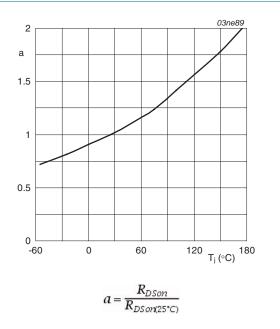


Fig 10. Normalized drain-source on-state resistance factor as a function of junction temperature

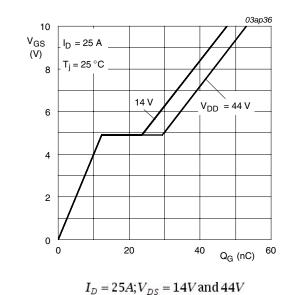
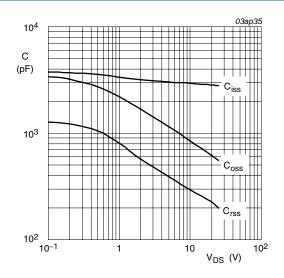
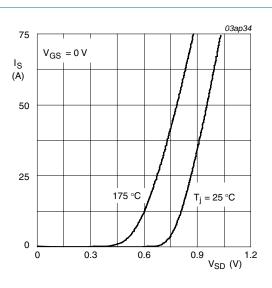


Fig 11. Gate-source voltage as a function of gate charge; typical values



 $V_{GS} = 0V; f = 1MHz$

Fig 12. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



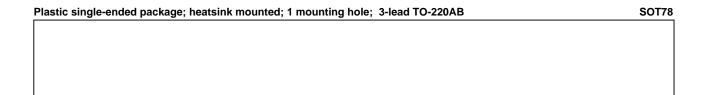
 $T_j = 25$ °C and 175°C; $V_{GS} = 0V$

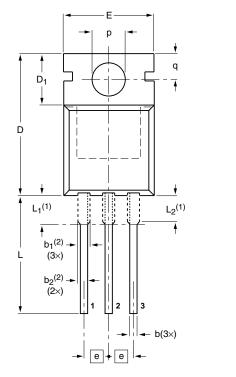
Fig 13. Source current as a function of source-drain voltage; typical values

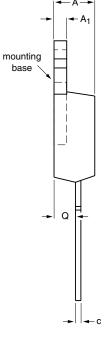
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7. Package outline







0 5 10 mm scale

DIMENSIONS (mm are the original dimensions)

UNI	ТА	A ₁	b	b ₁ (2)	b ₂ (2)	С	D	D ₁	E	е	L	L ₁ (1)	L ₂ ⁽¹⁾ max.	р	q	Q
mm	4.7 4.1	1.40 1.25	0.9 0.6	1.6 1.0	1.3 1.0	0.7 0.4	16.0 15.2	6.6 5.9	10.3 9.7	2.54	15.0 12.8	3.30 2.79	3.0	3.8 3.5	3.0 2.7	2.6 2.2

Notes

- 1. Lead shoulder designs may vary.
- 2. Dimension includes excess dambar.

OUTLINE	REFERENCES				EUROPEAN	ISSUE DATE	
VERSION	IEC	C JEDEC JEIT			PROJECTION	ISSUE DATE	
SOT78		3-lead TO-220AB	SC-46			08-04-23 08-06-13	

Fig 14. Package outline SOT78 (TO-220AB)

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8. Revision history

Table 7. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes				
PHP119NQ06T_2	20100416	Product data sheet	-	PHP_PHB119NQ06T-01				
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. 							
 Legal texts have been adapted to the new company name where appropriate. 								
	 Typenumbe 	r PHP119NQ061 separate	d from data sheet PHP_PF	1B119NQ061-01.				
PHP_PHB119NQ06T-01 (9397 750 13176)	20040505	Product data	-	-				

9. Legal information

9.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions"
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