# **RF Power LDMOS Transistors**

# High Ruggedness N-Channel Enhancement-Mode Lateral MOSFETs

These rugged devices are designed for use in high VSWR industrial, scientific and medical applications and HF and VHF communications as well as radio and VHF TV broadcast, aerospace and mobile radio applications. Their design allows for wide frequency range use up to 250 MHz.

### Typical Performance: V<sub>DD</sub> = 50 Vdc

Frequency (MHz)	Signal Type	P <sub>out</sub> (W)	G <sub>ps</sub> (dB)	η <sub>D</sub> (%)
40.68	CW	100 CW	26.0	75.1
136–174	CW	85 CW	23.1	72.2
230 (1)	Pulse (100 μsec, 20% Duty Cycle)	115 Peak	21.1	76.7

#### Load Mismatch/Ruggedness

Frequency (MHz)	Signal Type	VSWR	P <sub>in</sub> (W)	Test Voltage	Result
40.68	CW	> 65:1 at all Phase Angles	0.64 Peak (3 dB Overdrive)	50	No Device Degradation
230	Pulse (100 μsec, 20% Duty Cycle)	> 65:1 at all Phase Angles	1.8 Peak (3 dB Overdrive)	50	No Device Degradation

1. Measured in 230 MHz typical fixture.

#### Features

- Mirror pinout versions (A and B) to simplify use in a push-pull, two-up configuration
- Characterized from 30 to 50 V
- Suitable for linear application
- Integrated ESD protection with greater negative gate-source voltage range for improved Class C operation
- Included in NXP product longevity program with assured supply for a minimum of 15 years after launch

#### **Typical Applications**

- · Industrial, scientific, medical (ISM)
  - Laser generation
  - Plasma etching
  - Particle accelerators
  - MRI and other medical applications
  - Industrial heating, welding and drying systems
- Broadcast
  - Radio broadcast
  - VHF TV broadcast
- Mobile radio
- VHF base stations
- HF and VHF communications
- · Switch mode power supplies

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**VRoHS** 

# MRF101AN MRF101BN

PREPRODUCTION

1.8–250 MHz, 100 W CW, 50 V WIDEBAND RF POWER LDMOS TRANSISTORS



This document contains information on a preproduction product. Specifications and information herein are subject to change without notice.

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#### **Table 1. Maximum Ratings**

Rating	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-0.5, +133	Vdc
Gate-Source Voltage	V <sub>GS</sub>	-6.0, +10	Vdc
Operating Voltage	V <sub>DD</sub>	50	Vdc
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C
Case Operating Temperature Range	T <sub>C</sub>	-40 to +150	°C
Operating Junction Temperature Range (1,2)	TJ	-40 to +175	°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	182 0.91	W W/°C

#### **Table 2. Thermal Characteristics**

Characteristic	Symbol	Value <sup>(2,3)</sup>	Unit
Thermal Resistance, Junction to Case CW: Case Temperature 77°C, 150 W CW, 50 Vdc, I <sub>DQ</sub> = 100 mA, 40.68 MHz	$R_{ extsf{ heta}JC}$	1.1	°C/W
Thermal Impedance, Junction to Case Pulse: Case Temperature 73°C, 113 W Peak, 100 μsec Pulse Width, 20% Duty Cycle, 50 Vdc, I <sub>DQ</sub> = 100 mA, 230 MHz	Z <sub>θJC</sub>	0.37	°C/W

#### **Table 3. ESD Protection Characteristics**

Test Methodology	Class		
Human Body Model (per JS-001-2017)	1B, passes 1000 V		
Charge Device Model (per JS-002-2014)	C3, passes 1200 V		

#### Table 4. Moisture Sensitivity Level

Test Methodology		Package Peak Temperature	Unit
Per JESD22-A113, IPC/JEDEC J-STD-020	0	260	°C

# Table 5. Electrical Characteristics ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit	
Off Characteristics						
$ \begin{array}{l} \mbox{Gate-Source Leakage Current} \\ \mbox{(V}_{GS} = 5 \mbox{ Vdc}, \mbox{ V}_{DS} = 0 \mbox{ Vdc}) \end{array} $	I <sub>GSS</sub>			1	μAdc	
Drain-Source Breakdown Voltage $(V_{GS} = 0 \text{ Vdc}, I_D = 50 \text{ mAdc})$	V <sub>(BR)DSS</sub>	133		_	Vdc	
Zero Gate Voltage Drain Leakage Current (V <sub>DS</sub> = 100 Vdc, V <sub>GS</sub> = 0 Vdc)	I <sub>DSS</sub>	—		10	μAdc	
On Characteristics						
Gate Threshold Voltage (V <sub>DS</sub> = 10 Vdc, I <sub>D</sub> = 290 μAdc)	V <sub>GS(th)</sub>	1.7	2.2	2.7	Vdc	
Gate Quiescent Voltage (V <sub>DS</sub> = 50 Vdc, I <sub>D</sub> = 100 mAdc)	V <sub>GS(Q)</sub>		2.5	_	Vdc	
Drain-Source On-Voltage (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 1 Adc)	V <sub>DS(on)</sub>	—	0.45	_	Vdc	
Forward Transconductance (V <sub>DS</sub> = 10 Vdc, I <sub>D</sub> = 8.8 Adc)	9fs	—	7.1	_	S	

1. Continuous use at maximum temperature will affect MTTF.

2. MTTF calculator available at http://www.nxp.com/RF/calculators. (Calculator available when part is in production.)

3. Refer to AN1955, Thermal Measurement Methodology of RF Power Amplifiers. Go to http://www.nxp.com/RF and search for AN1955.

(continued)

#### MRF101AN MRF101BN

# Table 5. Electrical Characteristics ( $T_A = 25^{\circ}C$ unless otherwise noted) (continued)

Characteristic	Symbol	Min	Тур	Max	Unit
Dynamic Characteristics					
Reverse Transfer Capacitance (V <sub>DS</sub> = 50 Vdc ± 30 mV(rms)ac @ 1 MHz, V <sub>GS</sub> = 0 Vdc)	C <sub>rss</sub>		0.96	_	pF
Output Capacitance (V <sub>DS</sub> = 50 Vdc ± 30 mV(rms)ac @ 1 MHz, V <sub>GS</sub> = 0 Vdc)	C <sub>oss</sub>	_	43.4	_	pF
Input Capacitance (V <sub>DS</sub> = 50 Vdc, V <sub>GS</sub> = 0 Vdc ± 30 mV(rms)ac @ 1 MHz)	C <sub>iss</sub>	—	149	_	pF

**Typical Performance – 230 MHz** (In NXP 230 MHz Fixture, 50 ohm system)  $V_{DD}$  = 50 Vdc,  $I_{DQ}$  = 100 mA,  $P_{in}$  = 0.9 W, f = 230 MHz, 100 µsec Pulse Width, 20% Duty Cycle

Common-Source Amplifier Output Power	Pout		115		W
Power Gain	G <sub>ps</sub>	_	21.1	-	dB
Drain Efficiency	η <sub>D</sub>	_	76.7	—	%

Table 6. Load Mismatch/Ruggedness (In NXP 230 MHz Fixture, 50 ohm system) I<sub>DQ</sub> = 100 mA

Frequency (MHz)	Signal Type	VSWR	P <sub>in</sub> (W)	Test Voltage, V <sub>DD</sub>	Result
230	Pulse (100 μsec, 20% Duty Cycle)	> 65:1 at all Phase Angles	1.8 Peak (3 dB Overdrive)	50	No Device Degradation

# PACKAGE DIMENSIONS



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TITLE:			DOCUMEN	NT NO: 98ASA01106D	REV: O
TO-220-3L			STANDAR	D: NON-JEDEC	
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# MRF101AN MRF101BN



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TO-220-3L			STANDAR	D: NON-JEDEC	
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#### NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETER, ANGLES ARE IN DEGREES.
- 2. INTERPRET DIMENSIONS AND TOLERANCES AS PER ASME Y14.5M-1994.
- 3. DIMENSION D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.13 MM (.005 INCH) PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.
- A. HATCHING REPRESENTS THE EXPOSED AREA OF THE THERMAL PAD (PIN 4). DIMENSIONS D2 AND E1 REPRESENT THE VALUES BETWEEN THE TWO OPPOSITE POINTS ALONG THE EDGES OF THE EXPOSED AREA OF THE THERMAL PAD. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION D1 AND E1.
- 5 DIMENSIONS 61 DO NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.15 MM (.006 INCH) PER SIDE IN EXCESS OF THE DIMENSIONS 61 AT MAXIMUM MATERIAL CONDITION.
- 6. EJECTOR MARKS ON TOP SURFACE ARE PERMITTED AND IT IS SUPPLIER OPTION. THE MAXIMUM DEPTH OF EJECTOR MARK IS 0.25 MM (.010 INCH)

	INCH		MILLIMETER			INCH		MILLIMETER	
DIM	MIN	MAX	MIN	MAX	DIM	MIN	MAX	MIN	MAX
А	.167	.190	4.25	4.83	E1	.303		7.70	
A1	.047	.053	1.20	1.34	е	.10 BSC		2.54 BSC	
A2	.098	.115	2.50	2.92	H1	.240	.264	6.10	6.70
b	.028	.038	0.71	0.97	H2	.240	.264	6.10	6.70
b1	.045	.070	1.14	1.78	L	.500	.567	12.70	14.40
с	.014	.024	0.356	0.61	L1	.144	.159	3.65	4.05
D	.564	.624	14.32	15.86	Р	.142	.155	3.60	3.95
D1	.330		8.39		Q	.100	.119	2.54	3.04
D2	.480	.504	12.20	12.80	aaa	.014		0.35	
Е	.392	.412	9.96	10.47	bbb	.014		0.35	
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