

Important notice

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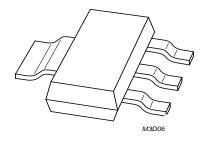
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



BSP41; BSP43 NPN medium power transistors

Product data sheet Supersedes data of 1997 Sep 05 1999 Apr 26



NXP Semiconductors Product data sheet

NPN medium power transistors

BSP41; **BSP43**

FEATURES

- High current (max. 1 A)
- Low voltage (max. 80 V).

APPLICATIONS

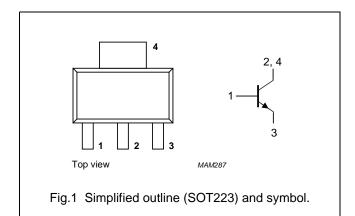
- Telephony and general industrial applications
- · Thick and thin-film circuits.

DESCRIPTION

NPN medium power transistor in a SOT223 plastic package. PNP complements: BSP31; BSP32 and BSP33.

PINNING

PIN	DESCRIPTION		
1	base		
2,4	collector		
3	emitter		



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BSP41		_	70	V
	BSP43		_	90	V
V _{CEO}	collector-emitter voltage	open base			
	BSP41		_	60	V
	BSP43		_	80	V
V _{EBO}	emitter-base voltage	open collector	_	5	V
I _C	collector current (DC)		_	1	Α
I _{CM}	peak collector current		_	2	Α
I _{BM}	peak base current		_	0.2	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	1.3	W
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Device mounted on a printed-circuit board, single sided copper, tinplated, mounting pad for collector 1 cm². For other mounting conditions, see "Thermal considerations for SOT223 in the General Part of associated Handbook".

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NPN medium power transistors

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THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	93	K/W
R _{th j-s}	thermal resistance from junction to soldering point		12	K/W

Note

1. Device mounted on a printed-circuit board, single sided copper, tinplated, mounting pad for collector 1 cm². For other mounting conditions, see *"Thermal considerations for SOT223 in the General Part of associated Handbook"*.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 60 V	_	100	nA
		I _E = 0; V _{CB} = 60 V; T _j = 150 °C	_	50	μΑ
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = 5 V$	_	100	nA
h _{FE}	DC current gain	$I_C = 100 \mu A; V_{CE} = 5 V; note 1$	30	_	
		I _C = 100 mA; V _{CE} = 5 V; note 1	100	300	
		$I_C = 500 \text{ mA}; V_{CE} = 5 \text{ V}; \text{ note 1}$	50	_	
V_{CEsat}	collector-emitter saturation voltage	$I_C = 150 \text{ mA}; I_B = 15 \text{ mA}; \text{ note 1}$	_	0.25	٧
		$I_C = 500 \text{ mA}$; $I_B = 50 \text{ mA}$; note 1	-	0.5	V
V _{BEsat}	base-emitter saturation voltage	IC = 150 mA; IB = 15 mA; note 1	-	1	V
		$I_C = 500 \text{ mA}$; $I_B = 50 \text{ mA}$; note 1	_	1.2	V
f _T	transition frequency	$I_C = 50 \text{ mA}; V_{CE} = 10 \text{ V}; f = 100 \text{ MHz}$	100	_	MHz

Note

1. Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.01.$

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NXP Semiconductors Product data sheet

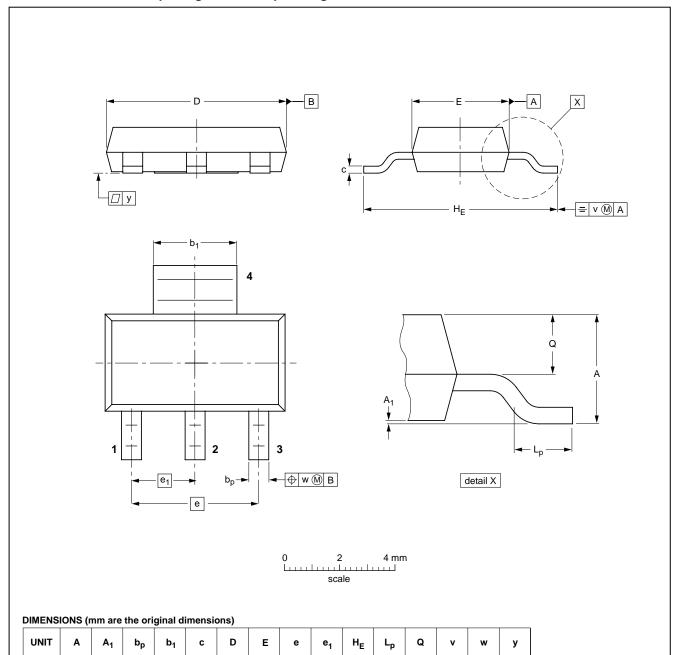
NPN medium power transistors

BSP41; **BSP43**

PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 4 leads

SOT223



OUTLINE		REFER	RENCES	EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE
SOT223			SC-73		97-02-28

SC-73

2.3

4.6

0.95

0.2

0.1

99-09-13

3.7 3.3

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0.32

0.22

6.7

mm

SOT223

0.01

0.60

2.9

NXP Semiconductors Product data sheet

NPN medium power transistors

BSP41; **BSP43**

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
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NXP Semiconductors

Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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