

Product data sheet

1. General description

P-channel enhancement mode Field-Effect Transistor (FET) in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

2. Features and benefits

- Logic-level compatible
- · Very fast switching
- Trench MOSFET technology
- ESD protection up to 1 kV
- Ultra thin package profile with 0.37 mm height

3. Applications

- Relay driver
- High-speed line driver
- High-side load switch
- Switching circuits

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-	-50	V
V _{GS}	gate-source voltage	_		-20	-	20	V
I _D	drain current	V _{GS} = -10 V; T _{amb} = 25 °C	[1]	-	-	-230	mA
Static chara	octeristics						
R _{DSon}	drain-source on-state resistance	V _{GS} = -10 V; I _D = -100 mA; T _j = 25 °C		-	4.5	7.5	Ω

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².



5. Pinning information

Table 2. F	Pinning infor	mation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	1	D
2	S	source		
3	D	drain	Transparent top view DFN1006B-3 (SOT883B)	G G S 017aaa259

6. Ordering information

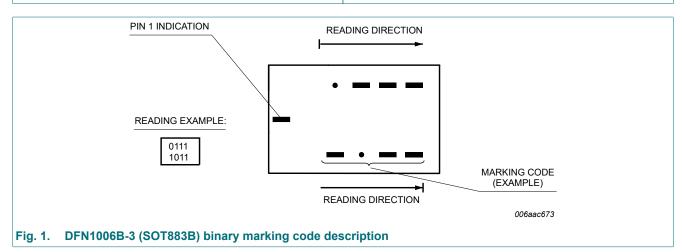
Table 3. Ordering information

Type number Package					
	Name	Description	Version		
BSS84AKMB	DFN1006B-3	plastic, leadless ultra small plastic package; 3 solder lands; 0.35 mm pitch; 1.0 mm x 0.6 mm x 0.37 mm body	SOT883B		

7. Marking

Table 4. Marking codes

Type number	Marking code
BSS84AKMB	0000
	0010



8. Limiting values

Table 5. Limiting values

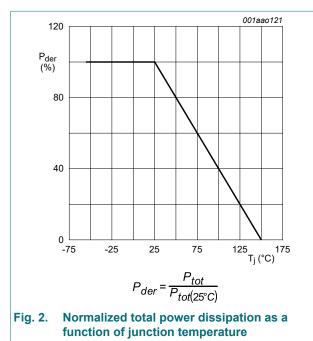
In accordance with the Absolute Maximum Rating System (IEC 60134).

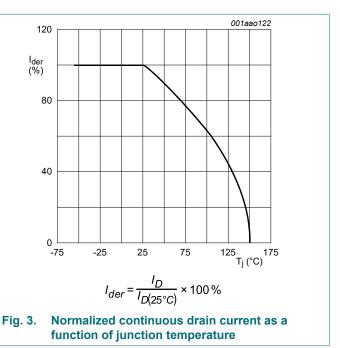
Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _j = 25 °C		-	-50	V
V _{GS}	gate-source voltage			-20	20	V
I _D	drain current	V _{GS} = -10 V; T _{amb} = 25 °C	[1]	-	-230	mA
		V _{GS} = -10 V; T _{amb} = 100 °C	[1]	-	-150	mA
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	-0.9	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	360	mW
			[1]	-	715	mW
		T _{sp} = 25 °C		-	2700	mW
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drai	n diode			I		_
I _S	source current	T _{amb} = 25 °C	[1]	-	-230	mA
ESD maxim	um rating					_
V _{ESD}	electrostatic discharge voltage	НВМ	[3]	-	1000	V

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 1 cm².

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

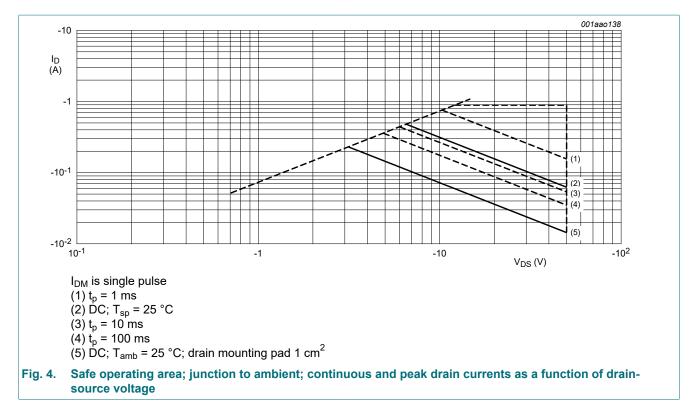
[3] Measured between all pins.





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50 V, single P-channel Trench MOSFET

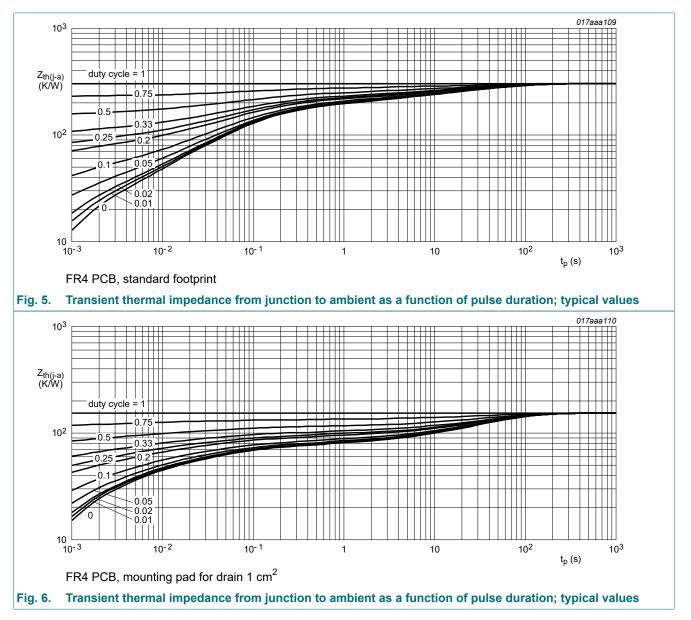


9. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
R _{th(j-a)} thermal resistance from junction to ambient	thermal resistance from	in free air	[1]	-	305	350	K/W
		[2]	-	150	175	K/W	
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	40	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 1 cm².



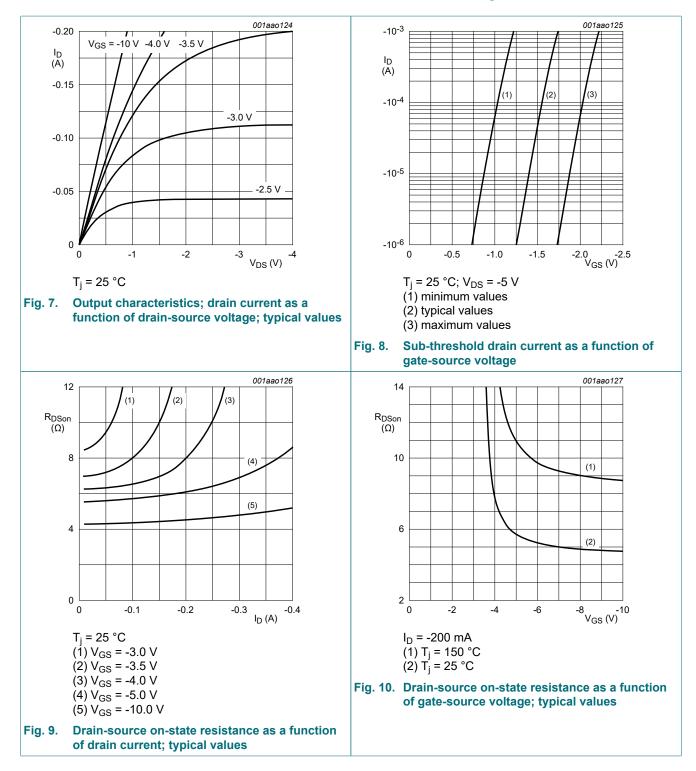
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10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	cteristics					
V _{(BR)DSS}	drain-source breakdown voltage	I _D = -250 μA; V _{GS} = 0 V; T _j = 25 °C	-50	-	-	V
V _{GSth}	gate-source threshold voltage	I _D = -250 μA; V _{DS} =V _{GS} ; T _j = 25 °C	-1.1	-1.6	-2.1	V
I _{DSS}	drain leakage current	V _{DS} = -50 V; V _{GS} = 0 V; T _j = 25 °C	-	-	-1	μA
		V _{DS} = -50 V; V _{GS} = 0 V; T _j = 150 °C	-	-	-2	μA
I _{GSS}	gate leakage current	V _{GS} = -20 V; V _{DS} = 0 V; T _j = 25 °C	-	-	-10	μA
		V_{GS} = 20 V; V_{DS} = 0 V; T_j = 25 °C	-	-	-10	μA
R _{DSon}	drain-source on-state resistance	V _{GS} = -10 V; I _D = -100 mA; T _j = 25 °C	-	4.5	7.5	Ω
		V _{GS} = -10 V; I _D = -100 mA; T _j = 150 °C	-	8	13.5	Ω
		V _{GS} = -5 V; I _D = -100 mA; T _j = 25 °C	-	5.7	8.5	Ω
9 _{fs}	forward transconductance	V _{DS} = -10 V; I _D = -100 mA; T _j = 25 °C	-	150	-	mS
Dynamic ch	aracteristics					
Q _{G(tot)}	total gate charge	V _{DS} = -25 V; I _D = -200 mA; V _{GS} = -5 V;	-	0.26	0.35	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.12	-	nC
Q _{GD}	gate-drain charge	V_{DS} = -10 V; I _D = -200 mA; V _{GS} = -5 V; T _j = 25 °C	-	0.09	-	nC
C _{iss}	input capacitance	V _{DS} = -25 V; f = 1 MHz; V _{GS} = 0 V;	-	24	36	pF
C _{oss}	output capacitance	T _j = 25 °C	-	4.5	-	pF
C _{rss}	reverse transfer capacitance		-	1.3	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = -30 V; R_{L} = 250 Ω ; V_{GS} = -10 V;	-	13	26	ns
t _r	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	11	-	ns
t _{d(off)}	turn-off delay time]	-	48	96	ns
t _f	fall time		-	25	-	ns
Source-drai	n diode					
V _{SD}	source-drain voltage	I _S = -115 mA; V _{GS} = 0 V; T _j = 25 °C	-0.48	-0.85	-1.2	V

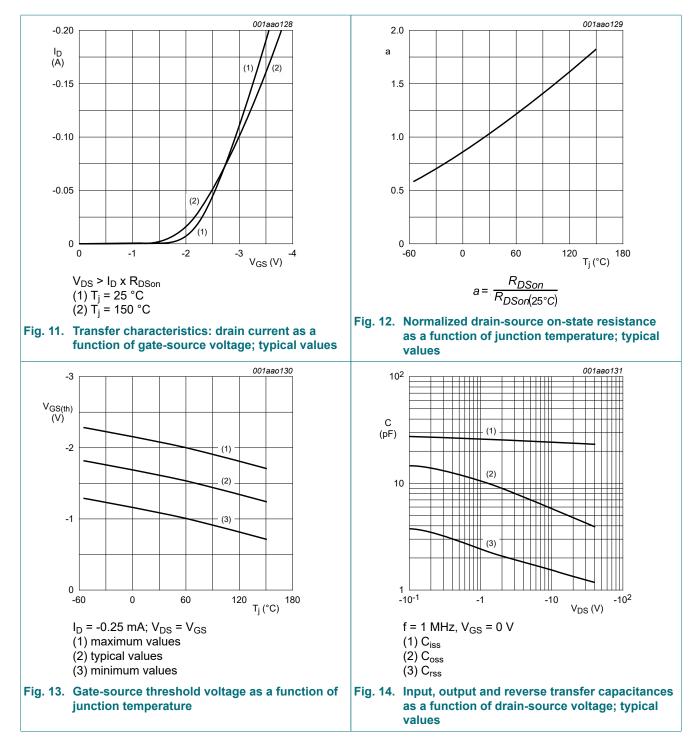
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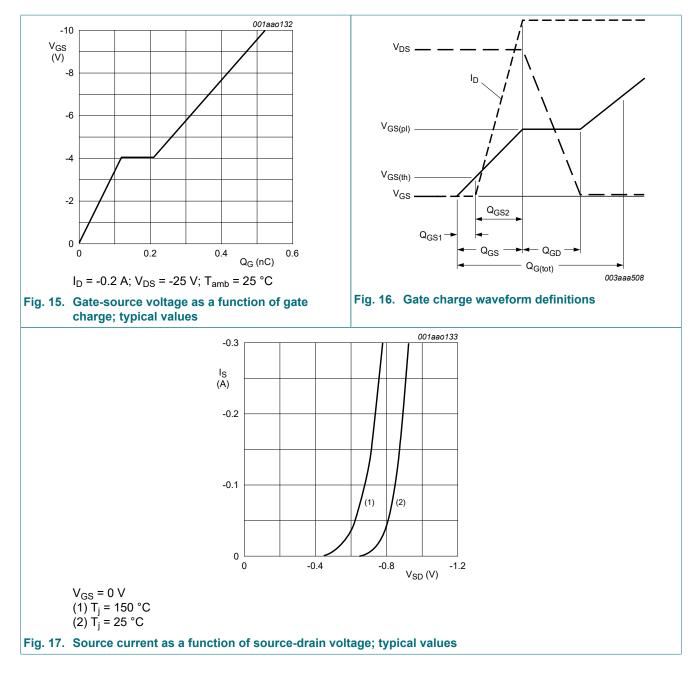


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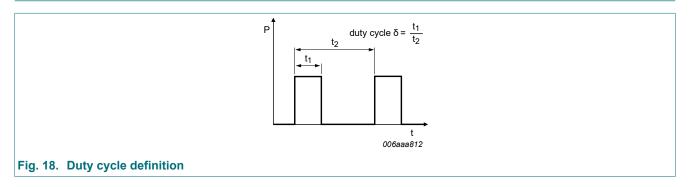
50 V, single P-channel Trench MOSFET



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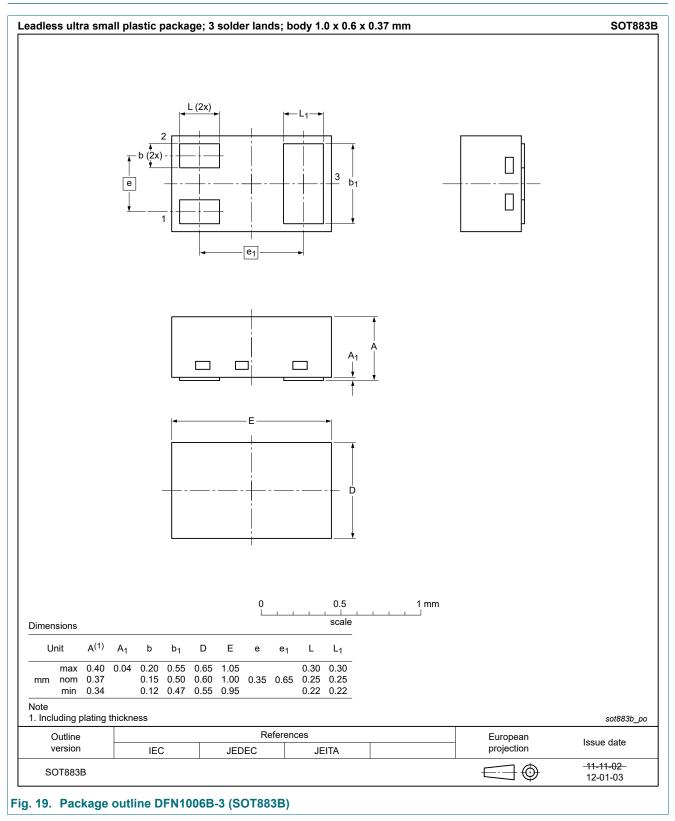
11. Test information



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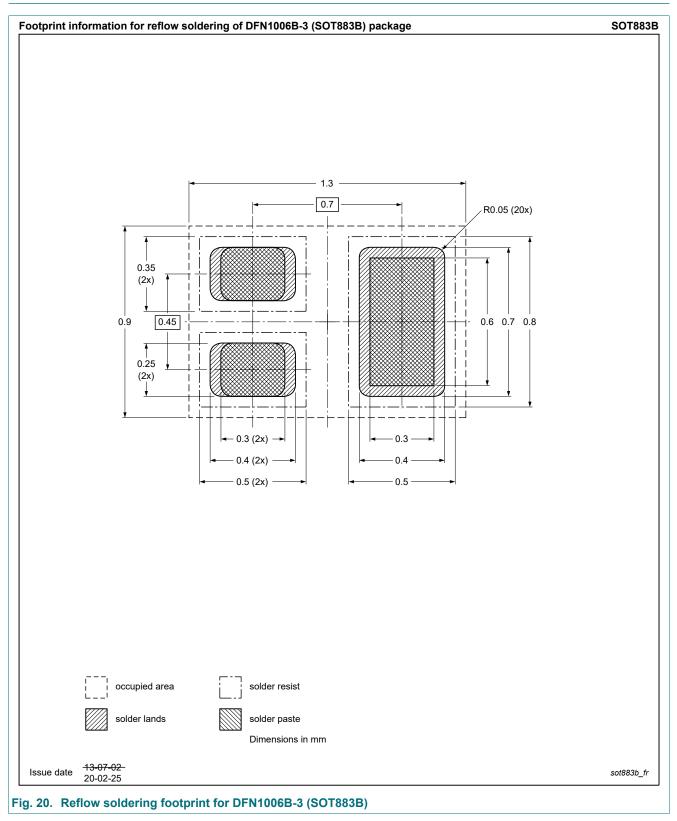
12. Package outline



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13. Soldering



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14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BSS84AKMB v.2	20201027	Product data sheet	-	BSS84AKMB v.1			
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 						
BSS84AKMB v.1	20120606	Product data sheet	-	-			

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Product data sheet

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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27 October 2020

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