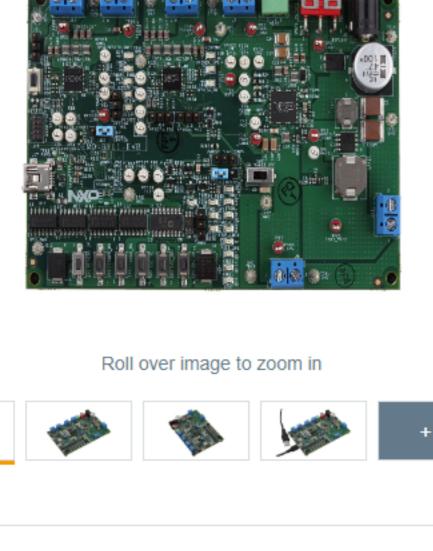




sequencing and safety features. The demonstrator kit comes with a USB cable used as a communication interface between computer and BYLink concept to have control on the full PMIC



Supported Devices | Features

WAKE1 - FS85

DESIGN FILES SOFTWARE

Safety

MCU

▶ VDDM_3.3 V

▶ V_UC_3.3 V

POR (for PGOOD)

PORST (for RESETB)
GPIO6

ADC_5 V ▶ VDDIO_3.3 V Г٦

Block Diagram

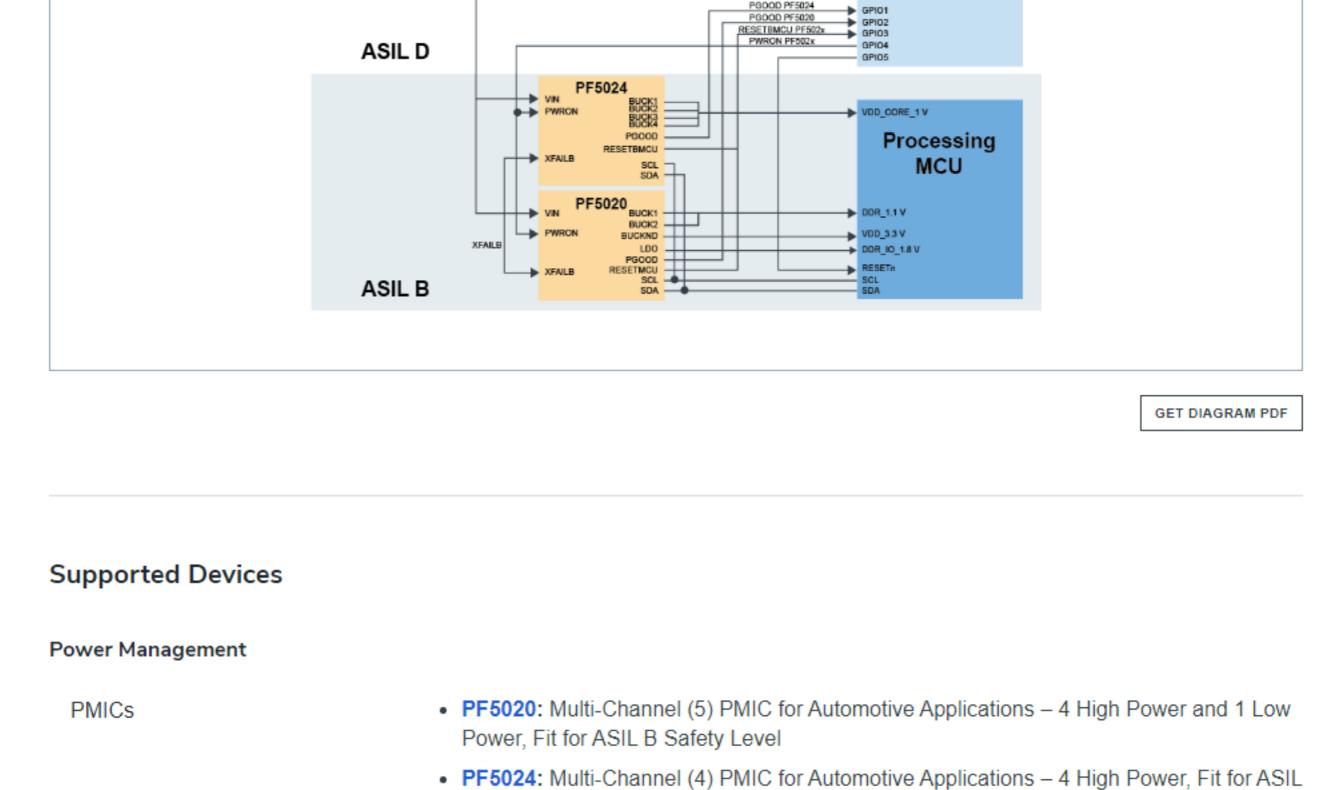
Product Details

BYLink Tool Block Diagram

Select a section:

Block Diagram

V_N - 12 V/24 V



B Safety Level

· BYLink concept including:

· USB com between PC to demo kit

I²C communication to PMIC devices

FS8500 device

PF5024 device

PF5020 device

· OTP emulation

DEMO-BYL1-EVB

BYLink Multiprocessor Demo Board .

FS8510

→ BUCK3

▶ LD01

→ BOOST

PGOOD

RSTB FOCU1/2 VPRE_4.1 V

PGOOD - FS85

RSTB

FSD8 Safety State

FS8400: Safety System Basis Chip for S32 Microcontrollers, Fit for ASIL B

• FS8500: Safety System Basis Chip for S32 Microcontrollers, Fit for ASIL D

Features

Topology

Features

Interfaces

Buy Options

Safety SBCs

 Power sequencing · Safety and fault management

Assembled and tested evaluation board in an anti-static bag 3.0 ft USB-STD A to USB-B mini cable · Jumpers mounted on board Click to expand

Kit contains

Quick start guide

NXP (3)

Design Resources

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Design Tools & Files Printed Circuit Boards and Schematics Design Files - miscellaneous

USER MANUAL UM11768, BYLink Multiprocessor Demo Board - User Manual PDF Rev 1 Apr 7, 2022 4.9 MB UM11768 English

3 documents **USER GUIDE** Getting Started with the DEMO-BYL1-EVB Evaluation Board GETTING STARTED HTML Apr 7, 2022 GS-DEMO-BYL1-EVB English **USER GUIDE**

2 design files

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1 software file

Sign in required

Sign in required

APPLICATION DEVELOPMENT TOOLS

BYLink Multiprocessor Demo GUI

DEMO-BYL1-EVB Schematics

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