MMBFJ177LT1G, SMMBFJ177LT1G

JFET Chopper

P-Channel - Depletion

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Gate Voltage	V_{DG}	-25	Vdc
Gate-Source Voltage	V _{GS}	25	Vdc

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

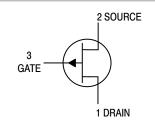
Total Device Dissipation FR-5 Board (Note 1) T _A = 25°C Derate above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	556	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

^{1.} FR-5 = $1.0 \times 0.75 \times 0.062$ in.



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SOT-23 (TO-236) CASE 318-08 STYLE 10

MARKING DIAGRAM



6Y = Specific Device Code

M = Date Code*

= Pb–Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBFJ177LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
SMMBFJ177LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MMBFJ177LT1G, SMMBFJ177LT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Chara	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Gate-Source Breakdown Voltage (V _{DS} =	0, I _D = 1.0 μAdc)	V _{(BR)GSS}	30	-	Vdc
Gate Reverse Current (V _{DS} = 0 Vdc, V _G	_S = 20 Vdc)	I _{GSS}	-	1.0	nAdc
Gate Source Cutoff Voltage (V _{DS} = −15 V	/dc, I _D = -10 nAdc)	V _{GS(off)}	0.8	2.5	Vdc
ON CHARACTERISTICS					
Zero-Gate-Voltage Drain Current (V _{GS} =	= 0, V _{DS} = -15 Vdc) (Note 2)	I _{DSS}	-1.5	-20	mAdc
Drain Cutoff Current (V _{DS} = −15 Vdc, V _G	S = 10 Vdc)	I _{D(off)}	-	-1.0	nAdc
Drain Source On Resistance (I _D = −500	uAdc)	r _{DS(on)}	-	300	Ω
Input Capacitance	V _{DS} = 0, V _{GS} = 10 Vdc	C _{iss}	-	11	pF
Reverse Transfer Capacitance	f = 1.0 MHz	C _{rss}	_	5.5	1

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 2. Pulse Test: Pulse Width $< 300 \ \mu s$, Duty Cycle $\le 2\%$.

TYPICAL CHARACTERISTICS

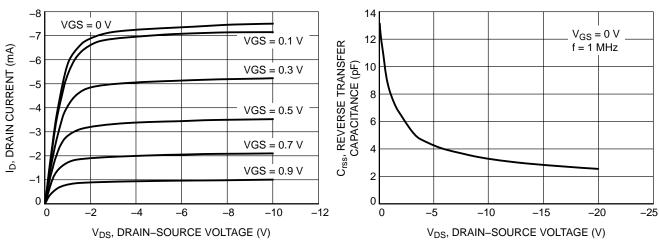


Figure 1. Drain Current vs. Drain-Source Voltage

Figure 2. Reverse Transfer Capacitance

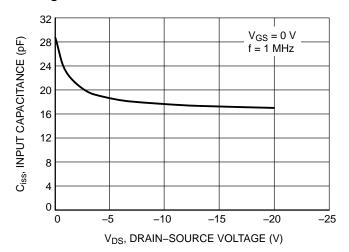


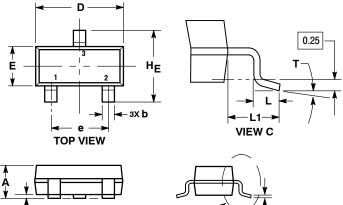
Figure 3. Input Capacitance



SOT-23 (TO-236) CASE 318-08 **ISSUE AS**

DATE 30 JAN 2018

SCALE 4:1



SEE VIEW C

END VIEW

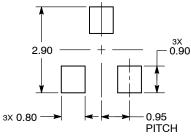
NOTES:

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
 MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	М	ILLIMETE	RS		INCHES	
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
T	0°		10°	0°		10°

RECOMMENDED SOLDERING FOOTPRINT

SIDE VIEW



2. ANODE

STYLE 27: PIN 1. CATHODE 2. CATHODE

3. CATHODE

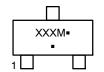
DIMENSIONS: MILLIMETERS

2. SOURCE

STYLE 28: PIN 1. ANODE 2. ANODE

3. ANODE

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

STYLE 13: PIN 1. SOURCE

2. DRAIN

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE	ON
STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12:	
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. CATHODE	

3. CATHODE	3. GATE	3. CATHODE-ANODE	3. ANODE	3. GATE	3. ANODE
STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20:
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	PIN 1. CATHODE	PIN 1. CATHODE

2. CATHODE

2. CATHODE

PIN 1.	GATE	PIN 1.	ANODE	PIN 1.	NO CONNECTION	PIN 1.	NO CONNECTION	PIN 1.	CATHODE	PIN 1.	CATHODE
2.	CATHODE	2.	CATHODE	2.	ANODE	2.	CATHODE	2.	ANODE	2.	ANODE
3.	ANODE	3.	CATHODE	3.	CATHODE	3.	ANODE	3.	CATHODE-ANODE	3.	GATE

STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
SOURCE	OUTPUT	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
DRAIN	INPUT	CATHODE	3. SOURCE	3. GATE	NO CONNECTION

Γ		SOT-23 (TO-236)		PAGE 1 OF 1
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STYLE 14: PIN 1. CATHODE

2. GATE

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