

ECOSPARK® Ignition IGBT

300 mJ, 400 V, N-Channel Ignition IGBT

ISL9V3040x3ST-F085C

Features

- SCIS Energy = 300 mJ at $T_J = 25^{\circ}C$
- Logic Level Gate Drive
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Applications

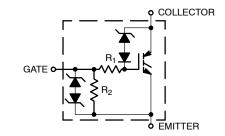
- Automotive Ignition Coil Driver Circuits
- High Current Ignition System
- Coil on Plug Application

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Symbol	Parameter	Value	Unit
BV _{CER}	Collector to Emitter Breakdown Voltage (IC = 1 mA)	400	V
BV _{ECS}	Emitter to Collector Voltage – Reverse Battery Condition (IC = 10 mA)	24	V
E _{SCIS25}	ISCIS = 14.2 A, L = 3.0 mHy, RGE = 1 K Ω , T $_{\rm C}$ = 25°C (Note 1)	300	mJ
E _{SCIS150}	ISCIS = 10.6 A, L = 3.0 mHy, RGE = 1 K Ω , T _C = 150°C (Note 2)	170	mJ
IC25	Collector Current Continuous at VGE = 4.0 V, T _C = 25°C	21	Α
IC110	Collector Current Continuous at VGE = 4.0 V, T _C = 110°C	17	Α
V_{GEM}	Gate to Emitter Voltage Continuous	±10	V
PD	Power Dissipation Total, T _C = 25°C	150	W
	Power Dissipation Derating, $T_C > 25^{\circ}C$	1	W/°C
T _J , T _{STG}	Operating Junction and Storage Temperature	-55 to +175	°C
T _L	Lead Temperature for Soldering Purposes (1/8" from case for 10 s)	300	°C
T _{PKG}	Reflow Soldering according to JESD020C	260	°C
ESD	HBM–Electrostatic Discharge Voltage at 100 pF, 1500 Ω	4	kV
	CDM–Electrostatic Discharge Voltage at 1 Ω	2	kV

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Self clamped inductive Switching Energy (ESCIS25) of 300 mJ is based on the test conditions that is starting T_J = 25°C, L = 3 mHy, ISCIS = 14.2 A, VCC = 100 V during inductor charging and VCC = 0 V during time in clamp.
- Self Clamped inductive Switching Energy (ESCIS150) of 170 mJ is based on the test conditions that is starting T_J = 150°C, L = 3mHy, ISCIS = 10.6 A, VCC = 100 V during inductor charging and VCC = 0 V during time in clamp.

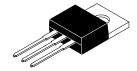






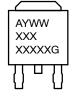
DPAK3 CASE 369AS

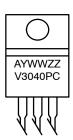
D²PAK-3 CASE 418AJ



TO-220-3LD CASE 340AT

MARKING DIAGRAMS





A = Assembly Location

 Y
 = Year

 WW
 = Work Week

 XXXX
 = Device Code

 G
 = Pb-Free Package

 ZZ
 = Assembly Lot Number

V3040PC = Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 7 of this data sheet.

THERMAL RESISTANCE RATINGS

Characteristic	Symbol	Max	Units
Junction-to-Case - Steady State (Drain)		1	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Symbol	Parameter	Test Conditions		Min	Тур.	Max.	Units
OFF CHARA	ACTERISTICS						
BV _{CER}	Collector to Emitter Breakdown Voltage	I_{CE} = 2 mA, V_{GE} = 0 V, R_{GE} = 1 k Ω , T_{J} = -40 to 150°C		370	400	430	V
BV _{CES}	Collector to Emitter Breakdown Voltage	I _{CE} = 10 mA, V _{GE} = 0 V, R _{GE} = 0, T _J = -40 to 150°C		390	420	450	V
BV _{ECS}	Emitter to Collector Breakdown Voltage	$I_{CE} = -75 \text{ mA}, V_{GE} = 0 \text{ V},$ $T_{J} = 25^{\circ}\text{C}$		30	_	_	V
BV _{GES}	Gate to Emitter Breakdown Voltage	I _{GES} = ±2 mA		±12	±14	-	V
I _{CER}	Collector to Emitter Leakage Current	V _{CE} = 175 V	T _J = 25°C	-	_	25	μΑ
		$R_{GE} = 1 k\Omega$	T _J = 150°C	-	-	1	mA
I _{ECS}	Emitter to Collector Leakage Current	V _{EC} = 24 V	T _J = 25°C	-	_	1	mA
			T _J = 150°C	-	_	40	1
R ₁	Series Gate Resistance		•	-	70	-	Ω
R ₂	Gate to Emitter Resistance			10K	-	26K	Ω
ON CHARA	CTERISTICS						
V _{CE(SAT)}	Collector to Emitter Saturation Voltage	I _{CE} = 6 A, V _{GE} = 4 V, T _J = 25°C		-	1.25	1.65	V
V _{CE(SAT)}	Collector to Emitter Saturation Voltage	I _{CE} = 10 A, V _{GE} = 4.5 V, T _J = 150°C		-	1.58	1.80	V
V _{CE(SAT)}	Collector to Emitter Saturation Voltage	I _{CE} = 15 A, V _{GE} = 4.5 V, T _J = 150°C		-	1.90	2.20	V
DYNAMIC C	HARACTERISTICS	•		•	•	•	•
Q _{G(ON)}	Gate Charge	I _{CE} = 10 A, V _{CE} = 12 V, V _{GE} = 5 V		-	17	_	nC
V _{GE(TH)}	Gate to Emitter Threshold Voltage	I _{CE} = 1 mA	T _J = 25°C	1.3	-	2.2	V
		V _{CE} = V _{GE}	T _J = 150°C	0.75	-	1.8	1
V_{GEP}	Gate to Emitter Plateau Voltage	V _{CE} = 12 V, I _{CE} = 10 A		-	3.0	-	V
SWITCHING	CHARACTERISTICS						
td _{(ON)R}	Current Turn-On Delay Time-Resistive	$V_{CE} = 14 \text{ V}, R_L = 1 \Omega, \\ V_{GE} = 5 \text{ V}, R_G = 470 \Omega, \\ T_J = 25^{\circ}\text{C}$		_	0.7	4	μs
t _{rR}	Current Rise Time-Resistive			-	2.1	7	1
td _{(OFF)L}	Current Turn-Off Delay Time-Inductive	$V_{CE} = 300 \text{ V, L} = 1 \text{ mH,}$ $V_{GE} = 5 \text{ V, R}_{G} = 470 \Omega,$ $I_{CE} = 6.5 \text{ A, T}_{J} = 25^{\circ}\text{C}$		_	4.8	15	
t _{fL}	Current Fall Time-Inductive			-	2.8	15	1

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

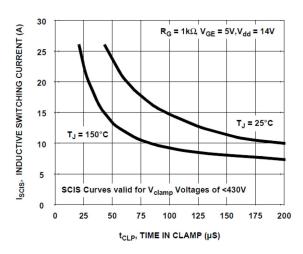


Figure 1. Self Clamped Inductive Switching Current vs. Time in Clamp

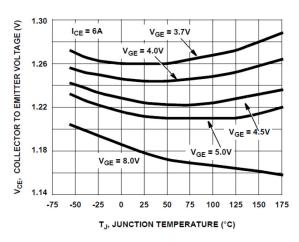


Figure 3. Collector to Emitter On–State Voltage vs. Junction Temperature

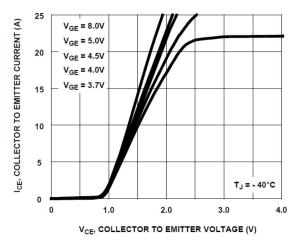


Figure 5. Collector to Emitter On-State Voltage vs. Collector Current

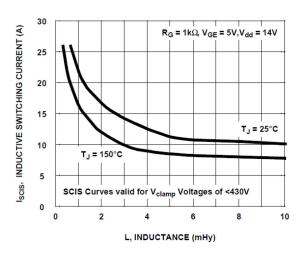


Figure 2. Self Clamped Inductive Switching Current vs. Inductance

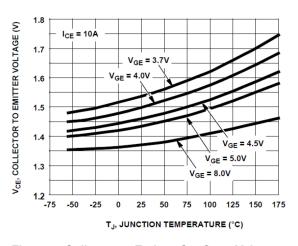


Figure 4. Collector to Emitter On–State Voltage vs. Junction Temperature

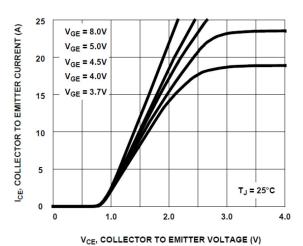


Figure 6. Collector to Emitter On-State Voltage vs. Collector Current

TYPICAL CHARACTERISTICS (continued)

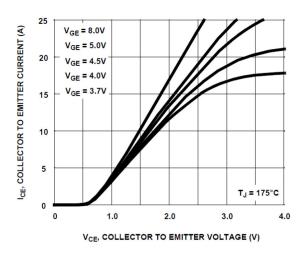


Figure 7. Collector to Emitter On-State Voltage vs. Collector Current

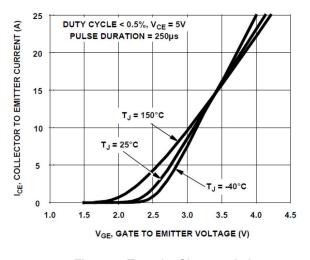


Figure 8. Transfer Characteristics

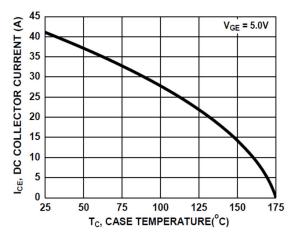


Figure 9. DC Collector Current vs. Case Temperature

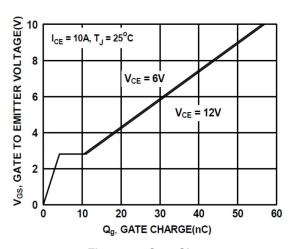


Figure 10. Gate Charge

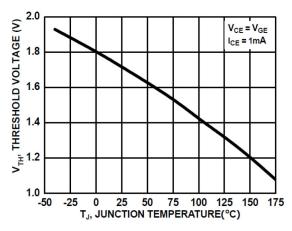


Figure 11. Threshold Voltage vs. Junction Temperature

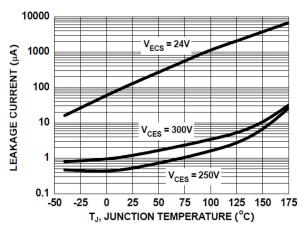
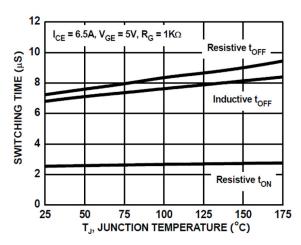


Figure 12. Leakage Current vs. Junction Temperature

TYPICAL CHARACTERISTICS (continued)



2000 f = 1MHz $V_{GE} = 0V$ 1600 CAPACITANCE (pF) CIES 1200 800 $\mathsf{C}_{\mathsf{RES}}$ 400 COES 0 15 20 25 10 V_{DS}, DRAIN TO SOURCE VOLTAGE (V)

Figure 13. Switching Time vs. Junction Temperature

Figure 14. Capacitance vs. Collector to Emitter

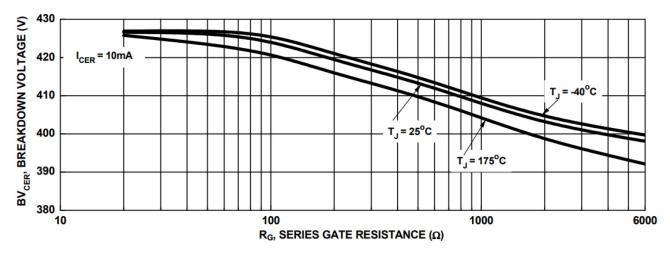


Figure 15. Break Down Voltage vs. Series Resistance

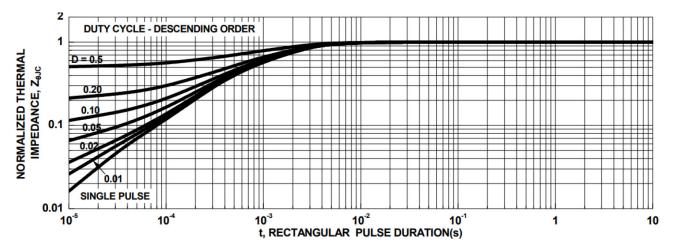


Figure 16. IGBT Normalized Transient Thermal Impedance, Junction to Case

TEST CIRCUIT AND WAVEFORMS

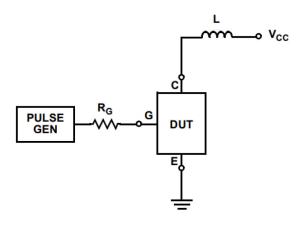


Figure 17. Inductive Switching Test Circuit

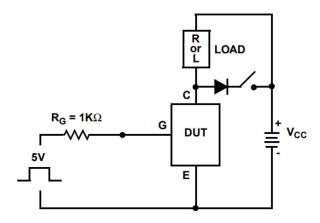


Figure 18. t_{ON} and t_{OFF} Switching Test Circuit

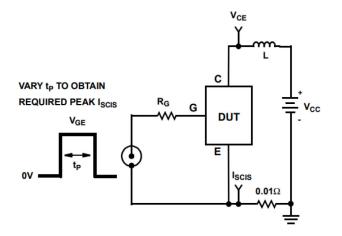


Figure 19. Energy Test Circuit

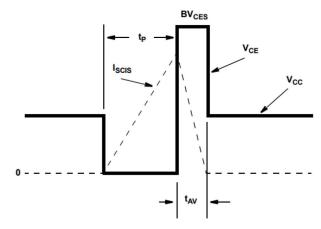


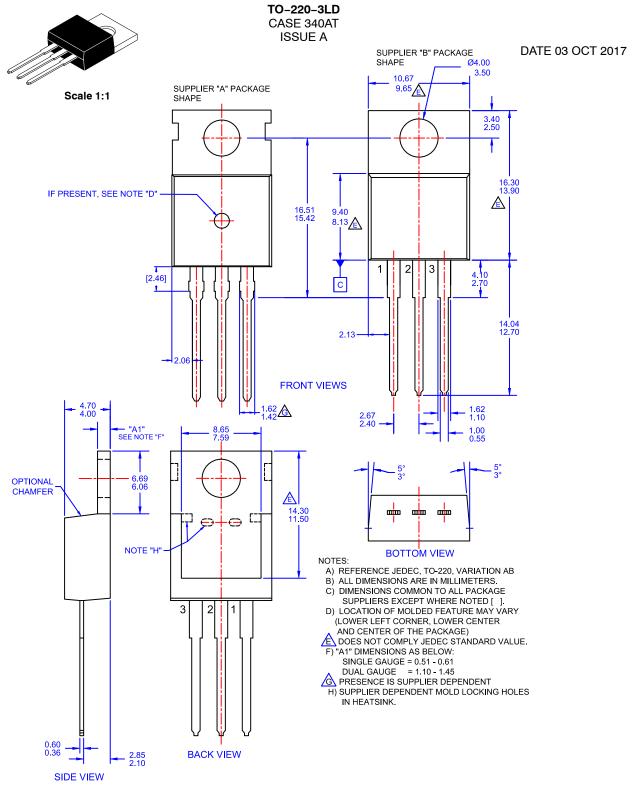
Figure 20. Energy Waveforms

PACKAGE MARKING AND ORDERING INFORMATION

Device	Package	Shipping [†]
ISL9V3040D3ST-F085C	DPAK (Pb-Free)	2500 Units/Tape & Reel
ISL9V3040S3ST-F085C	D2PAK (Pb-Free)	800 Units/Tape & Reel
ISL9V3040P3-F085C	TO220 (Pb-Free)	50 Units/Tube

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

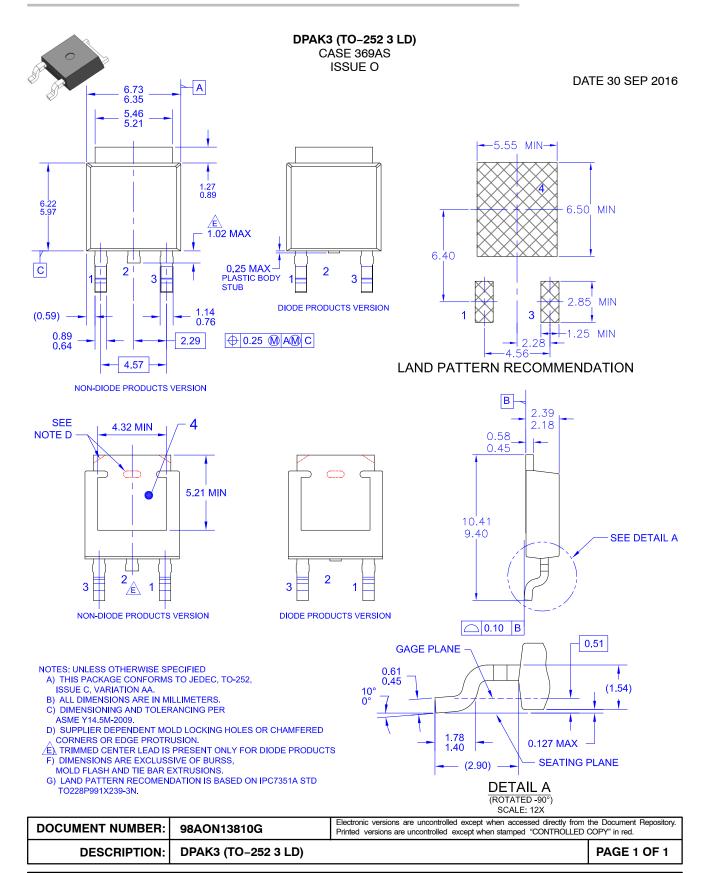
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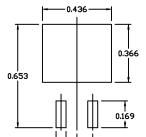
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D²PAK-3 (TO-263, 3-LEAD) CASE 418AJ ISSUE F

DATE 11 MAR 2021



RECOMMENDED MOUNTING FOOTPRINT

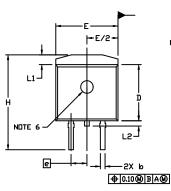
0.100 PITCH

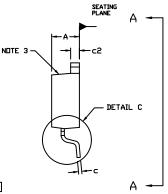
For additional information on our Pb-Free strategy and soldering details, please download the IN Seniconductor Soldering and Mounting Techniques Reference Manual, SILIERRM/D.

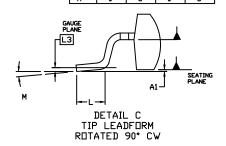
NOTES

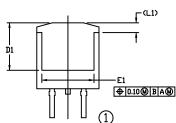
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: INCHES
- 3. CHAMFER OPTIONAL.
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.005 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
- THERMAL PAD CONTOUR IS OPTIONAL WITHIN DIMENSIONS E, L1, D1, AND E1.
- 6. OPTIONAL MOLD FEATURE.
- 7. ①,② ... DPTIONAL CONSTRUCTION FEATURE CALL DUTS.

	INCHES		MILLIMETERS	
DIM	MIN.	MAX.	MIN.	MAX.
Α	0.160	0.190	4.06	4.83
A1	0.000	0.010	0.00	0.25
b	0.020	0.039	0.51	0.99
С	0.012	0.029	0.30	0.74
c2	0.045	0.065	1.14	1.65
D	0.330	0.380	8.38	9.65
D1	0.260		6.60	
E	0.380	0.420	9.65	10.67
E1	0.245		6.22	
e	0.100 BSC		2.54	BSC
Н	0.575	0.625	14.60	15.88
L	0.070	0.110	1.78	2.79
L1		0.066		1.68
L5		0.070		1.78
L3	0.010 BSC		0.25	BSC
м	0+	8*	n•	8.

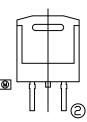


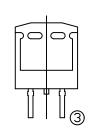


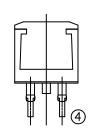




VIEW A-A







VIEW A-A

OPTIONAL CONSTRUCTIONS

GENERIC MARKING DIAGRAMS*

XX
XX
XXXXXXXX
AWLYWWG
AYWW
AYWW
AKA

IC Standard Rectifier SSG

XXXXXX = Specific Device Code A = Assembly Location

 WL
 = Wafer Lot

 Y
 = Year

 WW
 = Work Week

 W
 = Week Code (SSG)

 M
 = Month Code (SSG)

 G
 = Pb-Free Package

 AKA
 = Polarity Indicator

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present. Some products may not follow the Generic Marking.

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