74AHC1G04; 74AHCT1G04

Inverter

Rev. 11 — 11 January 2022

Product data sheet

1. General description

The 74AHC1G04; 74AHCT1G04 is a single inverter. Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

The AHC device has CMOS input switching levels and supply voltage range 2 V to 5.5 V.

The AHCT device has TTL input switching levels and supply voltage range 4.5 V to 5.5 V.

2. Features and benefits

- Wide supply voltage range from 2.0 to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- · High noise immunity
- · CMOS low power dissipation
- · Latch-up performance exceeds 100 mA per JESD 78 Class II Level A
- · Symmetrical output impedance
- · Balanced propagation delays
- · Input levels:
 - For 74AHC1G04: CMOS level
 - For 74AHCT1G04: TTL level
- · Multiple package options
- ESD protection:
 - HBM JESD22-A114E: exceeds 2000 V
 - MM JESD22-A115-A: exceeds 200 V
 - CDM JESD22-C101C: exceeds 1000 V
- Specified from -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AHC1G04GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads;	SOT353-1
74AHCT1G04GW			body width 1.25 mm	
74AHC1G04GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753
74AHCT1G04GV	-			
74AHC1G04GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package;	SOT886
74AHCT1G04GM			no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	



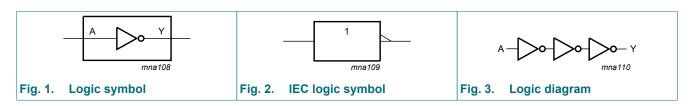
4. Marking

Table 2. Marking codes

Type number	Marking [1]
74AHC1G04GW	AC
74AHCT1G04GW	CC
74AHC1G04GV	A04
74AHCT1G04GV	C04
74AHC1G04GM	AC
74AHCT1G04GM	CC

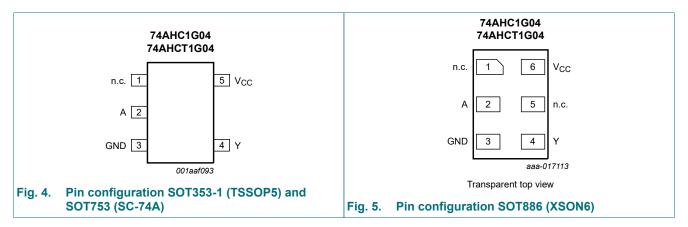
^[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1. Pinning



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Product data sheet

6.2. Pin description

Table 3. Pin description

Symbol	Pin		Description
	SOT353-1 and SOT753	SOT886	
n.c.	1	1	not connected
А	2	2	data input
GND	3	3	ground (0 V)
Υ	4	4	data output
n.c.	-	5	not connected
V _{CC}	5	6	supply voltage

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level

Input	Output
Α	Υ
L	Н
Н	L

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CC}	supply voltage		-0.5	+7.0	V
VI	input voltage		-0.5	+7.0	V
I _{IK}	input clamping current	V _I < -0.5 V	-20	-	mA
I _{OK}	output clamping current	$V_O < -0.5 \text{ V or } V_O > V_{CC} + 0.5 \text{ V}$ [1]	-	±20	mA
Io	output current	$-0.5 \text{ V} < \text{V}_{\text{O}} < \text{V}_{\text{CC}} + 0.5 \text{ V}$	-	±25	mA
I _{CC}	supply current		-	75	mA
I _{GND}	ground current		-75	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 ^{\circ}\text{C} \text{ to } +125 ^{\circ}\text{C}$ [2]	-	250	mW

^[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

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^[2] For SOT353-1 (TSSOP5) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C. For SOT753 (SC-74A) package: P_{tot} derates linearly with 3.8 mW/K above 85 °C.

For SOT886 (XSON6) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	74AHC1G04			74	Unit		
			Min	Тур	Max	Min	Тур	Max	
V _{CC}	supply voltage		2.0	5.0	5.5	4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	0	-	5.5	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and	V _{CC} = 3.3 V ± 0.3 V	-	-	100	-	-	-	ns/V
	fall rate	V _{CC} = 5.0 V ± 0.5 V	-	-	20	-	-	20	ns/V

10. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	25 °C				°C to 5 °C		°C to 5 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
74AHC1	G04									
V _{IH}	HIGH-level	V _{CC} = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
	input voltage	V _{CC} = 3.0 V	2.1	-	-	2.1	-	2.1	-	V
		V _{CC} = 5.5 V	3.85	-	-	3.85	-	3.85	-	V
V _{IL}	LOW-level	V _{CC} = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
	input voltage	V _{CC} = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
		V _{CC} = 5.5 V	-	-	1.65	-	1.65	-	1.65	V
V _{OH}	HIGH-level	V _I = V _{IH} or V _{IL}								
	output voltage	I _O = -50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -50 μA; V _{CC} = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I _O = -50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		$I_O = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.58	-	-	2.48	-	2.40	-	V
		I_{O} = -8.0 mA; V_{CC} = 4.5 V	3.94	-	-	3.8	-	3.70	-	V
V _{OL}	LOW-level	V _I = V _{IH} or V _{IL}								
	output voltage	I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		$I_O = 4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
		$I_O = 8.0 \text{ mA}; V_{CC} = 4.5 \text{ V}$	-	-	0.36	-	0.44	-	0.55	V
l _l	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μΑ
Cı	input capacitance		-	1.5	10	-	10	-	10	pF

Symbol	Parameter	Conditions		25 °C		-40 °C to +85 °C			°C to 5 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
74AHCT	1G04									
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	-	-	2.0	-	2.0	-	V
V_{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	-	0.8	-	0.8	-	0.8	V
V _{OH}	HIGH-level	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$								
	output voltage	Ι _Ο = -50 μΑ	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -8.0 mA	3.94	-	-	3.8	-	3.70	-	V
V_{OL}	LOW-level	$V_I = V_{IH}$ or V_{IL} ; $V_{CC} = 4.5 \text{ V}$								
	output voltage	Ι _Ο = 50 μΑ	-	0	0.1	-	0.1	-	0.1	V
		I _O = 8.0 mA	-	-	0.36	-	0.44	-	0.55	V
l _l	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	-	10	-	40	μA
ΔI _{CC}	additional supply current	per input pin; $V_I = 3.4 \text{ V}$; other inputs at V_{CC} or GND; $I_O = 0 \text{ A}$; $V_{CC} = 5.5 \text{ V}$	-	-	1.35	-	1.5	-	1.5	mA
Cı	input capacitance		-	1.5	10	-	10	-	10	pF

11. Dynamic characteristics

Table 8. Dynamic characteristics

GND = 0 V; $t_r = t_f = \le 3.0$ ns. For test circuit see Fig. 7.

Symbol	Parameter	Conditions		25 °C			°C to 5 °C		°C to 5 °C	Unit
			Min	Тур	Max	Min	Max	Min	Max	
74AHC1	G04									
t _{pd}	propagation	A to Y; see <u>Fig. 6</u> [1]								
	delay	$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V; } C_L = 15 \text{ pF}$ [2]	-	4.3	7.1	1.0	8.5	1.0	11.0	ns
		V _{CC} = 3.0 V to 3.6 V; C _L = 50 pF [2]	-	6.1	10.6	1.0	12	1.0	14.5	ns
		V _{CC} = 4.5 V to 5.5 V; C _L = 15 pF [3]	-	3.1	5.5	1.0	6.5	1.0	7.0	ns
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V; } C_L = 50 \text{ pF}$ [3]	-	4.5	7.5	1.0	8.5	1.0	9.5	ns
C _{PD}	power dissipation capacitance	per buffer; $C_L = 50 \text{ pF}$; $f = 1 \text{ MHz}$; [4] $V_I = GND \text{ to } V_{CC}$	-	15	-	-	-	-	-	pF

Symbol	Parameter	Conditions			25 °C		_	°C to 5 °C		°C to 5 °C	Unit
				Min	Тур	Max	Min	Max	Min	Max	
74AHCT	1G04				•			•		•	
t _{pd}	propagation	A to Y; see Fig. 6	[1]								
	delay	V _{CC} = 4.5 V to 5.5 V; C _L = 15 pF	[3]	-	3.4	6.7	1.0	7.5	1.0	8.5	ns
		V _{CC} = 4.5 V to 5.5 V; C _L = 50 pF	[3]	-	4.9	7.7	1.0	8.5	1.0	10.0	ns
C _{PD}	power dissipation capacitance	per buffer; C_L = 50 pF; f = 1 MHz; V_I = GND to V_{CC}	[4]	-	16	-	-	-	-	-	pF

- t_{pd} is the same as t_{PLH} and t_{PHL} .
- Typical values are measured at V_{CC} = 3.3 V.
- Typical values are measured at $V_{CC} = 5.0 \text{ V}$. C_{PD} is used to determine the dynamic power dissipation P_D (μ W). $P_D = C_{PD} \times V_{CC}^2 \times f_i + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz; f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in Volts;

N = total load switching outputs;

 $\Sigma(C_L \times V_{CC}^2 \times f_0) = \text{sum of outputs.}$

11.1. Waveforms and test circuit

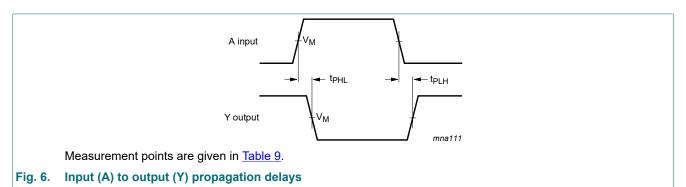
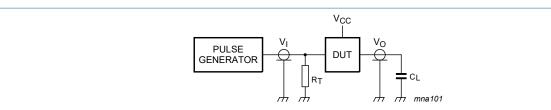


Table 9. Measurement point

Туре	Input	Input	Output
	V _I	V _M	V _M
74AHC1G04	GND to V _{CC}	0.5 × V _{CC}	0.5 × V _{CC}
74AHCT1G04	GND to 3.0 V	1.5 V	0.5 × V _{CC}



Test data is given in Table 8. Definitions for test circuit:

C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to output impedance Z_0 of the pulse generator.

Fig. 7. Test circuit for measuring switching times

12. Package outline

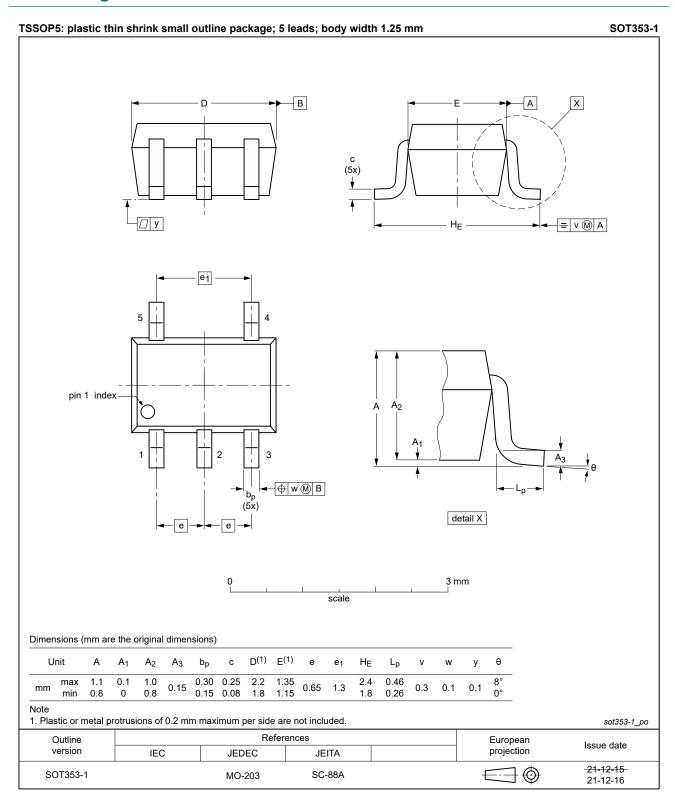


Fig. 8. Package outline SOT353-1 (TSSOP5)

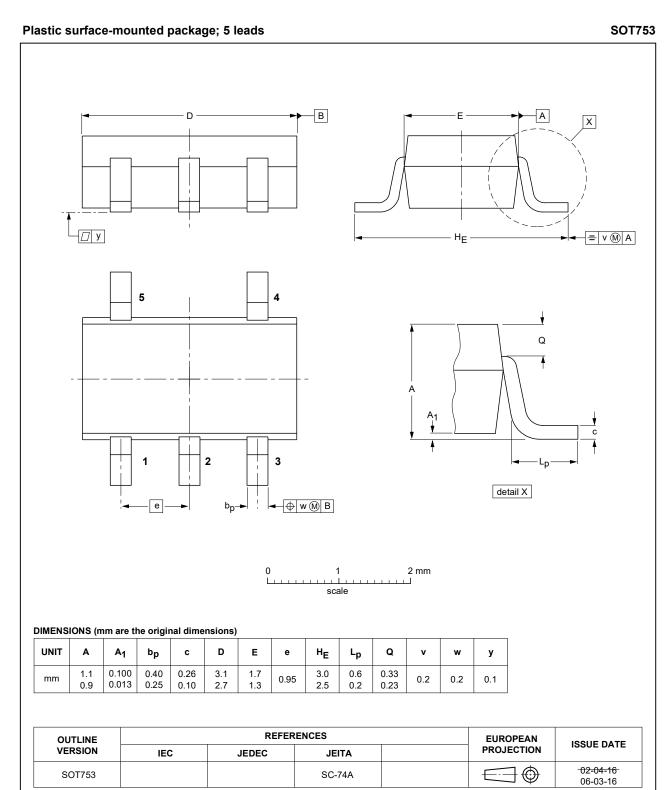


Fig. 9. Package outline SOT753 (SC-74A)

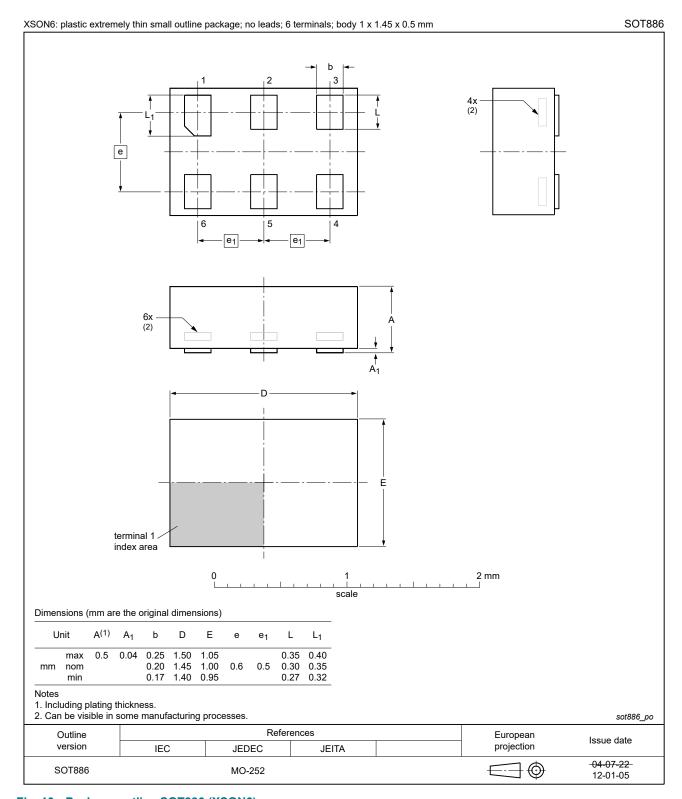


Fig. 10. Package outline SOT886 (XSON6)

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13. Abbreviations

Table 10. Abbreviations

Acronym	Description
CDM	Charged Device Model
CMOS	Complementary Metal-Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
НВМ	Human Body Model
MM	Machine Model
TTL	Transistor-Transistor Logic

14. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes	
74AHC_AHCT1G04 v.11	20220111	Product data sheet	-	74AHC_AHCT1G04 v.10	
Modifications:	 Section 1 and Section 2 updated. Fig. 8: Package outline drawing SOT353-1 (TSSOP5) has changed. 				
74AHC_AHCT1G04 v.10	20190924	Product data sheet	-	74AHC_AHCT1G04 v.9	
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Table 5: Derating values for P_{tot} total power dissipation have been updated. 				
74AHC_AHCT1G04 v.9	20150310	Product data sheet	-	74AHC_AHCT1G04 v.8	
Modifications:	Added type numbers 74AHC1G04GM and 74AHCT1G04GM.				
74AHC_AHCT1G04 v.8	20141106	Product data sheet	-	74AHC_AHCT1G04 v.7	
Modifications:	<u>Section 4</u> : table note added.				
74AHC_AHCT1G04 v.7	20070531	Product data sheet	-	74AHC_AHCT1G04 v.6	
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. Package SOT353 changed to SOT353-1 in <u>Table 1</u> and <u>Section 12</u>. Quick reference data and Soldering sections removed. 				
74AHC_AHCT1G04 v.6	20030904	Product specification	-	74AHC_AHCT1G04 v.5	
74AHC_AHCT1G04 v.5	20020527	Product specification	-	74AHC_AHCT1G04 v.4	
74AHC_AHCT1G04 v.4	20020215	Product specification	-	74AHC_AHCT1G04 v.3	
74AHC_AHCT1G04 v.3	20010131	Product specification	-	74AHC_AHCT1G04 v.2	
74AHC_AHCT1G04 v.2	19990127	Product specification	-	74AHC_AHCT1G04_N v.1	
74AHC_AHCT1G04_N v.1	19981125	Preliminary specification	-	-	

Product data sheet

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
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74AHC_AHCT1G04

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