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PIR-GEVB: PIR (Passive Infrared) Shield Evaluation Board

The PIR Shield Evaluation Board uses the NCS36000, a fully integrated mixed-signal CMOS device designed for low-cost passive infrared controlling applications, with the PCA9655E's 16 bits of General Purpose parallel Input and Output (GPIO) expansion through the I2C-bus and SMBus to create a functional passive infrared sensor evaluation board.



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Techni	cal Do	cuments					

Туре	Document Title	Document ID/Size	Rev			
Eval Board: BOM	PIR-GEVB Bill of Materials (ROHS Compliant)	PIR-GEVB_BOM_ROHS - 28 KB	1			
Eval Board: Gerber	PIR-GEVB Gerber Layout Files (Zip Format)	PIR-GEVB_GERBER - 1024 KB	0			
Eval Board: Schematic	PIR-GEVB Schematic	PIR-GEVB_SCHEMATIC - 14 KB	1			

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