

ON Semiconductor

Is Now

onsemi™

To learn more about onsemi™, please visit our website at
www.onsemi.com

onsemi and **onsemi** and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi** product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner. Other names and brands may be claimed as the property of others.



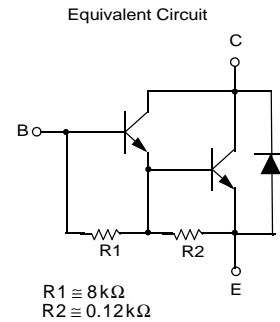
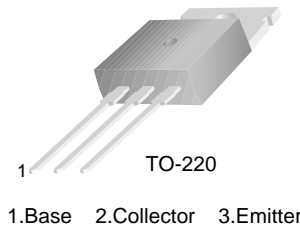
ON Semiconductor®

TIP120 / TIP121 / TIP122

NPN Epitaxial Darlington Transistor

Features

- Medium Power Linear Switching Applications
- Complementary to TIP125 / TIP126 / TIP127



Ordering Information

Part Number	Top Mark	Package	Packing Method
TIP120	TIP120	TO-220 3L (Single Gauge)	Bulk
TIP120TU	TIP120	TO-220 3L (Single Gauge)	Rail
TIP121	TIP121	TO-220 3L (Single Gauge)	Bulk
TIP121TU	TIP121	TO-220 3L (Single Gauge)	Rail
TIP122	TIP122	TO-220 3L (Single Gauge)	Bulk
TIP122TU	TIP122	TO-220 3L (Single Gauge)	Rail

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V_{CBO}	Collector-Base Voltage	TIP120	60
		TIP121	80
		TIP122	100
V_{CEO}	Collector-Emitter Voltage	TIP120	60
		TIP121	80
		TIP122	100
V_{EBO}	Emitter-Base Voltage	5	V
I_C	Collector Current (DC)	5	A
I_{CP}	Collector Current (Pulse)	8	A
I_B	Base Current (DC)	120	mA
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-65 to 150	$^\circ\text{C}$

TIP120 / TIP121 / TIP122 — NPN Epitaxial Darlington Transistor

Thermal Characteristics

Values are at $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
P_C	Collector Dissipation ($T_A = 25^\circ\text{C}$)	2	W
	Collector Dissipation ($T_C = 25^\circ\text{C}$)	65	

Electrical Characteristics

Values are at $T_C = 25^\circ\text{C}$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
$V_{CEO(sus)}$	Collector-Emitter Sustaining Voltage	TIP120	$I_C = 100\text{ mA}, I_B = 0$	60	V
		TIP121		80	
		TIP122		100	
I_{CEO}	Collector Cut-Off Current	TIP120	$V_{CE} = 30\text{ V}, I_B = 0$	0.5	mA
		TIP121	$V_{CE} = 40\text{ V}, I_B = 0$	0.5	
		TIP122	$V_{CE} = 50\text{ V}, I_B = 0$	0.5	
I_{CBO}	Collector Cut-Off Current	TIP120	$V_{CB} = 60\text{ V}, I_E = 0$	0.2	mA
		TIP121	$V_{CB} = 80\text{ V}, I_E = 0$	0.2	
		TIP122	$V_{CB} = 100\text{ V}, I_E = 0$	0.2	
I_{EBO}	Emitter Cut-Off Current	$V_{EB} = 5\text{ V}, I_C = 0$		2	mA
h_{FE}	DC Current Gain ⁽¹⁾	$V_{CE} = 3\text{ V}, I_C = 0.5\text{ A}$	1000		
		$V_{CE} = 3\text{ V}, I_C = 3\text{ A}$	1000		
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage ⁽¹⁾	$I_C = 3\text{ A}, I_B = 12\text{ mA}$		2.0	V
		$I_C = 5\text{ A}, I_B = 20\text{ mA}$		4.0	
$V_{BE(on)}$	Base-Emitter On Voltage ⁽¹⁾	$V_{CE} = 3\text{ V}, I_C = 3\text{ A}$		2.5	V
C_{ob}	Output Capacitance	$V_{CB} = 10\text{ V}, I_E = 0,$ $f = 0.1\text{ MHz}$		200	pF

Note:

1. Pulse test: $p_w \leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

Typical Performance Characteristics

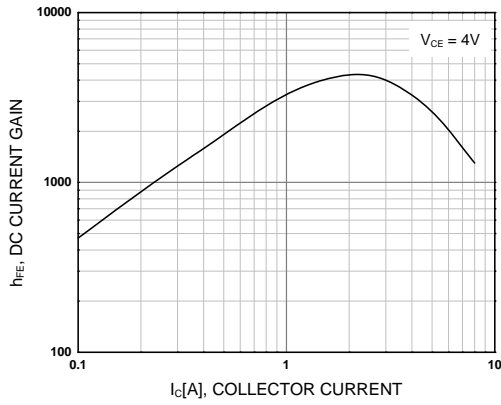


Figure 1. DC Current Gain

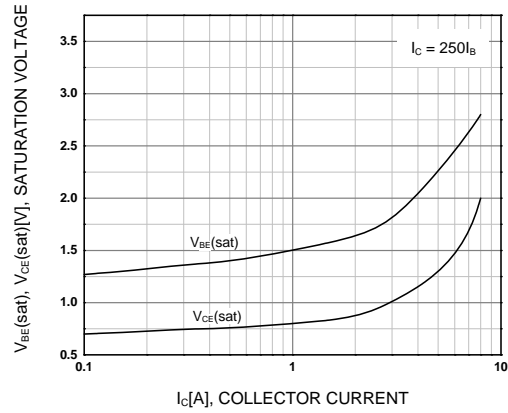


Figure 2. Base-Emitter Saturation Voltage and Collector-Emitter Saturation Voltage

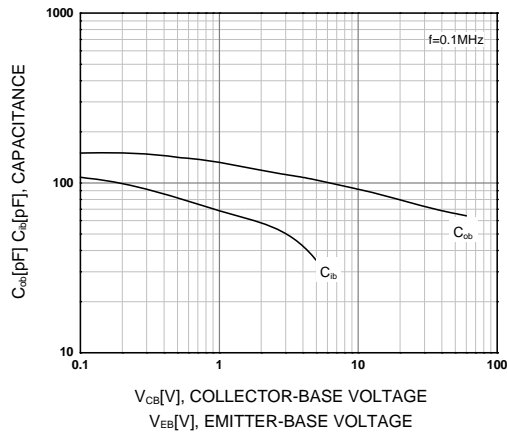


Figure 3. Output and Input Capacitance vs. Reverse Voltage

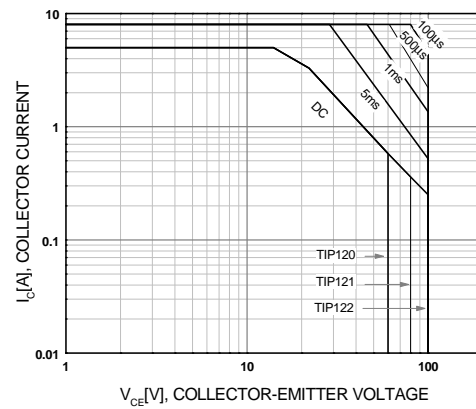


Figure 4. Safe Operating Area

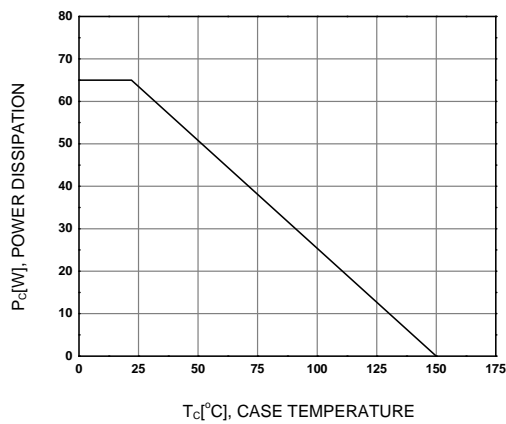


Figure 5. Power Derating

Physical Dimensions

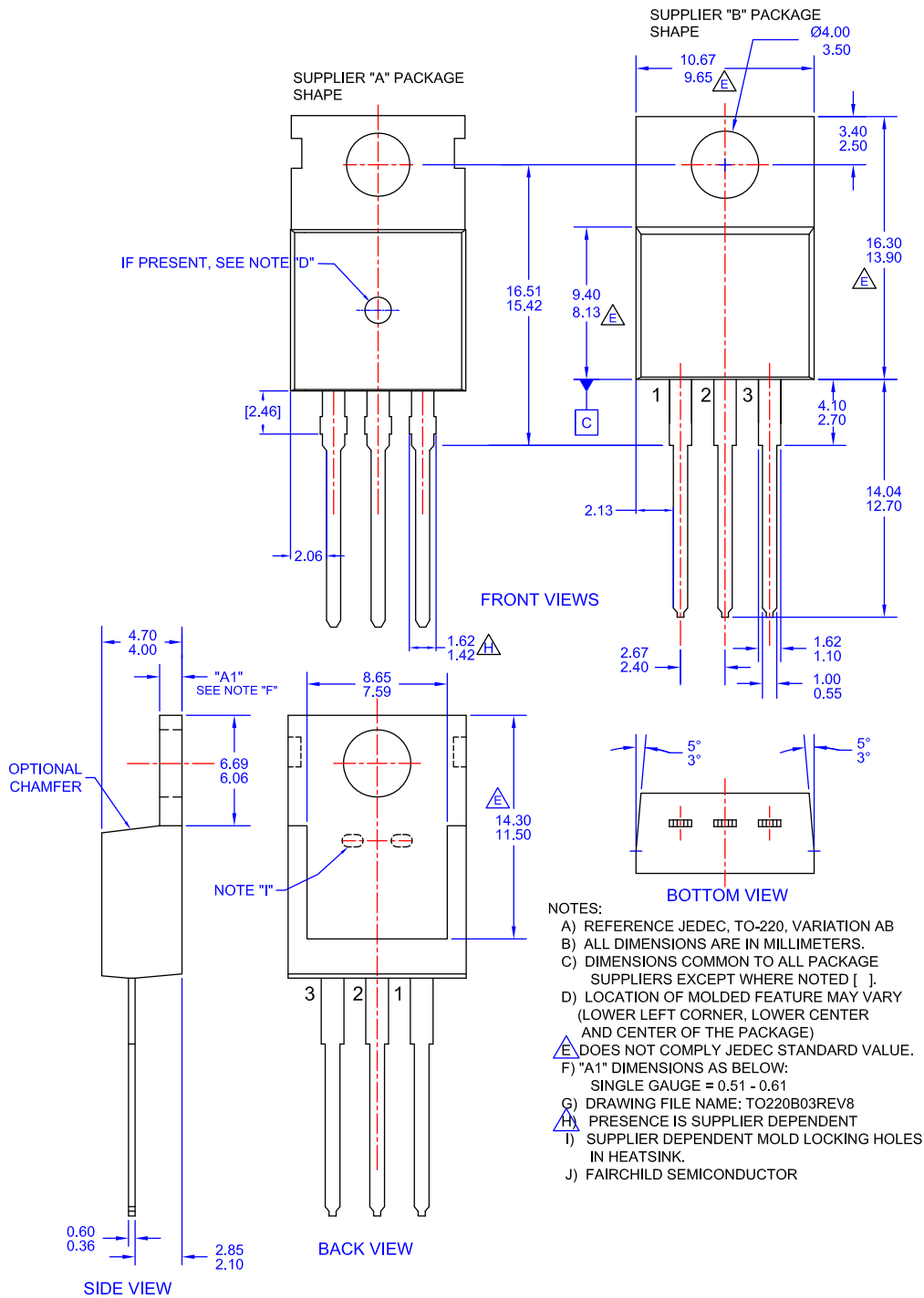


Figure 6. TO-220, MOLDED, 3LEAD, JEDEC VARIATION AB

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com
Order Literature: <http://www.onsemi.com/orderlit>
For additional information, please contact your local
Sales Representative