

SANYO Semiconductors DATA SHEET

N-Channel Silicon MOSFET

2SK4120LS — General-Purpose Switching Device **Applications**

Features

- · Low ON-resistance, low input capacitance, ultrahigh-speed switching.
- · Adoption of high reliability HVP process.
- · Attachment workability is good by Mica-less package.
- · Avalanche resistance guarantee.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		450	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	IDc*1	Limited only by maximum temperature	10	Α
	I _{Dpack} *2	Tc=25°C (SANYO's ideal heat dissipation condition)*3	7.9	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	34	Α
Allowable Power Dissipation	D-		2.0	W
	PD	Tc=25°C (SANYO's ideal heat dissipation condition)*3	33	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) *4	EAS		318	mJ
Avalanche Current *5	I _{AV}		10	Α

^{*1} Shows chip capability

The method is applying silicone grease to the backside of the device and attaching the device to water-cooled radiator made of aluminium.

Marking: K4120

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SANYO Semiconductor Co., Ltd.

TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

^{*2} Package limited

^{*3} SANYO's condition is radiation from backside.

^{*4} VDD=99V, L=5mH, IAV=10A

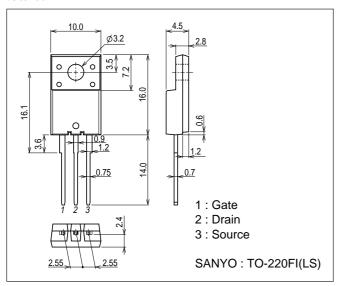
^{*5} L≤5mH, single pulse

Electrical Characteristics at Ta=25°C

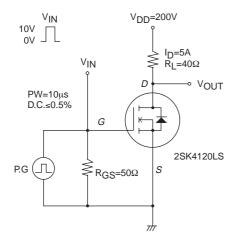
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=10mA, VGS=0V	450			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =360V, V _{GS} =0V			100	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±30V, V _{DS} =0V			±100	nA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	3		5	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =5A	2.4	4.8		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)	I _D =5A, V _{GS} =10V		0.52	0.68	Ω
Input Capacitance	Ciss	V _{DS} =30V, f=1MHz		630		pF
Output Capacitance	Coss	V _{DS} =30V, f=1MHz		140		pF
Reverse Transfer Capacitance	Crss	V _{DS} =30V, f=1MHz		32		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		18		ns
Rise Time	t _r	See specified Test Circuit.		54		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		73		ns
Fall Time	tf	See specified Test Circuit.		32		ns
Total Gate Charge	Qg	V _{DS} =200V, V _{GS} =10V, I _D =10A		24		nC
Gate-to-Source Charge	Qgs	V _{DS} =200V, V _{GS} =10V, I _D =10A		4.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =200V, V _{GS} =10V, I _D =10A		15		nC
Diode Forward Voltage	V _{SD}	IS=10A, VGS=0V		0.9	1.2	V

Package Dimensions

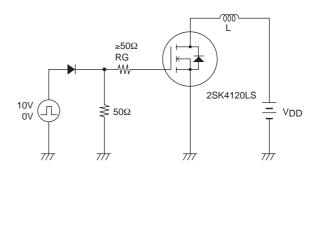
unit : mm (typ) 7509-002

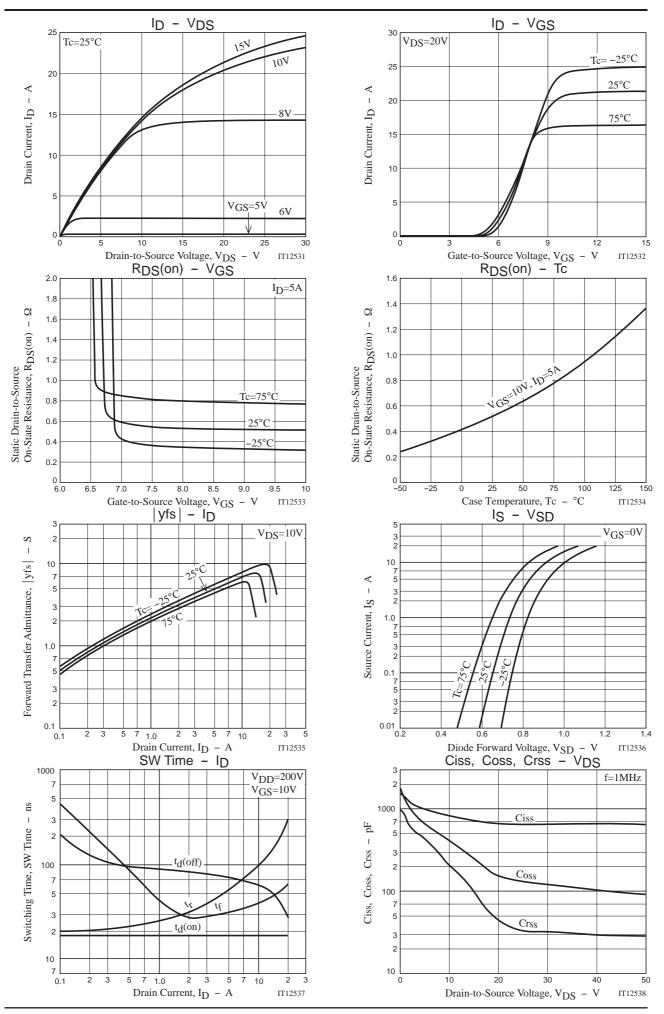


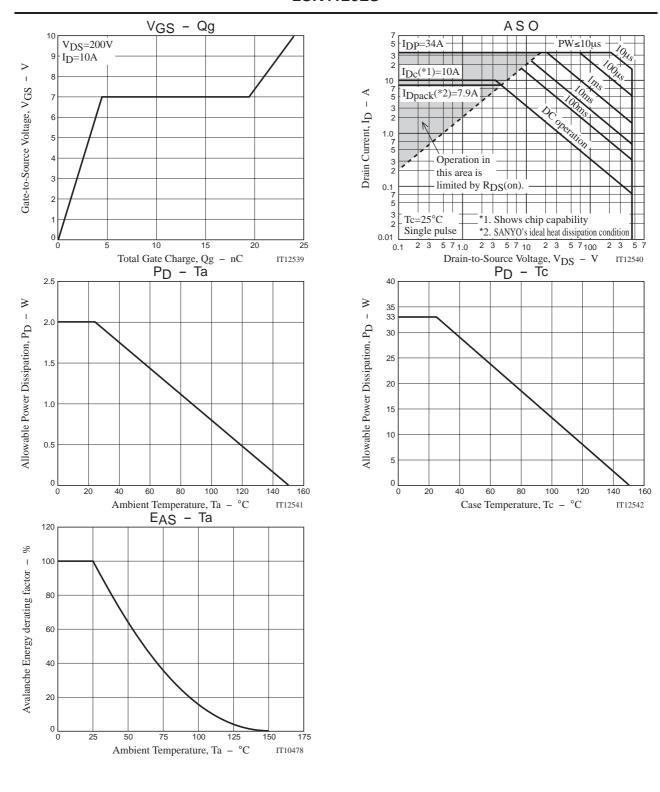
Switching Time Test Circuit



Avalanche Resistance Test Circuit







Note on usage: Since the 2SK4120LS is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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