

AM/FM/HD-Radio™/DAB submicron technology automotive receiver



Features



- AEC-Q100 qualified
- · Dual AM/FM reception with digital IF processing
- Digital radio support for dual-channel HD-Radio[™] and DAB/DRM reception through external coprocessor
- Integrated phase antenna diversity processing
- · Fully integrated dual VCO for world tuning
- · High performance PLLs for fast RDS system
- Integrated IF-filters with high selectivity, dynamic range and adaptive bandwidth control
- Drift-free and alignment-free digital IF-signal processing with high performance and flexibility
- · Integrated RF switch matrix in FM
- · RDS demodulation with group and block synchronization
- Filtered MPX for VICS applications
- High performance stereo decoder with noise-blanker
- Analog DAC stereo output and I²S digital output
- I²S, JESD204B, LVDS, high-speed digital I/Q base-band interface
- I²C/SPI bus-controlled
- Single 3.3 V external supply

Product status link TDA7707

Product summary			
Order code	Package	Packing	
TDA7707		Tray	
TDA7707EB	VFQFPN64	ITay	
TDA7707TR	(9 x 9 x 1 mm)	Tape & Reel	
TDA7707EBTR	,	Reel	

Description

The TDA7707 is a single chip fully-CMOS quad-band, dual-channel tuner aimed at receivers for analog and digital terrestrial radio broadcasting.

When paired to STMicroelectronics's STA680 or STA660 digital decoder ICs, the TDA7707 constitutes a complete optimized RF and baseband AM/FM/HD/DAB/DRM solution for automotive grade receivers and other applications.

The TDA7707 combines state-of-art performance with minimum external component count, making it therefore ideal for integration into car radios and other AM/FM/HD/DAB/DRM devices with challenging performance, quality, reliability and, last but not least, cost requirements.

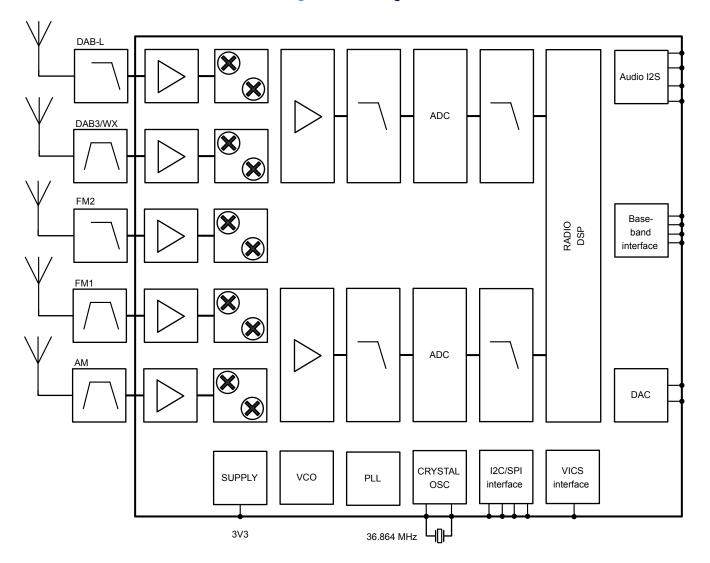
Combining TDA7707 with either STA680 or STA660 results in a complete, multistandard receiver solution, with low bill of material, high performance and real automotive grade quality and reliability.

Two device versions are available, differing only by the ROM content: the TDA7707EB requires a small FW code to be downloaded for booting the IC, thus making it especially suited to systems whose microcontroller has limited code storage capability.



1 Block diagram

Figure 1. Block diagram





2 Electrical specification

2.1 Absolute maximum ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Test condition	Min	Тур	Max	Units
V_{CC}	Abs. supply voltage	-	-0.5	-	3.6	V
T _{stg}	Storage temperature	-	-55	-	150	°C
		Human Body model	> ±2000 ⁽¹⁾			
V _{ESD} ESD absolute minimum withstand voltage	ESD absolute minimum withstand voltage	Charged device model	> ±500 ⁽²⁾		V	
		Charged device model, corner pins	> ±750			
-	Max. input current at any pin (latch-up characteristic)	I _{INMAX}	±100			mA

- 1. |±1000| on AM_IN pin
- 2. |±400| on AM_IN pin

2.2 Thermal data

Table 2. Thermal data

Symbol	Parameter	Parameter Test Condition		Units
R _{th j-amb} Thermal Resistance junction-to-ambient		Multilayer 2s2p as per JEDEC JESD51-7		°C/W
		Thermally optimized multilayer 2s2p board	22	C / VV

2.3 General key parameters

Table 3. General key parameters

Symbol	Parameter	Test Condition		Тур	Max	Units
V _{CC}	3.3 V supply voltage		3.15	3.3	3.45	V
		Two active RF channels, V_{CC} = 3.45 V	-	-	519	
I _{CC}	Supply current	Two active RF channels, JESD204b interface activated (10 pF load), V_{CC} = 3.45 V	-	-	600	mA
T _{amb}	Ambient Temperature Range	-	-40	-	85	°C
T _{j_oper}	Operative Junction Temp	-	-	-	125	°C
P _{diss}	Dissipated power	Two active RF channels, R _{ext} = 9 Ohm, I2S baseband interface activated	-	-	1.5	W
1 diss Dissipated (Dissipated power	Two active RF channels, $R_{\rm ext}$ = 7.5 Ohm, JESD204b interface activated	-	-	1.7	VV

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3 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: www.st.com. ECOPACK is an ST trademark.

3.1 VFQFPN-64 (9x9x1.0mm) package information

Figure 2. VFQFPN-64 (9x9x1.0 mm) package outline

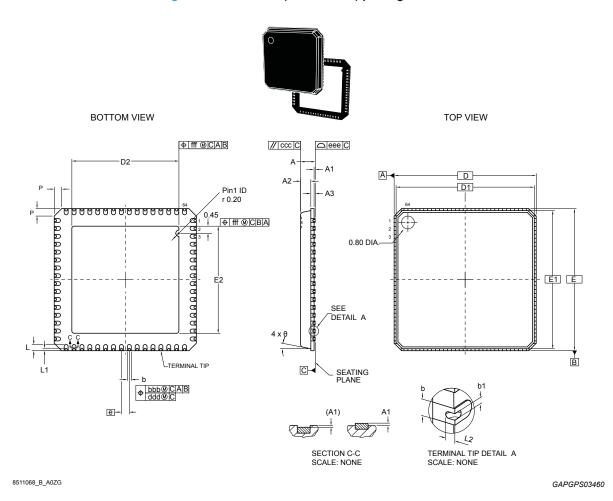


Table 4. VFQFPN-64 (9x9x1.0 mm) package mechanical data

Ref.		Dimensions in mm		
IXGI.	Min.	Тур.	Max.	
Θ	-	-	14	
Α	-	-	1.0	
A1	0.00	-	0.05	
A2	0.55 -		0.80	
A3	0.20 REF			
b ⁽¹⁾	0.18	0.25	0.30	

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Ref.		Dimensions in mm		
Kei.	Min.	Тур.	Max.	
b1	-	0.15	-	
D		9.00 BSC		
D1 ⁽²⁾		8.75 BSC		
D2	-	6.70	-	
е		0.50 BSC		
E		9.00 BSC		
E1 ⁽²⁾		8.75 BSC		
E2	-	6.70	-	
L	0.30	-	0.50	
L1		0.15 REF		
L2	-	0.10	-	
Р	-	-	0.60	
Tolerance of form and position	n			
aaa		0.15		
bbb		0.10		
ccc	0.10			
ddd		0.05		
eee		0.08		
fff		0.10		

^{1.} Maximum allowable burr is 0.076 mm in all directions.

Note:

The package is compliant to IPC/JEDEC J-STD-020D June 2007 standard Moisture/Reflow Sensitivity Classification for Nonhermetic Solid State Surface Mount Devices, MSL Level 3.

^{2.} D1 and E1 are Maximum plastic body size dimensions including mold mismatch. Dimensions D1 and E1 do not include mold flash or protrusions. Allowable mold flash or protrusions is "0.25 mm (0.0098 inch)" per side.



Revision history

Table 5. Document revision history

Date	Version	Changes
16-Dec-2014	1	Initial release.
04-Jun-2018	2	Fully revised.
25-Nov-2019	3	Updated Figure 1. Block diagram.



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