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FUSB252 Evaluation Board Users Guide

Version 1.1 – 4/6/2018

Overview

The FUSB252 evaluation board is designed to be inserted between a board that contains a Type-C controller (such as FUSB301 or FUSB302) and a source of voltage for OVP testing. Test points on the CC pins will allow OVP test scenarios to test the devices intended function. An LED indicator will show an OVP condition and the device should break connection to the USB host. If there isn't access to a Type-C voltage source, test points (GND and VDD) are available on the board for direct power supply connections.

After performing OVP testing, the Type-C receptacle can accept a Type-C device such as a memory stick to demonstrate normal USB functionality (with SEL=L).



Figure 1 – Board Top View and DP/DM Select Pin Map

Board Operation:

- The FUSB252 datasheet should be consulted for proper voltages.
- Power should be provided via the VDD and GND test points.
- Jumpers are provided for operation of the DP/DM switch. Switch needs to be enabled ($OE \setminus = 0$) to pass through the switch.
- The jumpers also provide the direction of the switch (see above illustration).
(SEL=L: HS1D+/HS1D-, SEL=H: HS2DP/HS2DM)
- Zero Ohm resistors (R1, R2) connect the SBU pins to the Type-C plug. They can be removed if desired for testing needs.

