

### STC04IE170HP

Monolithic emitter switched bipolar transistor ESBT® 1700 V - 4 A - 0.17  $\Omega$ 

#### **Features**

V <sub>CS(ON)</sub>	Ic	R <sub>CS(ON)</sub>
0.7 V	4 A	0.17 Ω

- High voltage / high current cascode configuration
- Low equivalent ON resistance
- Very fast-switch, up to 150 kHz
- Squared RBSOA, up to 1700 V
- Very low  $C_{ISS}$  driven by  $R_G = 47 \Omega$
- Very low turn-off cross over time

#### **Application**

■ Aux SMPS for three-phase mains

### **Description**

The STC04IE170HP is manufactured in Monolithic ESBT technology, aimed at providing the best performance in high frequency / high voltage applications. It is designed for use in gate driven based topologies.

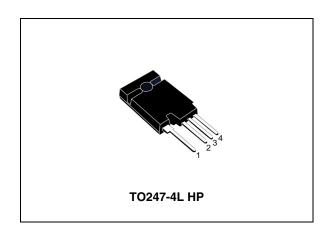


Figure 1. Internal schematic diagrams

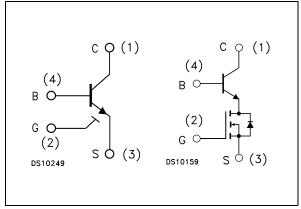


Table 1. Device summary

Order code	Marking	Package	Packing	
STC04IE170HP	C04IE170HP	TO247-4L HP	Tube	

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Electrical ratings STC04IE170HP

# 1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CS(SS)</sub>	Collector-source voltage (V <sub>BS</sub> = V <sub>GS</sub> = 0)	1700	V
V <sub>BS(OS)</sub>	Base-source voltage (I <sub>C</sub> = 0, V <sub>GS</sub> = 0)	30	V
V <sub>SB(OS)</sub>	Source-base voltage (I <sub>C</sub> = 0, V <sub>GS</sub> = 0)	17	V
V <sub>GS</sub>	Gate-source voltage	± 17	V
I <sub>C</sub>	Collector current	4	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5 ms)	8	Α
Ι <sub>Β</sub>	Base current	4	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 1 ms)	8	Α
P <sub>tot</sub>	Total dissipation at T <sub>c</sub> ≤ 25°C	50	W
T <sub>stg</sub>	Storage temperature	-40 to 150	°C
T <sub>J</sub>	Max. operating junction temperature	150	°C

Table 3. Thermal data

Symbol	Parameter	Value	Unit
$R_{thJC}$	Thermal resistance junction-case	2.5	°C/W

### 2 Electrical characteristics

 $(T_{case} = 25 \, ^{\circ}C \text{ unless otherwise specified})$ 

 Table 4.
 Electrical characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
I <sub>CS(SS)</sub>	Collector cut-off current (V <sub>BS</sub> = V <sub>GS</sub> = 0)	V <sub>CS</sub> = 1700 V			100	μΑ
I <sub>BS(OS)</sub>	Base cut-off current $(I_C = 0, V_{GS} = 0)$	V <sub>BS</sub> = 30 V			10	μΑ
I <sub>SB(OS)</sub>	Source cut-off current $(I_C = 0, V_{GS} = 0)$	V <sub>SB</sub> = 17 V			100	μΑ
I <sub>GS(OS)</sub>	Gate-source leakage current (V <sub>BS</sub> = 0)	V <sub>GS</sub> = ± 17V			100	nA
V <sub>CS(ON)</sub>	Collector-source ON voltage	$V_{GS} = 10 \text{ V}$ $I_{C} = 4 \text{ A}$ $I_{B} = 0.8 \text{ A}$ $V_{GS} = 10 \text{ V}$ $I_{C} = 1.5 \text{ A}$ $I_{B} = 0.15 \text{ A}$		0.7 0.6	1.5 1.4	<b>V V</b>
h <sub>FE</sub> <sup>(1)</sup>	DC current gain	$V_{CS} = 1 \text{ V } V_{GS} = 10 \text{ V } I_{C} = 4 \text{ A}$ $V_{CS} = 1 \text{ V } V_{GS} = 10 \text{ V } I_{C} = 1.5 \text{ A}$	4 7	5.5 11		
V <sub>BS(ON)</sub>	Base-source ON voltage	$V_{GS} = 10 \text{ V } I_C = 4 \text{ A}$ $I_B = 0.8 \text{ A}$ $V_{GS} = 10 \text{ V } I_C = 1.5 \text{ A}$ $I_B = 0.15 \text{ A}$		1.3 0.9	1.5 1.1	V V
V <sub>GS(th)</sub>	Gate threshold voltage	$V_{BS} = V_{GS}$ $I_B = 250 \mu A$	2	3	4	V
C <sub>iss</sub>	Input capacitance (V <sub>GS</sub> = V <sub>CB</sub> = 0)	V <sub>CS</sub> = 25 V f = 1 MHz		510		pF
Q <sub>GS(tot)</sub>	Gate-source charge (V <sub>CB</sub> = 0)	V <sub>GS</sub> = 10 V		3.9		nC
t <sub>s</sub>	Inductive load Storage time Fall time	$ \begin{aligned} & V_{GS} = 10 \ V & R_G = 47 \ \Omega \\ & V_{Clamp} = 1360 \ V & t_p = 4 \ \mu s \\ & I_C = 2 \ A & I_B = 0.4 \ A \end{aligned} $		770 10		ns ns
t <sub>s</sub>	Inductive load Storage time Fall time	$\begin{aligned} & V_{GS} = 10 \text{ V} & R_G = 47 \ \Omega \\ & V_{Clamp} = 1360 \text{V} & t_p = 4 \ \mu\text{s} \\ & I_C = 2 \text{ A} & I_B = 0.2 \text{ A} \end{aligned}$		410 10		ns ns
V <sub>CS(dyn)</sub>	Collector-source dynamic voltage (0.5 µs)	$\begin{split} &V_{CC} = V_{Clamp} = 400 \ V \\ &V_{GS} = 10 \ V &I_{C} = 1.5 \ A \\ &I_{B} = 0.3 \ A &t_{peak} = 500 \ ns \\ &R_{G} = 47 \ \Omega &I_{Bpeak} = 3 \ A \ (2 \ I_{C}) \end{split}$		5.36		V
V <sub>CS(dyn)</sub>	Collector-source dynamic voltage (1 µs)	$\begin{split} &V_{CC} = V_{Clamp} = 400 \ V \\ &V_{GS} = 10 \ V &I_{C} = 1.5 \ A \\ &I_{B} = 0.3 \ A &t_{peak} = 500 \ ns \\ &R_{G} = 47 \ \Omega &I_{Bpeak} = 3 \ A \ (2I_{C}) \end{split}$		4.32		٧
V <sub>CSW</sub>	Maximum collector- source voltage at turn- off without snubber	$R_G = 47 \Omega$ $h_{FE} = 5$ $I_C = 4 A$	1700			V

<sup>1.</sup> Pulsed duration = 300  $\mu$ s, duty cycle  $\leq$  1.5%.

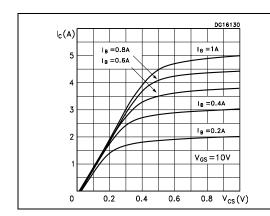


Electrical characteristics STC04IE170HP

### 2.1 Electrical characteristics (curves)

Figure 2. Output characteristics

Figure 3. Collector-source dynamic voltage



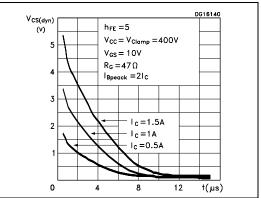
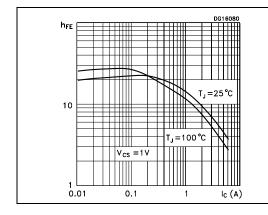


Figure 4. DC current gain

Figure 5. Gate threshold voltage vs temperature



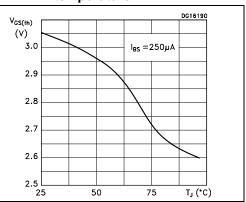
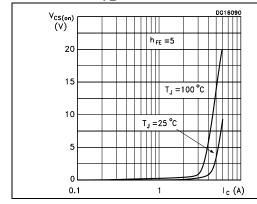
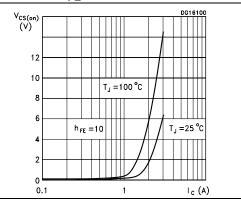


Figure 6. Collector-source ON voltage Figure 7.  $(h_{FE} = 5)$ 

igure 7. Collector-source ON voltage (h<sub>FE</sub> = 10)





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Figure 8. Base-source ON voltage  $(h_{FE} = 5)$ 

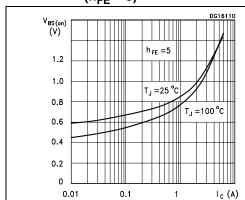


Figure 9. Base-source ON voltage  $(h_{FE} = 10)$ 

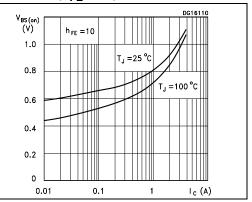
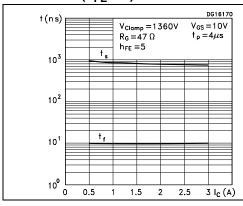


Figure 10. Inductive load switching time Figure 11. Inductive load switching time  $(h_{FE}=5)$   $(h_{FE}=10)$ 



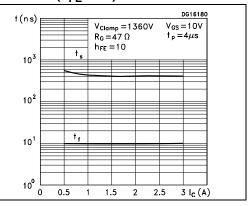
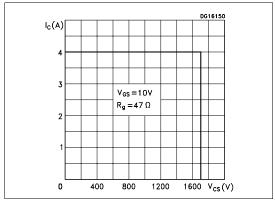


Figure 12. Reverse biased safe operating area



## 3 Package mechanical data

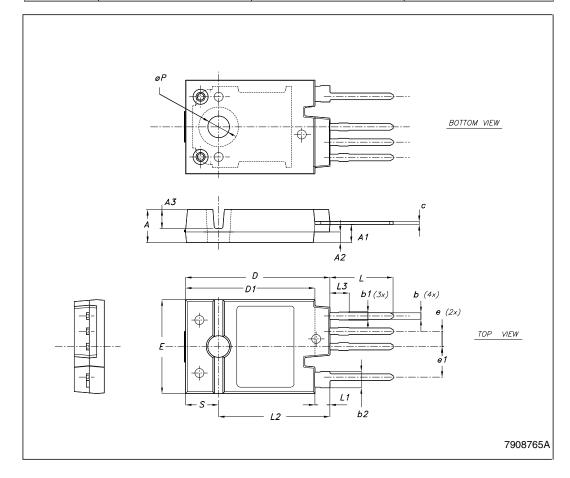
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#### TO247-4L HP mechanical data

DIM.		mm.	
DIWI.	MIN.	TYP	MAX.
Α	5.50	5.65	5.80
A1	2.85	3.15	3.25
A2		1.92	
A3		3.18	
b	0.95	1.10	1.30
b1	1.10		1.50
b2	2.50		2.90
С	0.40		0.80
D	23.85	24	24.15
D1		21.50	
E	15.45	15.60	15.75
е		2.54	
e1		5.08	
L	10.20		10.80
L1	2.20	2.50	2.80
L2		18.50	
L3		3	
øΡ	3.55		3.65
S		5.50	





Revision history STC04IE170HP

# 4 Revision history

Table 5. Document revision history

Date	Revision	Changes	
27-Sep-2006	1	First release.	
21-Nov-2006	2	Improved application target.	
17-Jun-2009	3	Updated Figure 2 on page 4 and mechanical data.	

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