

STA660

DAB/DAB+/DMB-A multi-standard digital radio decoder



TFBGA289

Features



- DAB/DAB+/DMB-A digital radio channel decoding
- Multi-standard digital radio source decoding
- Simultaneous reception of two DAB channels
- Two LVDS/I2S interfaces for receiving DAB baseband signals
- SDRAM interface (16 bit data bus)
- I²S input/output digital audio interfaces
- SPI interface
- UART interface
- 1.2 V core supply, 3.3 V I/O
- TFBGA289 package

Description

STA660 is an ARM-based and DSP-based system-on-chip able to receive and decode DAB baseband digital signals and with embedded application SW making it suitable for several different radio application use-cases.

The digital radio standards supported by STA660 are: DAB (ETSI EN 300 401), DAB+ (ETSI TS 102 563) and DMB (ETSI TS 102 428).

Product status link			
STA660			
Product summary			
Order code	Package	Packing	
STA660	TFBGA289	Tray	
STA660TR		Tape & Reel	

1 Function description

57/

The STA660 can demodulate and decode two digital radio streams in parallel thus allowing to implement a DAB foreground plus DAB background application.

1.1 DAB/DAB+/DMB-A signal processing

The STA660 detects, acquires, and demodulates DAB/DAB+/DMB-A signal. Such functions are primarily implemented by dedicated hardware accelerators. The demodulated signal is then processed for audio decoding and service decoding by the ARM9 processor or Xp70 DSP.

The STA660 is able to simultaneously demodulate two different DAB/DAB+/DMB-A signals radio streams. This feature allows the device to decode one digital radio audio/data stream in parallel with the reception of a data service broadcasted on a different radio channel.

In the single channel implementation (single RF tuner) the STA660 demodulates at the same time both the audio and the data carried inside a single ensemble. This means that the user can both listen to audio and receive traffic information or data broadcasted on that specific single ensemble.

In the dual channel implementation the STA660 can simultaneously demodulate audio and data associated to different ensembles. This means that it is possible to receive traffic information broadcasted on ensemble A while listening to audio program broadcasted on ensemble B.

1.2 Overview of main functional blocks

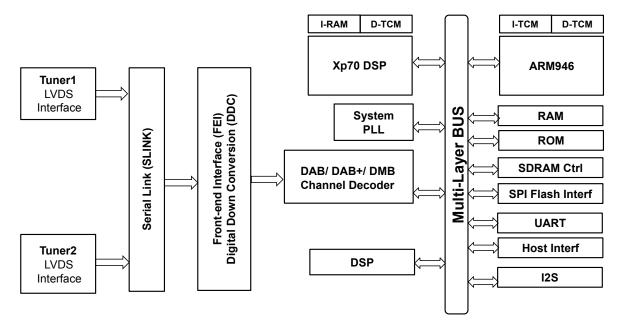


Figure 1. Functional block diagram

2 Electrical specifications

2.1 Absolute maximum ratings

Table 1. Absolute maximum ratings

Symbol	Parameter	Min.	Max.	Unit
VDDE	I/O logic supply voltage	- 0.3	3.7	V
VDD	Core logic supply voltage	- 0.3	1.4	V
VDDS	Core logic supply voltage	- 0.3	1.4	V
AVDD_SYSPLL	SYS PLL analog supply voltage	- 0.3	2.8	V
AVDD_AUDPLL	AUD PLL analog supply voltage	- 0.3	2.8	V
AVDD2V5_OSCI	OSCI analog supply voltage	- 0.3	2.8	V
DVDD_SYSPLL	SYS PLL digital supply voltage	- 0.3	1.4	V
DVDD_AUDPLL	AUD PLL digital supply voltage	- 0.3	1.4	V
AVDD1V2_OSCI	OSCI digital supply voltage	- 0.3	1.4	V
VESD-HBM	Electrostatic discharge HBM (leakage<µA)	-	±2000	V
VESD-CDM	Electrostatic discharge CDM (leakage<µA)	-	±500	V

Caution: Stresses above those listed under "Absolute maximum ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note: All voltages are referred to the relative GND.

2.2 Thermal data

Table 2. Thermal data

Symbol	Parameter	Value	Unit
R _{TH j-amb}	Thermal resistance junction-to-ambient (1)		°C/W

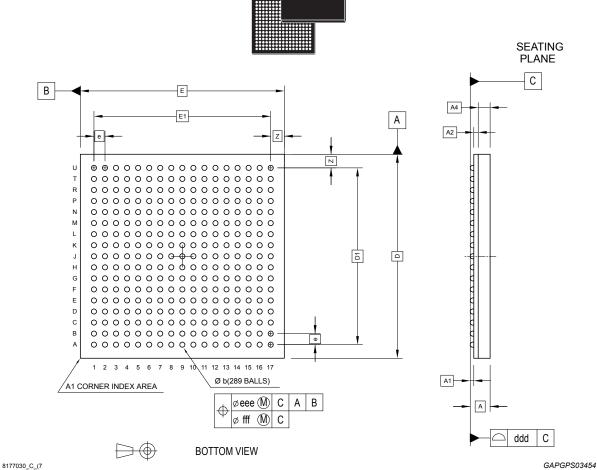
1. According to JEDEC specification on a 2s2p std board.

3 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

3.1 TFBGA289 (15x15x1.2 mm) package information

Figure 2. TFBGA289 (15x15x1.2 mm) package outline



0111030_0_(

Symbol	Dimensions (mm)			
	Min	Тур	Мах	
A	-	-	1.20	
A1	0.21	-	_	
A2	_	0.20	_	
A4	_	-	0.62	
b	0.35	0.40	0.48	
D	14.85	15.00	15.15	
D1	_	12.80	_	
E	14.85	15.00	15.15	
E1	_	12.80	_	
e	_	0.80	-	
Z	_	1.10	_	
ddd	-	-	0.10	
eee	_	-	0.15	
fff	-	-	0.08	

Table 3. TFBGA289 (15x15x1.2 mm) mechanical data

Revision history

Table 4. Document revision history

Date	Version	Changes
22-Feb-2019	1	Initial release.

Contents

1	1 Function description		
	1.1	DAB/DAB+/DMB-A signal processing.	. 2
	1.2	Overview of main functional blocks	. 2
2	Electi	rical specifications	.3
	2.1	Absolute maximum ratings	. 3
	2.2	Thermal data	. 3
3	Packa	age information	.4
	3.1	TFBGA289 (15x15x1.2 mm) package information	.4
Revi	ision h	istory	.6



IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2019 STMicroelectronics – All rights reserved