# Current-Shunt Monitor, Voltage Output, Bi-Directional Zero-Drift

The NCS210, NCS211, NCS212, NCS213, NCS214, and NCS215 are voltage output current shunt monitors that can measure voltage across shunts at common-mode voltages from -0.3 V to 26 V, independent of supply voltage. Six fixed gains are available: 50 V/V, 75 V/V, 100 V/V, 200 V/V, 500 V/V, or 1000 V/V. The low offset of the zero-drift architecture enables current sensing with maximum drops across the shunt as low as 10 mV full-scale.

The devices can operate from a single +2.7 V to +26 V power supply, drawing a maximum of 100  $\mu$ A of supply current. All versions are specified over the extended operating temperature range (-40°C to +125°C). Available in SC-70 and thin UQFN space-saving packages.

#### Features

- Wide Common–Mode Input Range –0.3 V to 26 V
- Supply Voltage Range from 2.7 V to 26 V
- Low Offset Voltage ±60 μV Max
- Low Offset Drift (0.1  $\mu$ V/°C)
- Low Gain Error (max 1%)
- Rail-to-rail Input and Output Capability
- Low Current Consumption (typ 65 μA, 100 μA max)
- NCV Prefix for Automotive and Other Applications Requiring Unique Site Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Typical Applications**

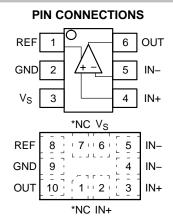
- Current Sensing (High-Side/Low-Side)
- Automotive
- Telecom
- Sensors



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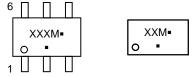
#### www.onsemi.com

SC70-6	UQFN10
SQ SUFFIX	MU SUFFIX
CASE 419B	CASE 488AT



\*NC denotes no internal connection. These pins can be left floating or connected to any voltage between Vs and GND.

#### MARKING DIAGRAMS



XXX = Specific Device Code (See page 4) M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

Product	Gain	R3-R4	R1-R2
NCS213	50	20 kΩ	1 MΩ
NCS215	75	13.3 kΩ	1 MΩ
NCS214	100	10 kΩ	1 MΩ
NCS210	200	5 kΩ	1 MΩ
NCS211	500	2 kΩ	1 MΩ
NCS212	1000	1 kΩ	1 MΩ
		•	

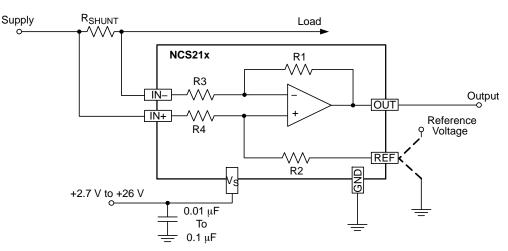
 $V_{OUT} = (I_{LOAD} \times R_{SHUNT})GAIN + V_{REF}$ 

#### **ORDERING INFORMATION**

This document contains information on some products that are still under development. ON Semiconductor reserves the right to change or discontinue these products without notice.

See detailed ordering, marking and shipping information on page 4 of this data sheet.

Semiconductor Components Industries, LLC, 2016 October, 2016 – Rev. 6



**Figure 1. Application Schematic** 

#### Table 1. MAXIMUM RATINGS

Rating				Value	Unit
Supply Voltage (Note 1)		NCS21x NCV21x	V <sub>S</sub>	+26 +28	V
Analog Inputs	Differential (V <sub>IN+</sub> )–(V <sub>IN-</sub> )		$V_{IN+,}V_{IN-}$	-26 to +26	V
	Common–Mode (Note 2)	NCS21x NCV21x		GND-0.3 to +26 GND-0.3 to +28	
REF Input			V <sub>REF</sub>	GND-0.3 to ( V <sub>s</sub> ) +0.3	V
Output (Note 2)			V <sub>OUT</sub>	GND–0.3 to ( V <sub>s</sub> ) +0.3	V
Input Current into Any Pin (Note 2)				5	mA
Maximum Junction Temperature			T <sub>J(max)</sub>	+150	°C
Storage Temperature Range			TSTG	-65 to +150	°C
ESD Capability, Human Body Model	(Note 3)		HBM	±3000	V
ESD Capability, Machine Model (Not	e 3)		MM	±100	V
Charged Device Model (Note 3)			CDM	±1000	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Refer to ELECTRICAL CHARACTERISTICS, RECOMMENDED OPERATING RANGES and/or APPLICATION INFORMATION for safe operating parameters.

2. Input voltage at any pin may exceed the voltage shown if current at that pin is limited to 5 mA.

3. This device series incorporates ESD protection and is tested by the following methods

- ESD Human Body Model tested per AEC–Q100–002 (EIA/JÉSD22–A114) ESD Machine Model tested per AEC–Q100–003 (EIA/JESD22–A115)
  - ESD Charged Device Model tested per AEC–Q100–011.

Latchup Current Maximum Rating: 50 mA per JEDEC standard: JESD78

#### Table 2. THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal Characteristics, SC70 (Note 4) Thermal Resistance, Junction-to-Air (Note 5)	$R_{\thetaJA}$	250	°C/W

4. Refer to ELECTRICAL CHARACTERISTICS, RECOMMENDED OPERATING RANGES and/or APPLICATION INFORMATION for safe operating parameters.

5. Values based on copper area of 645 mm<sup>2</sup> (or 1 in<sup>2</sup>) of 1 oz copper thickness and FR4 PCB substrate.

#### Table 3. RECOMMENDED OPERATING RANGES

Rating	Symbol	Min	Max	Unit
Supply Voltage	V <sub>S</sub>	2.7	26	V
Ambient Temperature	T <sub>A</sub>	-40	125	°C

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

#### Table 4. ELECTRICAL CHARACTERISTICS

**Boldface** limits apply over the specified temperature range,  $T_A = -40^{\circ}C$  to  $125^{\circ}C$ , guaranteed by characterization and/or design.

At  $T_A = +25^{\circ}$ C,  $V_{SENSE} = V_{IN+} - V_{IN-}$ . NCS210, NCS213, NCS214 and NCS215:  $V_S = +5$  V,  $V_{IN+} = 12$  V, and  $V_{REF} = V_S/2$ , unless otherwise noted. NCS211 and NCS212:  $V_S = +12$  V,  $V_{IN+} = 12$  V, and  $V_{REF} = V_S/2$ , unless otherwise noted.

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit

GAIN						
NCS210 NCS211 NCS212 NCS213 NCS214 NCS215			G	200 500 1000 50 100 75		V/V
Gain Error		$V_{SENSE} = -5 \text{ mV} \text{ to } 5 \text{ mV}$	G <sub>e</sub>	±0.2	±1	%
	NCV210	$V_{SENSE} = -5 \text{ mV} \text{ to } 5 \text{ mV}$	G <sub>e</sub>	±0.2	±1.5	%
Gain Error vs.	. Temperature	$T_A = -10^{\circ}C$ to $125^{\circ}C$		7	14	ppm/°C
	NCV210	$T_A = -40^{\circ}C$ to $125^{\circ}C$		7	70	ppm/°C
Nonlinearity E	Error	$V_{SENSE} = -5 \text{ mV} \text{ to } 5 \text{ mV}$		±0.01		%
Maximum Car	pacitive Load	No sustained oscillation		1		nF

#### VOLTAGE OFFSET

Offset Voltage (RTI Note 6)	NCS210,NCS211, NCS212, NCS214, NCS215	V <sub>SENSE</sub> = 0 mV	V <sub>OS</sub>	±0.55	±60	μV
	NCS213			±5	±100	μV
	NCV210			±0.55	±90	μV
Offset Drift			δV/δΤ	0.1	0.6	μV/°C
	NCS213		δV/δΤ	0.5	1.5	μV/°C
	NCV210		δV/δΤ	0.1	1.0	μV/°C

#### INPUT

Input Bias Current		V <sub>SENSE</sub> = 0 mV	I <sub>IB</sub>			60	μΑ
Common-Mode Ir	nput Voltage Range		V <sub>CM</sub>	-0.3		26	V
Common–Mode Rejection Ratio	NCS210, NCS211, NCS212, NCS214, NCS215	$V_S = 5 V$ , $V_{IN+} = 2 V$ to +26 V, $V_{SENSE} = 0 mV$	CMRR	103	115		dB
	NC3215	V <sub>S</sub> = 3.3 V, V <sub>IN+</sub> = 3 V to +26 V, V <sub>SENSE</sub> = 0 mV		103	115		dB
		$V_{S} = 3.3 \text{ V}, V_{IN+} = 0 \text{ V} \text{ to } +26 \text{ V}, V_{SENSE} = 0 \text{ mV} (T_{A} = -10^{\circ}\text{C to } 85^{\circ}\text{C})$		103	120		dB
Common–Mode Rejection Ratio	NCS213	$V_S = 5 V$ , $V_{IN+} = 2 V to +26 V$ , $V_{SENSE} = 0 mV$	CMRR	97	110		dB
		V <sub>S</sub> = 3.3 V, V <sub>IN+</sub> = 3 V to +26 V, V <sub>SENSE</sub> = 0 mV		97	110		dB
		$V_{S} = 3.3 \text{ V}, V_{IN+} = 0 \text{ V} \text{ to } +26 \text{ V}, V_{SENSE} = 0 \text{ mV} (T_{A} = -10^{\circ}\text{C to } 85^{\circ}\text{C})$		97	115		dB
Common–Mode Rejection Ratio	NCV210	$V_S = 5 V$ , $V_{IN+} = 2 V to +26 V$ , $V_{SENSE} = 0 mV$	CMRR	100	115		dB
		V <sub>S</sub> = 3.3 V, V <sub>IN+</sub> = 3 V to +26 V, V <sub>SENSE</sub> = 0 mV		100	115		dB
OUTPUT							
		Deferenced from CND	V		E	50	

Output Voltage Low	Referenced from GND $R_L = 10 \text{ k}\Omega$ to Ground	V <sub>OL</sub>	5	50	mV	
Output Voltage High	Referenced from V <sub>S</sub> R <sub>L</sub> = 10 k $\Omega$ to Ground	V <sub>OH</sub>	0.05	0.2	V	

#### **Table 4. ELECTRICAL CHARACTERISTICS**

**Boldface** limits apply over the specified temperature range,  $T_A = -40^{\circ}C$  to 125°C, guaranteed by characterization and/or design.

At  $T_A = +25^{\circ}$ C,  $V_{SENSE} = V_{IN+} - V_{IN-}$ . NCS210, NCS213, NCS214 and NCS215:  $V_S = +5$  V,  $V_{IN+} = 12$  V, and  $V_{REF} = V_S/2$ , unless otherwise noted. NCS211 and NCS212:  $V_S = +12$  V,  $V_{IN+} = 12$  V, and  $V_{REF} = V_S/2$ , unless otherwise noted.

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
DYNAMIC PERFORMANCE	·			•		
Bandwidth (f <sub>-3dB</sub> )	$\begin{array}{l} C_{LOAD} = 10 \ p\text{F}, \ \text{NCS210} \\ C_{LOAD} = 10 \ p\text{F}, \ \text{NCS211} \\ C_{LOAD} = 10 \ p\text{F}, \ \text{NCS212} \\ C_{LOAD} = 10 \ p\text{F}, \ \text{NCS213} \\ C_{LOAD} = 10 \ p\text{F}, \ \text{NCS214} \\ C_{LOAD} = 10 \ p\text{F}, \ \text{NCS215} \end{array}$	BW		40 25 10 100 60 50		kHz
Slew Rate		SR		0.4		V/μs
NOISE						
Spectral Density, 1 kHz (RTI Note 6)		e <sub>n</sub>		35		nV/√Hz
POWER SUPPLY						
Operating Voltage Range	V <sub>SENSE</sub> = 0 mV	Vs	2.7		26	V
Quiescent Current	V <sub>SENSE</sub> = 0 mV	I <sub>DD</sub>		65	100	μA
Quiescent Current over Temperature	V <sub>SENSE</sub> = 0 mV				115	μΑ
Power Supply Rejection Ratio	$V_{S}$ = +2.7 V to +26 V, $V_{IN+}$ =18 V, $V_{SENSE}$ = 0 mV	PSRR		±0.1	±10	μV/V

6. RTI = referenced-to-input.

#### **ORDERING INFORMATION**

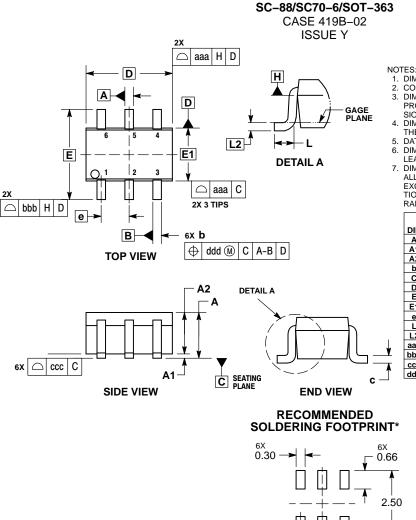
Device	Gain	Marking	Package	Shipping <sup>†</sup>
NCS210SQT2G	200	AAP		
NCS211SQT2G	500	AAM		
NCS212SQT2G (In Development)**	1000	AAN		
NCS213SQT2G (In Development)**	50	AAQ		
NCS214SQT2G (In Development)**	100	AAR		
NCS215SQT2G (In Development)**	75	AAT	SC70-6	2000 / Tana and Daal
NCV210SQT2G *	200	VAP	(Pb-Free)	3000 / Tape and Reel
NCV211SQT2G * (In Development)**	500	VAM		
NCV212SQT2G * (In Development)**	1000	VAN		
NCV213SQT2G * (In Development)**	50	VAQ		
NCV214SQT2G * (In Development)**	100	VAR		
NCV215SQT2G * (In Development)**	75	VAT		
NCS210MUTAG (In Development)**	200	TBD		
NCS211MUTAG (In Development)**	500	TBD		
NCS212MUTAG (In Development)**	1000	TBD	UQFN10	
NCS213MUTAG (In Development)**	50	TBD	(Pb-Free)	3000 / Tape and Reel
NCS214MUTAG (In Development)**	100	TBD	1	
NCS215MUTAG (In Development)**	75	TBD	1	

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D

\*NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

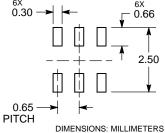
\*\* Contact local sales office for availability.

#### PACKAGE DIMENSIONS



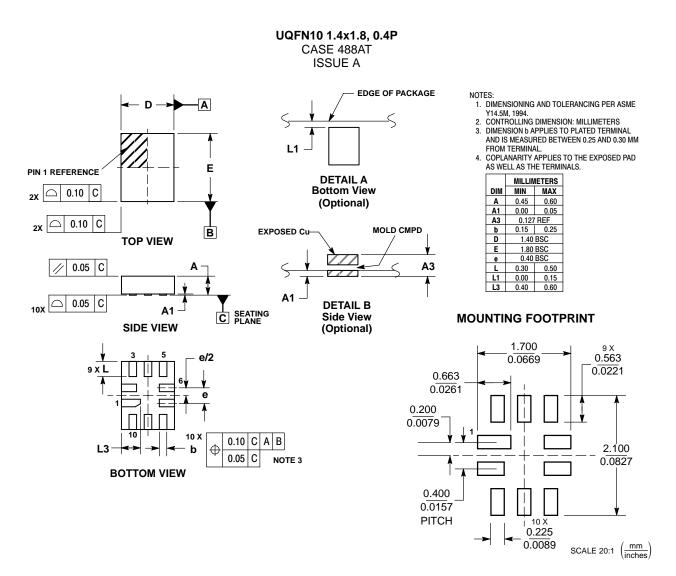
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRU-SIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H. DATUMS A AND B ARE DETERMINED AT DATUM H. DIMENSIONS DA DID & ADDI Y TO THE EI AT SECTION OF THE
- DIMENSIONS & AND C APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
- DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION 5 AT MAXIMUM MATERIAL CONDI-TION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α			1.10			0.043
A1	0.00		0.10	0.000		0.004
A2	0.70	0.90	1.00	0.027	0.035	0.039
b	0.15	0.20	0.25	0.006	0.008	0.010
С	0.08	0.15	0.22	0.003	0.006	0.009
D	1.80	2.00	2.20	0.070	0.078	0.086
Е	2.00	2.10	2.20	0.078	0.082	0.086
E1	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65 BSC			0.026 BSC		
L	0.26	0.36	0.46	0.010	0.014	0.018
L2	0.15 BSC			0.006 BSC		
aaa	0.15			0.006		
bbb	0.30			0.012		
CCC	0.10			0.004		
ddd	0.10			0.004		



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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