

### Rail-to-rail high output current single operational amplifier

Datasheet -production data

#### **Features**

■ Rail-to-rail input and output

■ Low noise: 9 nV/√Hz

■ Low distortion

 $\blacksquare$  High output current: 80 mA (able to drive 32 Ω

loads)

■ High-speed: 4 MHz, 1 V/µs
 ■ Operating from 2.7 V to 12 V
 ■ ESD internal protection: 1.5 kV

■ Latch-up immunity

■ Macromodel included in this specification

### **Applications**

■ Headphone amplifier

■ Piezoelectric speaker driver

■ Sound cards, multimedia systems

■ Line driver, actuator driver

■ Servo amplifier

Mobile phone and portable communication sets

Instrumentation with low noise as key factor

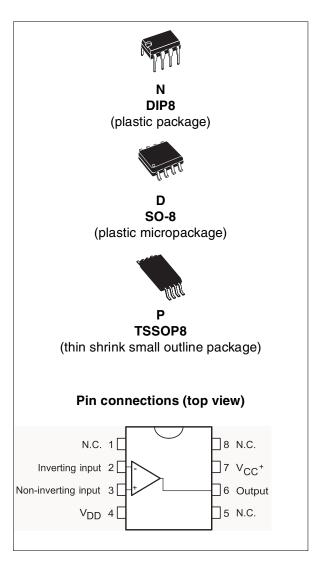


Table 1. Device summary

Order code	Temperature range	Package Packing		Marking
TS921IN	-40 °C, +125 °C	DIP8	Tube	TS921IN
TS921ID/IDT		SO-8	Tube or tape and reel	
TS921IPT		TSSOP8 (thin shrink outline package)	Tape and reel	9211

September 2012 Doc ID 5560 Rev 4 1/16

Description TS921

### 1 Description

The TS921 device is a rail-to-rail single BiCMOS operational amplifier optimized and fully specified for 3 V and 5 V operation.

Its high output current allows low load impedances to be driven.

The TS921 device exhibits very low noise, low distortion and low offset. It has a high output current capability which makes this device an excellent choice for high quality, low voltage or battery operated audio systems.

The device is stable for capacitive loads up to 500 pF.



### 2 Absolute maximum ratings

Table 2. Key parameters and their absolute maximum ratings

Symbol	Parameter	Condition	Value	Unit
V <sub>CC</sub>	Supply voltage <sup>(1)</sup>		14	V
V <sub>id</sub>	Differential input voltage <sup>(2)</sup>		±1	V
V <sub>i</sub>	Input voltage		V <sub>DD</sub> - 0.3 to V <sub>CC</sub> + 0.3	V
T <sub>stg</sub>	Storage temperature		-65 to +150	°C
Tj	Maximum junction temperature		150	°C
R <sub>thja</sub>	Thermal resistance junction-to-ambient	SO-8 TSSOP8 DIP8	125 120 85	°C/W
R <sub>thjc</sub>	Thermal resistance junction-to-case	SO-8 TSSOP8 DIP8	40 37 41	°C/W
		HBM Human body model <sup>(3)</sup>	1.5	kV
ESD	Electrostatic discharge	MM Machine model <sup>(4)</sup>	100	٧
		CDM Charged device model	1.5	kV
	Output short-circuit duration		See <sup>(5)</sup>	
	Latch-up immunity		200	mA
	Soldering temperature	10 sec., standard package	250	°C
	Soldering temperature	10 sec., lead-free package	260	

- 1. All voltage values, except differential voltage are with respect to network ground terminal.
- 2. Differential voltages are the non-inverting input terminal with respect to the inverting input terminal. If  $V_{id} > \pm 1 V$ , the maximum input current must not exceed  $\pm 1$  mA. In this case ( $V_{id} > \pm 1 V$ ) an input serie resistor must be added to limit input current
- 3. Human body model, 100 pF discharged through a 1.5 k $\Omega$  resistor into pin of device.
- 4. Machine model ESD, a 200 pF cap is charged to the specified voltage, then discharged directly into the IC with no external series resistor (internal resistor  $< 5 \Omega$ ), into pin to pin of device.
- There is no short-circuit protection inside the device: short-circuits from the output to V<sub>CC</sub> can cause excessive heating. The
  maximum output current is approximately 80 mA, independent of the magnitude of V<sub>CC</sub>. Destructive dissipation can result
  from simultaneous short-circuits on all amplifiers.

Table 3. Operating conditions

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Supply voltage	2.7 to 12	V
V <sub>icm</sub>	Common mode input voltage range	$V_{\rm DD}$ - 0.2 to $V_{\rm CC}$ + 0.2	V
T <sub>oper</sub>	Operating free air temperature range	-40 to +125	°C



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Electrical characteristics TS921

### 3 Electrical characteristics

Table 4. Electrical characteristics for  $V_{CC} = 3 \text{ V}$ ,  $V_{DD} = 0 \text{ V}$ ,  $V_{icm} = V_{CC}/2$ ,  $R_L$  connected to  $V_{CC}/2$ ,  $T_{amb} = 25 \,^{\circ}\text{C}$  (unless otherwise specified)

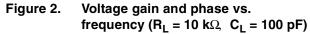
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>io</sub>	Input offset voltage	at T <sub>min</sub> . ≤T <sub>amb</sub> ≤T <sub>max</sub>			3 5	mV
ΔV <sub>io</sub>	Input offset voltage drift			2		μV/°C
I <sub>io</sub>	Input offset current	V <sub>out</sub> = 1.5 V		1	30	nA
I <sub>ib</sub>	Input bias current	V <sub>out</sub> = 1.5 V		15	100	nA
V <sub>OH</sub>	High level output voltage	$R_L = 600 \Omega$ $R_L = 32 \Omega$	2.87	2.63		V
V <sub>OL</sub>	Low level output voltage	$\begin{aligned} R_L &= 600 \ \Omega \\ R_L &= 32 \ \Omega \end{aligned}$		180	100	mV
A <sub>vd</sub>	Large signal voltage gain	$\begin{aligned} &V_{out} = 2 \ V_{pk\text{-}pk} \\ &R_L = 600 \ \Omega \\ &R_L = 32 \ \Omega \end{aligned}$		35 16		V/mV
GBP	Gain bandwidth product	$R_L = 600 \Omega$		4		MHz
I <sub>CC</sub>	Supply current	No load, V <sub>out</sub> = V <sub>CC</sub> /2		1	1.5	mA
CMR	Common mode rejection ratio		60	80		dB
SVR	Supply voltage rejection ratio	$V_{CC} = 2.7 \text{ to } 3.3 \text{ V}$	60	80		dB
I <sub>o</sub>	Output short-circuit current		50	80		mA
SR	Slew rate		0.7	1.3		V/μs
Pm	Phase margin at unit gain	$R_L = 600 \Omega$ , $C_L = 100 pF$		68		Degrees
GM	Gain margin	$R_L = 600 \Omega$ , $C_L = 100 pF$		12		dB
e <sub>n</sub>	Equivalent input noise voltage	f = 1 kHz		9		$\frac{\text{nV}}{\sqrt{\text{Hz}}}$
THD	Total harmonic distortion	$\begin{aligned} &V_{out} = 2 \ V_{pk-pk}, \ f = 1 \ kHz, \\ &A_{v} = 1, \ R_{L} = 600 \ \Omega \end{aligned}$		0.005		%

Table 5. Electrical characteristics for  $V_{CC}$  = 5 V,  $V_{DD}$  = 0 V,  $V_{icm}$  =  $V_{CC}/2$ ,  $R_L$  connected to  $V_{CC}/2$ ,  $T_{amb}$  = 25 °C (unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>io</sub>	Input offset voltage	at T <sub>min.</sub> ≤T <sub>amb</sub> ≤T <sub>max</sub>			3 5	mV
$\Delta V_{io}$	Input offset voltage drift			2		μV/°C
I <sub>io</sub>	Input offset current	V <sub>out</sub> = 1.5 V		1	30	nA
l <sub>ib</sub>	Input bias current	V <sub>out</sub> = 1.5 V		15	100	nA
V <sub>OH</sub>	High level output voltage	$R_L = 600 \Omega$ $R_L = 32 \Omega$	4.85	4.4		V
V <sub>OL</sub>	Low level output voltage	$R_L = 600 \Omega$ $R_L = 32 \Omega$		300	120	mV
A <sub>vd</sub>	Large signal voltage gain	$V_{out} = 2 V_{pk-pk}$ $R_L = 600 \Omega$ $R_L = 32 \Omega$		35 16		V/mV
GBP	Gain bandwidth product	$R_L = 600 \Omega$		4		MHz
I <sub>CC</sub>	Supply current	No load, V <sub>out</sub> = V <sub>CC</sub> /2		1	1.5	mA
CMR	Common mode rejection ratio		60	80		dB
SVR	Supply voltage rejection ratio	V <sub>CC</sub> = 4.5 to 5.5 V	60	80		dB
Io	Output short-circuit current		50	80		mA
SR	Slew rate		0.7	1.3		V/μs
Pm	Phase margin at unit gain	$R_L = 600 \Omega$ , $C_L = 100 pF$		68		Degrees
GM	Gain margin	$R_L = 600 \Omega$ , $C_L = 100 pF$		12		dB
e <sub>n</sub>	Equivalent input noise voltage	f = 1 kHz		9		$\frac{\text{nV}}{\sqrt{\text{Hz}}}$
THD	Total harmonic distortion	$V_{out} = 2 V_{pk-pk}, f = 1 \text{ kHz}, A_v = 1, R_L = 600 \Omega$		0.005		%

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Figure 1. Output short-circuit vs. output voltage ( $V_{CC} = 5 \text{ V}, V_{DD} = 0 \text{ V}$ )



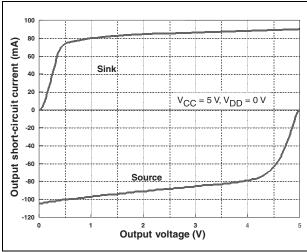
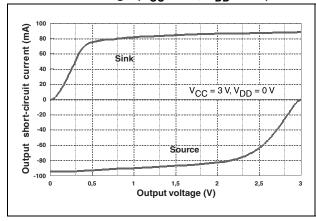


Figure 3. Output short-circuit vs. output voltage ( $V_{CC} = 3 \text{ V}, V_{DD} = 0 \text{ V}$ )

Figure 4. Equivalent input noise voltage vs. frequency ( $V_{CC} = \pm 1.5 \text{ V}$ ,  $R_L = 100 \Omega$ )



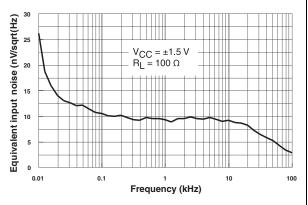
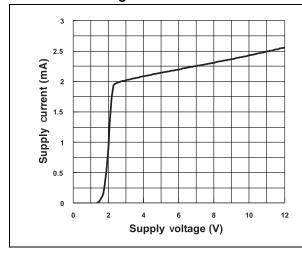
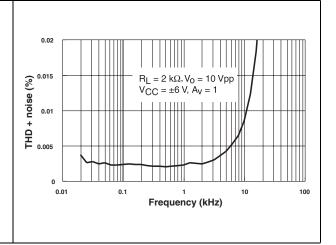


Figure 5. Output supply current vs. supply voltage

Figure 6. THD + noise vs. frequency ( $R_L = 2 k\Omega$ ,  $V_o = 10 \text{ Vpp}$ ,  $V_{CC} = \pm 6 \text{ V}$ ,  $A_v = 1$ )



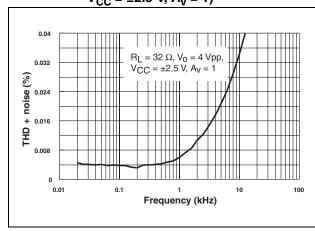


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Figure 7. THD + noise vs. frequency  $(R_L = 32~\Omega ~V_o = 4~Vpp, \\ V_{CC} = \pm 2.5~V, A_v = 1)$ 

Figure 8. THD + noise vs. output voltage  $(R_L = 600 \ \Omega, \ f = 1 \ kHz, \\ V_{CC} = 0/3 \ V, \ A_v = -1)$ 



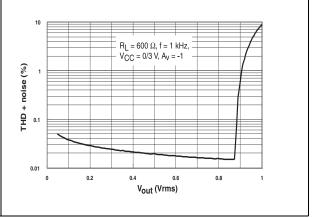
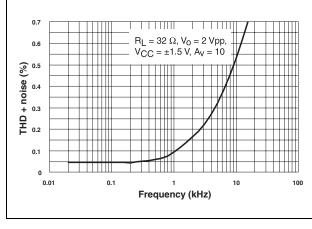


Figure 9. THD + noise vs. frequency  $(R_L = 32 \Omega, V_o = 2 Vpp, V_{CC} = \pm 1.5 V, A_v = 10)$ 

Figure 10. THD + noise vs. output voltage (R<sub>L</sub> = 32  $\Omega$ , f = 1 kHz, V<sub>CC</sub> = ±1.5 V, A<sub>V</sub> = -1)



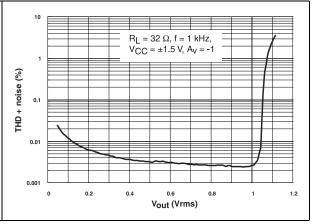
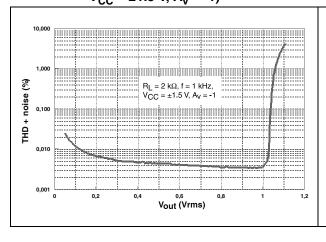
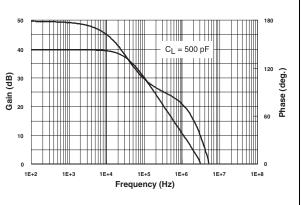


Figure 11. THD + noise vs. output voltage  $(R_L = 2 \ k\Omega, \ f = 1 \ kHz, \\ V_{CC} = \pm 1.5 \ V, \ A_V = -1)$ 

Figure 12. Open loop gain and phase vs. frequency ( $C_L = 500 \text{ pF}$ )





Macromodel TS921

#### 4 Macromodel

#### 4.1 Important note concerning this macromodel

Please consider following remarks before using this macromodel:

- All models are a trade-off between accuracy and complexity (i.e. simulation time).
- Macromodels are not a substitute to breadboarding; rather, they confirm the validity of a design approach and help to select surrounding component values.
- A macromodel emulates the NOMINAL performance of a TYPICAL device within SPECIFIED OPERATING CONDITIONS (i.e. temperature, supply voltage, etc.). Thus the macromodel is often not as exhaustive as the datasheet, its goal is to illustrate the main parameters of the product.
- Data issued from macromodels used outside of its specified conditions (V<sub>CC</sub>, temperature, etc.) or even worse: outside of the device operating conditions (V<sub>CC</sub>, V<sub>icm</sub>, etc.) are not reliable in any way.

In *Section 4.3*, the electrical characteristics resulting from the use of these macromodels are presented.

#### 4.2 Electrical characteristics from macromodelization

Table 6. Electrical characteristics resulting from macromodel simulation at  $V_{CC} = 3 \text{ V}$ ,  $V_{DD} = 0 \text{ V}$ ,  $R_L$ ,  $C_L$  connected to  $V_{CC/2}$ ,  $T_{amb} = 25 ^{\circ}\text{C}$  (unless otherwise specified)

Symbol	Conditions	Value	Unit
V <sub>io</sub>		0	mV
A <sub>vd</sub>	$R_L = 10 \text{ k}\Omega$	200	V/mV
I <sub>CC</sub>	No load, per operator	1.2	mA
V <sub>icm</sub>		-0.2 to 3.2	V
V <sub>OH</sub>	$R_L = 10 \text{ k}\Omega$	2.95	V
V <sub>OL</sub>	$R_L = 10 \text{ k}\Omega$	25	mV
I <sub>sink</sub>	V <sub>O</sub> = 3 V	80	mA
I <sub>source</sub>	V <sub>O</sub> = 0 V	80	mA
GBP	$R_L = 600 \text{ k}\Omega$	4	MHz
SR	$R_L = 10 \text{ k}\Omega$ , $C_L = 100 \text{ pF}$	1.3	V/µs
фm	$R_L = 600 \text{ k}\Omega$	68	Degrees

TS921 Macromodel

#### 4.3 Macromodel code

```
** Standard Linear Ics Macromodels, 1996.
** CONNECTIONS:
* 1 INVERTING INPUT
* 2 NON-INVERTING INPUT
* 3 OUTPUT
* 4 POSITIVE POWER SUPPLY
* 5 NEGATIVE POWER SUPPLY
.SUBCKT TS921 1 3 2 4 5 (analog)
                              ****** .MODEL MDTH D
IS=1E-8 KF=2.664234E-16 CJO=10F
* INPUT STAGE
CIP 2 5 1.000000E-12
CIN 1 5 1.000000E-12
EIP 10 5 2 5 1
EIN 16 5 1 5 1
RIP 10 11 8.125000E+00
RIN 15 16 8.125000E+00
RIS 11 15 2.238465E+02
DIP 11 12 MDTH 400E-12
DIN 15 14 MDTH 400E-12
VOFP 12 13 DC 153.5u
VOFN 13 14 DC 0
IPOL 13 5 3.200000E-05
CPS 11 15 1e-9
DINN 17 13 MDTH 400E-12
VIN 17 5 -0.100000e+00
DINR 15 18 MDTH 400E-12
VIP 4 18 0.400000E+00
FCP 4 5 VOFP 1.865000E+02
FCN 5 4 VOFN 1.865000E+02
FIBP 2 5 VOFP 6.250000E-03
FIBN 5 1 VOFN 6.250000E-03
* GM1 STAGE **********
FGM1P 119 5 VOFP 1.1
FGM1N 119 5 VOFN 1.1
RAP 119 4 2.6E+06
RAN 119 5 2.6E+06
* GM2 STAGE **********
G2P 19 5 119 5 1.92E-02
G2N 19 5 119 4 1.92E-02
R2P 19 4 1E+07
R2N 19 5 1E+07
*******
VINT1 500 0 5
```



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```
GCONVP 500 501 119 4 19.38!send ds VP, I(VP) = (V119-V4)/2/Ut VP 501 0 0
GCONVN 500 502 119 5 19.38!send ds VN, I(VN)=(V119-V5)/2/Ut VN 502 0 0
****** orientation isink isource ******
VINT2 503 0 5
FCOPY 503 504 VOUT 1
DCOPYP 504 505 MDTH 400E-9
VCOPYP 505 0 0
DCOPYN 506 504 MDTH 400E-9
VCOPYN 0 506 0
******
F2PP 19 5 poly(2) VCOPYP VP 0 0 0 0 0.5!multiply I(vout)*I(VP)=Iout*(V119-
V4)/2/Ut
F2PN 19 5 poly(2) VCOPYP VN 0 0 0 0 0.5 !multiply I(vout)*I(VN)=Iout*(V119-
V5)/2/Ut
F2NP 19 5 poly(2) VCOPYN VP 0 0 0 0 1.75 !multiply I(vout)*I(VP)=Iout*(V119-
V4)/2/Ut
F2NN 19 5 poly(2) VCOPYN VN 0 0 0 0 1.75 !multiply I(vout)*I(VN)=Iout*(V119-
V5)/2/Ut
* COMPENSATION ********
CC 19 119 25p
* OUTPUT*******
DOPM 19 22 MDTH 400E-12
DONM 21 19 MDTH 400E-12
HOPM 22 28 VOUT 6.250000E+02
VIPM 28 4 5.000000E+01
HONM 21 27 VOUT 6.250000E+02
VINM 5 27 5.000000E+01
VOUT 3 23 0
ROUT 23 19 6
COUT 3 5 1.300000E-10
DOP 19 25 MDTH 400E-12
VOP 4 25 1.052
DON 24 19 MDTH 400E-12
```

VON 24 5 1.052

.ENDS

TS921 Package information

## 5 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK specifications, grade definitions and product status are available at: <a href="www.st.com">www.st.com</a>. ECOPACK is an ST trademark.



Package information TS921

Figure 13. DIP8 package outline

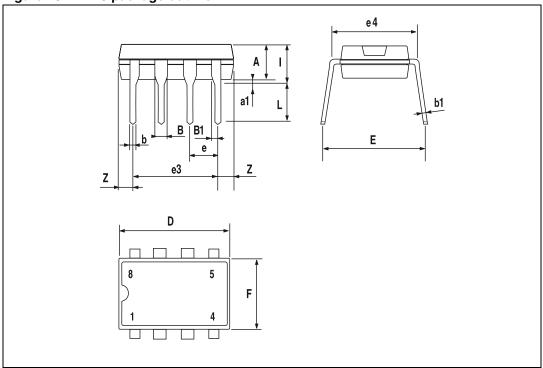


Table 7. DIP8 package mechanical data

	Dimensions							
Symbol		mm		inch				
	Min.	Тур.	Max.	Min.	Тур.	Max.		
А		3.3			0.130			
a1	0.7			0.028				
В	1.39		1.65	0.055		0.065		
B1	0.91		1.04	0.036		0.041		
b		0.5			0.020			
b1	0.38		0.5	0.015		0.020		
D			9.8			0.386		
E		8.8			0.346			
е		2.54			0.100			
e3		7.62			0.300			
e4		7.62			0.300			
F			7.1			0.280		
I			4.8			0.189		
L		3.3			0.130			
Z	0.44		1.6	0.017		0.063		

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TS921 Package information

Figure 14. SO-8 package outline

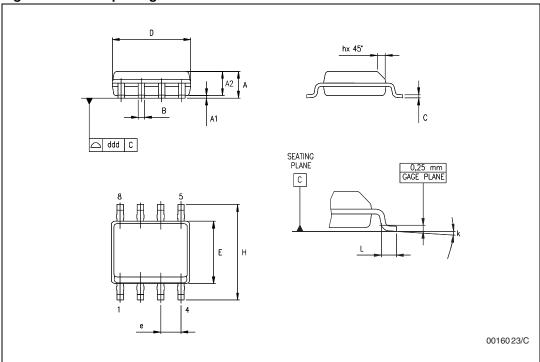


Table 8. SO-8 package mechanical data

	Dimensions					
Symbol		mm			inch	
	Min.	Тур.	Max.	Min.	Тур.	Max.
Α	1.35		1.75	0.053		0.069
A1	0.10		0.25	0.04		0.010
A2	1.10		1.65	0.043		0.065
В	0.33		0.51	0.013		0.020
С	0.19		0.25	0.007		0.010
D	4.80		5.00	0.189		0.197
Е	3.80		4.00	0.150		0.157
е		1.27			0.050	
Н	5.80		6.20	0.228		0.244
h	0.25		0.50	0.010		0.020
L	0.40		1.27	0.016		0.050
k		•	8° (	max.)		
ddd			0.1			0.04

Package information TS921

Figure 15. TSSOP8 package outline

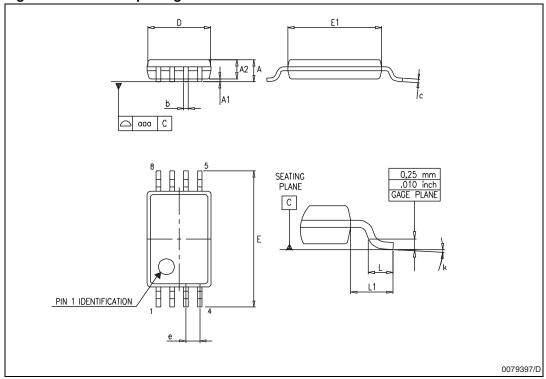


Table 9. TSSOP8 package mechanical data

			Dimens	ions		
Symbol		mm			inch	
	Min.	Тур.	Max.	Min.	Тур.	Max.
А			1.2			0.047
A1	0.05		0.15	0.002		0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
b	0.19		0.30	0.007		0.012
С	0.09		0.20	0.004		0.008
D	2.90	3.00	3.10	0.114	0.118	0.122
E	6.20	6.40	6.60	0.244	0.252	0.260
E1	4.30	4.40	4.50	0.169	0.173	0.177
е		0.65			0.0256	
K	0°		8°	0°		8°
L	0.45	0.60	0.75	0.018	0.024	0.030
L1		1			0.039	

TS921 Revision history

# 6 Revision history

Table 10. Document revision history

Date	Revision	Changes
Feb. 2001	1	Initial release - Product in full production.
Dec. 2004	2	Modifications on AMR table page 2 (explanation of $V_{id}$ and $V_{i}$ limits, ESD, MM and CDM values added, Rthja added)
Nov. 2005	3	The following changes were made in this revision:  PPAP references inserted in the datasheet see <i>Table 1</i> .  Data in tables <i>Electrical characteristics on page 4</i> reformatted for easier use.  Thermal Resistance Junction to Case added in <i>Table 2 on page 3</i> .
19-Sep-2012	4	Updated Figure on page 1(replaced V <sub>CC</sub> - by V <sub>DD</sub> ).  Updated (renamed) <i>Table 1</i> , removed TS921IYD/IYDT devices from <i>Table 1</i> .  Moved <i>Description</i> to page 2.  Updated <i>Figure 1</i> to <i>Figure 4</i> , <i>Figure 6</i> to <i>Figure 12</i> (added conditions to titles).  Updated ECOPACK text and reformatted <i>Section 5</i> (added <i>Table 7</i> to <i>Table 9</i> , reversed order of figures and tables).  Minor corrections throughout document.

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