# VCAN26A2-03G

**Vishay Semiconductors** 

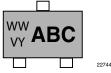
## **Bidirectional Symmetrical (BiSy) Low Capacitance, Dual-Line ESD Protection Diode in SOT-323**



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#### **MARKING** (example only)

**SHA** 



ABC = type code (see table below) WW = date code working week VY = date code year

#### LINKS TO ADDITIONAL RESOURCES



0	SPICE
Models	Models

#### FEATURES

- · For CAN and FLEX-bus applications
- Small SOT-323 package
- T<sub>J</sub> max. = 175 °C
- 2-line ESD protection
- Working range ± 26.5 V
- Low leakage current I<sub>R</sub> < 0.05 μA</li>
- Low load capacitance C<sub>D</sub> < 15 pF
- ESD immunity acc. IEC 61000-4-2 ± 30 kV contact discharge ± 30 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- e3 pins plated with tin (Sn)
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

ORDERING INFORMATION								
	ENVIR	ONMENTAL AN	ID QUALITY CO	DDE	PACKAG			
PART NUMBER (EXAMPLE) AEC-Q10 QUALIFIE		(FU)-FREE LERIVIINATIONS		TIN PLATED	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
QUA	QUALITIED	STANDARD	GREEN	FLATED	15K/BOX = MOQ	10K/BOX = MOQ		
VCAN26A2-03G	-	E		3	-08		VCAN26A2-03G-E3-08	
VCAN26A2-03G	Н	E		3	-08		VCAN26A2-03GHE3-08	
VCAN26A2-03G	-	E		3		-18	VCAN26A2-03G-E3-18	
VCAN26A2-03G	Н	E		3		-18	VCAN26A2-03GHE3-18	

PACKAGE DATA							
DEVICE NAME	EVICE NAME PACKAGE TYPE WEIGHT		MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS		
VCAN26A2-03G	SOT-323	6A2	5.65 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

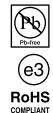
ABSOLUTE MAXIMUM RATINGS						
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	$T_A$ = 25 °C, acc. IEC 61000-4-5; $t_p$ = 8/20 $\mu s;$ single shot	I <sub>PPM</sub>	3	А		
Peak pulse power	$T_A$ = 25 °C; pin 1 or 2 to pin 3; acc. IEC 61000-4-5; $t_p$ = 8/20 $\mu s$ ; single shot	P <sub>PP</sub>	150	W		
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses, $T_A = 25 \text{ °C}$	V	± 30	kV		
	Air discharge acc. IEC 61000-4-2; 10 pulses, $T_A = 25 ^\circ\text{C}$	V <sub>ESD</sub>	± 30	kV		
	Contact discharge acc. ISO10605 330 pF / 330 $\Omega;$ 10 pulses, T_A = 25 °C		± 30	kV		
Operating temperature	Junction temperature	ТJ	-55 to +175	°C		
Storage temperature		T <sub>STG</sub>	-55 to +175	°C		

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1

Document Number: 85894

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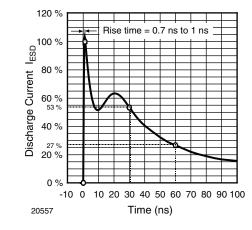


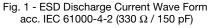
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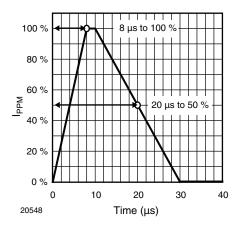
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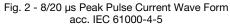
<b>ELECTRICAL CHARACTERISTICS</b> (pin 1 to 3, 3 to 1, 2 to 3, or 3 to 2) (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	MIN.	TYP.	MAX.	UNIT			
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	26.5	V		
Reverse voltage	At I <sub>R</sub> = 0.05 μA	V <sub>R</sub>	26.5	-	-	V		
Reverse current	At V <sub>RWM</sub> = 26.5 V	I <sub>R</sub>	-	-	0.05	μA		
Reverse breakdown voltage	At I <sub>R</sub> = 1 mA	V <sub>BR</sub>	28	30	32	V		
Reverse clamping voltage	At I <sub>PP</sub> 1 A; t <sub>p</sub> = 8/20 μs	V <sub>C</sub>	-	33	40	V		
	At I <sub>PP</sub> = I <sub>PPM</sub> = 3 A; t <sub>p</sub> = 8/20 μs	V <sub>C</sub>	-	40	50	V		
Capacitance	At $V_R = 0 V$ , f = 1 MHz	CD	-	10	15	pF		
	Diode capacitance matching at $V_R = 0 V$ , $C_{D13} vs. C_{D23}$	CD	-	-	1.5	pF		

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)









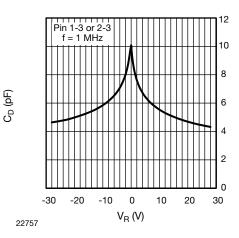


Fig. 3 - Typical Capacitance C<sub>D</sub> vs. Reverse Voltage V<sub>B</sub>

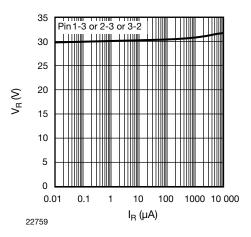


Fig. 4 - Typical Reverse Voltage V<sub>R</sub> vs. Reverse Current I<sub>R</sub>

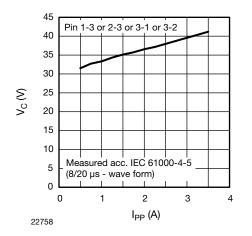
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Fig. 5 - Typical Peak Clamping Voltage V<sub>C</sub> vs. Peak Pulse Current  $I_{PP}$ 

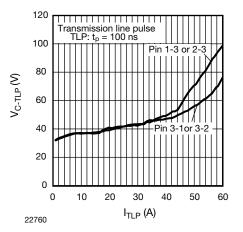
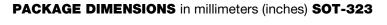
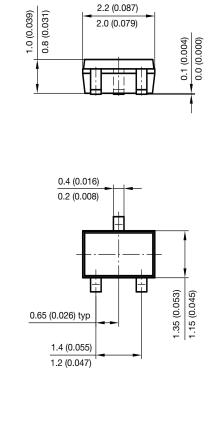


Fig. 6 - Typical Clamping Voltage V<sub>C-TLP</sub> vs. Pulse Current I<sub>TLP</sub>

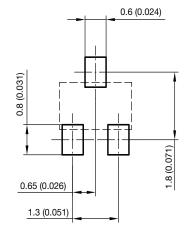




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Rev. 1.5, 08-Feb-2022

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Document Number: 85894

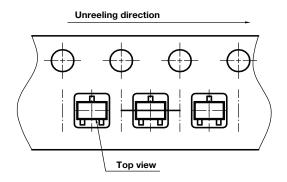
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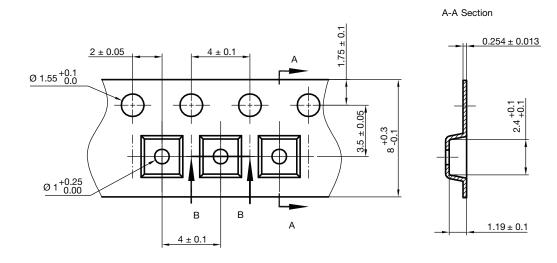
#### **ORIENTATION IN CARRIER TAPE SOT-323**



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#### **CARRIER TAPE SOT-323**



**B-B Section** 

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