RoHS

HALOGEN

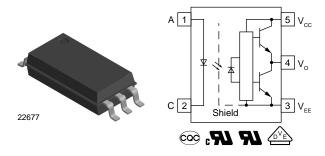
FREE

GREEN

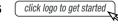


Vishay Semiconductors

Low Profile, 2.5 A Output Current IGBT and MOSFET Driver



DESIGN SUPPORT TOOLS







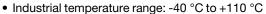


DESCRIPTION

The VOL3120 consists of an infrared light emitting diode optically coupled to an integrated circuit with a power output stage. This optocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control and solar inverter applications. The high operating voltage range of the output stage provides the drive voltages required by gate controlled devices. The voltage and current supplied by this optocoupler makes it ideally suited for directly driving high power IGBTs with ratings up to 1000 V / 100 A. The low profile and small footprint of the VOL3120 makes it an ideal choice for applications where board space and component height are at a premium, while still offering a high degree of isolation performance.

FEATURES

- 2.5 A minimum peak output current
- 48 kV/µs minimum common mode rejection (CMR) at $V_{CM} = 1500 \text{ V}$



- Wide operating V_{CC} range: 15 V to 32 V
- I_{CC} = 2.5 mA maximum supply current
- 0.5 µs maximum propagation delay time
- Under voltage lock-out (UVLO) with hysteresis
- Material categorization: for definitions of

compliance please see www.vishay.com/doc?99912

APPLICATIONS

- · Isolated IGBT / MOSFET gate driver
- · AC and brushless DC motor drives
- Induction stove top
- Industrial inverters
- Switch mode power supplies (SMPS)
- Uninterruptible power supplies (UPS)

AGENCY APPROVALS

The safety application model number covering all products in this datasheet is VOL3120. This model number should be used when consulting safety agency documents.

- UL1577
- cUL
- CQC
- DIN EN 60747-5-5 (VDE 0884) and reinforced insulation rating available with option "1"

ORDERING INFORMATION	
V O L 3 1 2 PART NUMBER	O - X O O # T LSOP-5 PACKAGE OPTION TAPE AND REEL
AGENCY CERTIFIED / PACKAGE	CMR (kV/μs)
UL, cUL, CQC	48
LSOP-5	VOL3120T
UL, cUL, CQC, VDE	48
LSOP-5	VOL3120-X001T



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	CONDITIONS	SYMBOL VALUE				
INPUT						
Input forward current		I _F	25	mA		
Peak transient input current	< 1 µs pulse width, 300 pps	I _{F(TRAN)}	1	А		
Reverse input voltage		V_{R}	5	V		
Output power dissipation		P _{diss}	40	mW		
LED junction temperature		Tj	125	°C		
OUTPUT						
High peak output current (1)		I _{OH(PEAK)}	2.5	А		
Low peak output current (1)		I _{OL(PEAK)}	2.5	А		
Supply voltage		(V _{CC} - V _{EE})	0 to 35	V		
Output voltage		V _{O(PEAK)}	0 to V _{CC}	V		
Output power dissipation		P _{diss}	220	mW		
Output junction temperature		Tj	125	°C		
OPTOCOUPLER						
Storage temperature range		T _{stg}	-55 to +150	°C		
Ambient operating temperature range		T _{amb}	-40 to +110	°C		
Total power dissipation		P _{tot}	260	mW		
Lead solder temperature	For 10 s, 1.6 mm below seating plane	T _{sld}	260	°C		

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability
- (1) Maximum pulse width = 10 μs, maximum duty cycle = 0.2 %. This value is intended to allow for component tolerances for designs with I_O peak minimum = 2.5 A. See applications section for additional details on limiting I_{OH} peak

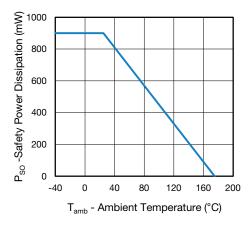


Fig. 1 - Safety Power Dissipation vs. Ambient Temperature

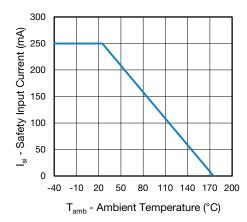


Fig. 2 - Safety Input Current vs. Ambient Temperature



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RECOMMENDED OPERATING CONDITIONS						
PARAMETER	SYMBOL	MIN.	MAX.	UNIT		
Power supply voltage	V _{CC} - V _{EE}	15	32	V		
Input LED current (on)	I _F	10	-	mA		
Input voltage (off)	V _{F(OFF)}	-3	0.8	V		
Operating temperature	T _{amb}	-40	+110	°C		

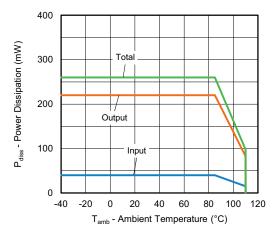


Fig. 3 - Power Dissipation vs. Ambient Temperature

ELECTRICAL CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
High level output ourrent	V _O = (V _{CC} - 4 V)	I _{OH}	0.5	-	-	Α
High level output current	V _O = (V _{CC} - 15 V)	I _{OH}	2.5	-	-	Α
Levelevel eviterit eviment	$V_O = (V_{EE} + 2.5 \text{ V})$	I _{OL}	0.5	-	-	Α
Low level output current	V _O = (V _{EE} + 15 V)	I _{OL}	2.5	-	-	Α
High level output voltage	I _O = -100 mA	V _{OH}	V _{CC} - 4	-	-	V
Low level output voltage	I _O = 100 mA	V _{OL}	-	0.2	0.5	V
High level supply current	Output open, I _F = 10 mA to 16 mA	I _{CCH}	-	-	2.5	mA
Low level supply current	Output open, V _F = -3 V to +0.8 V	I _{CCL}	-	-	2.5	mA
Threshold input current low to high	$I_{O} = 0 \text{ mA}, V_{O} > 5 \text{ V}$	I _{FLH}	-	3.4	8	mA
Threshold input voltage high to low		V_{FHL}	0.8	-	-	V
Input forward voltage	I _F = 10 mA	V_{F}	1	1.36	1.6	V
Temperature coefficient of forward voltage	I _F = 10 mA	$\Delta V_F/\Delta T_{amb}$	-	-1.4	-	mV/°C
Input reverse breakdown voltage	I _R = 10 μA	V_{BR}	5	-	-	V
Input capacitance	f = 1 MHz, V _F = 0 V	C _{IN}	-	45	-	рF
UVLO threshold	V > 5 V 10 mA	V _{UVLO+}	11	-	13.5	V
	$V_0 \ge 5 \text{ V}, I_F = 10 \text{ mA}$	V _{UVLO-}	9.5	-	12	V
UVLO hysteresis		UVLO _{HYS}	-	1.6	-	V
Capacitance (Input to Output)	f = 1 MHz, V _F = 0 V	C _{IO}	-	0.9	-	рF

Note

Minimum and maximum values were tested over recommended operating conditions (T_{amb} = -40 °C to +110 °C, I_{F(ON)} = 10 mA to 16 mA, V_{F(OFF)} = -3 V to 0.8 V, V_{CC} = 15 V to 32 V, V_{EE} = ground) unless otherwise specified. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements. All typical values were measured at T_{amb} = 25 °C and with V_{CC} - V_{EE} = 32 V

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SWITCHING CHARACTERISTICS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Propagation delay time to logic low output	$R_g = 10 \Omega$, $C_g = 10 nF$, $f = 10 kHz$, duty cycle = 50 %	t _{PHL}	0.1	0.25	0.5	μs
Propagation delay time to logic high output	$R_g = 10 \Omega$, $C_g = 10 nF$, $f = 10 kHz$, duty cycle = 50 %	t _{PLH}	0.1	0.25	0.5	μs
Pulse width distortion	$R_g = 10 \Omega$, $C_g = 10 nF$, $f = 10 kHz$, duty cycle = 50 %	PWD	-	-	0.3	μs
Rise time	$R_g = 10 \Omega$, $C_g = 10 nF$, $f = 10 kHz$, duty cycle = 50 %	t _r	-	0.1	-	μs
Fall time	$R_g = 10 \Omega$, $C_g = 10 nF$, $f = 10 kHz$, duty cycle = 50 %	t _f	-	0.1	-	μs
UVLO turn on delay	$V_0 > 5 V$, $I_F = 10 \text{ mA}$	T _{UVLO-ON}	-	0.8	-	μs
UVLO turn off delay	$V_{O} < 5 \text{ V}, I_{F} = 10 \text{ mA}$	T _{UVLO-OFF}	-	0.6	-	μs

Note

Minimum and maximum values were tested over recommended operating conditions (T_{amb} = -40 °C to +110 °C, I_{F(ON)} = 10 mA to 16 mA, V_{F(OFF)} = -3 V to 0.8 V, V_{CC} = 15 V to 32 V, V_{EE} = ground) unless otherwise specified. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements. All typical values were measured at T_{amb} = 25 °C and with V_{CC} - V_{EE} = 32 V

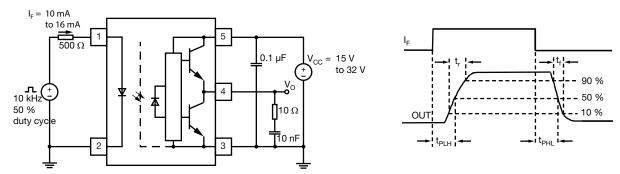


Fig. 4 - t_{PLH} , t_{PHL} , t_{r} and t_{f} Test Circuit and Waveforms

COMMON MODE TRANSIENT IMMUNITY						
PARAMETER TEST CONDITION SYMBOL MIN. TYP. MAX. UN						
Common mode transient immunity at logic high output	T_{amb} = 25 °C, I_F = 10 mA to 16 mA, V_{CM} = 1500 V, V_{CC} = 32 V	CM _H	48	-	-	kV/μs
Common mode transient immunity at logic low output	$T_{amb} = 25 ^{\circ}\text{C}, V_{CM} = 1500 \text{V}, \ V_{CC} = 32 \text{V}, V_{F} = 0 \text{V}$	CM _L	48	-	-	kV/μs

Note

Minimum and maximum values were tested over recommended operating conditions (T_{amb} = -40 °C to +110 °C, I_{F(ON)} = 10 mA to 16 mA, V_{F(OFF)} = -3 V to 0.8 V, V_{CC} = 15 V to 32 V, V_{EE} = ground) unless otherwise specified. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements. All typical values were measured at T_{amb} = 25 °C and with V_{CC} - V_{EE} = 32 V.

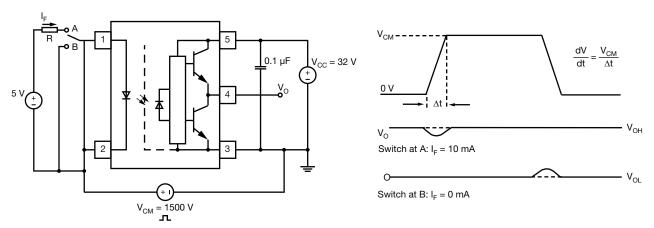


Fig. 5 - CMR Test Circuit and Waveforms

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PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		40 / 110 / 21	
Comparative tracking index		CTI	175	
Maximum rated withstanding isolation voltage	t = 1 min	V _{ISO}	5300	V _{RMS}
Maximum transient isolation voltage		V _{IOTM}	8000	V
Maximum repetitive peak isolation voltage		V _{IORM}	1050	V
Isolation resistance	T _{amb} = 25 °C, V _{DC} = 500 V	R _{IO}	≥ 10 ¹²	Ω
	$T_{amb} = 100 ^{\circ}C, V_{DC} = 500 V$	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	900	mW
Input safety current		I _{SI}	250	mA
Safety temperature		T _S	175	°C
Creepage distance			≥ 8	mm
Clearance distance			≥ 8	mm
Insulation thickness		DTI	≥ 0.4	mm
Input to output test voltage, method B	V_{IORM} x 1.875 = V_{PR} , 100 % production test with t_M = 1 s, partial discharge < 5 pC	V _{PR}	1969	V _{peak}
Input to output test voltage, method A	V_{IORM} x 1.6 = V_{PR} , 100 % production test with t_M = 10 s, partial discharge < 5 pC	V_{PR}	1680	V _{peak}
Environment (pollution degree in accordance to DIN VDE 0109)			2	

Note

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

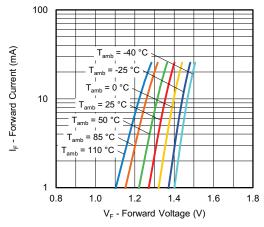


Fig. 6 - Forward Current vs. Forward Voltage

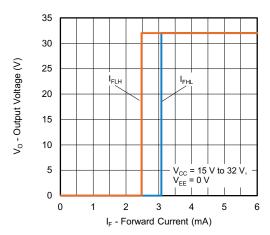


Fig. 7 - Output Voltage vs. Forward Current

[•] As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.



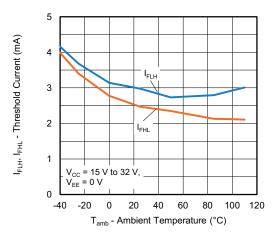


Fig. 8 - Threshold Current vs. Ambient Temperature

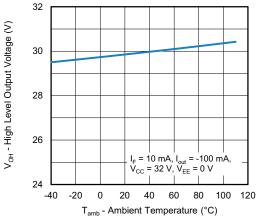


Fig. 9 - High Level Output Voltage vs. Ambient Temperature

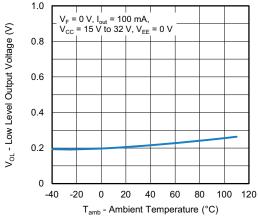


Fig. 10 - Low Level Output Voltage vs. Ambient Temperature

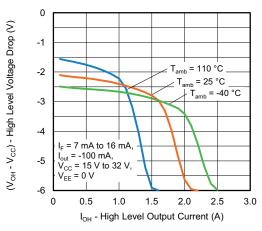


Fig. 11 - High Level Voltage Drop vs. High Level Output Current

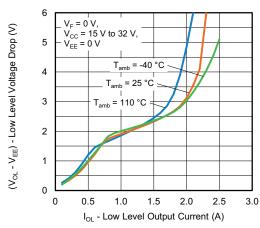


Fig. 12 - Low Level Voltage Drop vs. Low Level Output Current

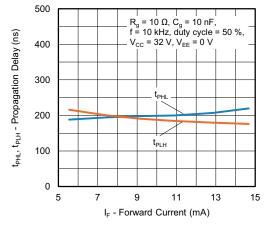


Fig. 13 - Propagation Delay vs. Forward Current

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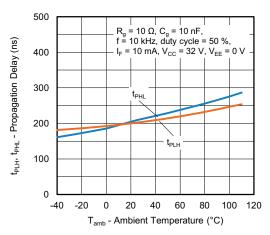


Fig. 14 - Propagation Delay vs. Ambient Temperature

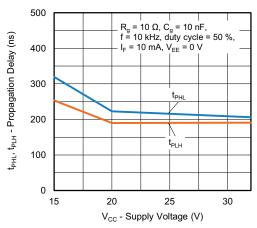


Fig. 15 - Propagation Delay vs. Supply Voltage

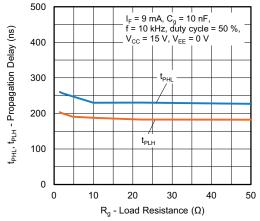


Fig. 16 - Propagation Delay vs. Load Resistance

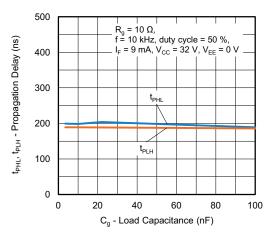


Fig. 17 - Propagation Delay vs. Load Capacitance

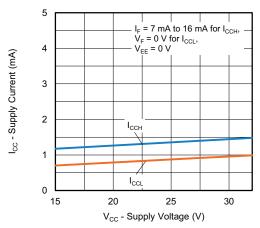


Fig. 18 - Supply Current vs. Supply Voltage

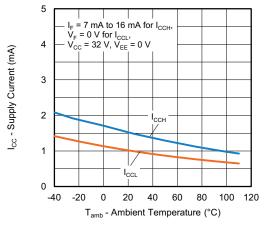


Fig. 19 - Supply Current vs. Ambient Temperature

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PACKAGE DIMENSIONS (in millimeters)

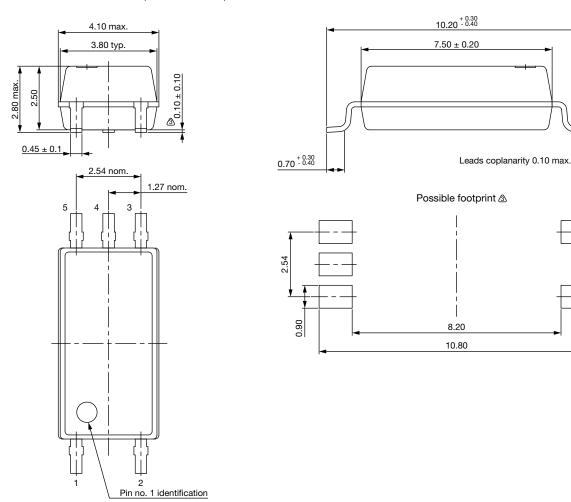


Fig. 20 - Package Drawing

PACKAGE MARKING

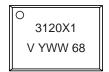


Fig. 21 - Example of VOL3120-X001T

Notes

- "YWW" is the date code marking (Y = year code, WW = week code)
- "X1" is only marked on option "1" parts
- Tape and reel suffix (T) is not part of the package marking

PACKING INFORMATION (tape and reel)

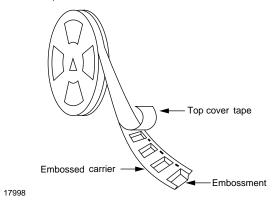
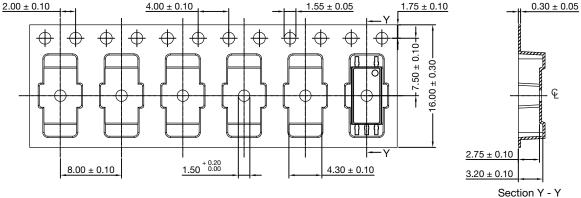


Fig. 22 - Tape and Reel Shipping Medium



Note:

1. Cumulative tolerance of 10 spocket holes is \pm 0.20.

Fig. 23 - Tape and Reel Packing (2000 pieces on reel)

SOLDER PROFILE

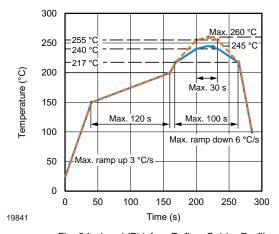


Fig. 24 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020

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