RoHS

COMPLIANT



Vishay Semiconductors

Dual INT-A-PAK Low Profile 3-Level Half Bridge Inverter Stage, 300 A



PRIMARY CHARACTERISTICS				
V _{CES}	600 V			
$V_{CE(on)}$ typical at $I_C = 300 \text{ A}$	1.72 V			
I _C at T _C = 25 °C	379 A			
Speed	8 kHz to 30 kHz			
Package	Dual INT-A-PAK low profile			
Circuit configuration	3-level half bridge inverter stage			

FEATURES

- Trench plus Field Stop IGBT technology
- FRED Pt® antiparallel and clamping diodes
- · Short circuit capability
- · Low stray internal inductances
- · Low switching loss
- UL approved file E78996



· Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATION

- Solar converters
- Uninterruptible power supplies

BENEFITS

- · Direct mounting on heatsink
- · Low junction to case thermal resistance
- Easy paralleling due to positive T_C of V_{CE(sat)}

PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Operating junction temperature	TJ		175		
Storage temperature range	T _{Stg}		-40 to +175	°C	
RMS isolation voltage	V _{ISOL}	T _J = 25 °C, all terminals shorted, f = 50 Hz, t = 1 s	3500		
Collector to emitter voltage	V _{CES}		600	V	
Gate to emitter voltage	V_{GES}		20		
Pulsed collector current	I _{CM}		650		
Clamped inductive load current	I _{LM}		650		
Continuous collector current		T _C = 25 °C	379	Α	
Continuous collector current	I _C	T _C = 80 °C	288		
Danier dissination	P _D	T _C = 25 °C	1250	W	
Power dissipation		T _C = 80 °C	792		
D5 - D6 CLAMPING DIODE					
Repetitive peak reverse voltage	V_{RRM}		600	V	
Single pulse forward current	I _{FSM}	10 ms sine or 6 ms rectangular pulse, T _J = 25 °C	800		
Diode continuous forward current		T _C = 25 °C	215	Α	
Diode continuous forward current	l _F	T _C = 80 °C	161	1	
Dower dissination	Б	T _C = 25 °C	500		
Power dissipation	P _D	T _C = 80 °C	317	W	
D - D2 - D3 - D4 AP DIODE					
Single pulse forward current	I _{FSM}	10 ms sine or 6 ms rectangular pulse, T _J = 25 °C	800		
Diode continuous forward current I _F		T _C = 25 °C	215	Α	
		T _C = 80 °C	161		
Dower discination	В	T _C = 25 °C	500	W	
Power dissipation	P_{D}	T _C = 80 °C	317	7 vv	

Note

Absolute Maximum Ratings indicate sustained limits beyond which damage to the device may occur



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Q1 - Q2 - Q3 - Q4 TRENCH IGBT							
Collector to emitter breakdown voltage	BV_CES	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu\text{A}$	600	-	-		
Collector to emitter voltage		$V_{GE} = 15 \text{ V}, I_{C} = 300 \text{ A}$	-	1.72	2.5	v	
Collector to entitler voltage	$V_{CE(on)}$	$V_{GE} = 15 \text{ V}, I_{C} = 300 \text{ A}, T_{J} = 125 ^{\circ}\text{C}$	-	1.93	-	7 V	
Gate threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_{C} = 16.8 \text{ mA}$	2.9	4.8	7.5		
Temperature coefficient of threshold voltage	$\Delta V_{GE(th)}/\Delta T_{J}$	V _{CE} = V _{GE} , I _C = 1 mA (25 °C to 125 °C)	-	-17.8	-	mV/°C	
Forward transconductance	g _{fe}	V _{CE} = 20 V, I _C = 300 A	-	315	-	S	
Transfer characteristics	V_{GE}	$V_{CE} = 20 \text{ V}, I_{C} = 300 \text{ A}$	-	7.9	-	V	
Zero gate voltage collector current	I _{CES}	V _{GE} = 0 V, V _{CE} = 600 V	-	0.4	250	μА	
		V _{GE} = 0 V, V _{CE} = 600 V, T _J = 125 °C	-	300	-		
Gate to emitter leakage current	I_{GES}	$V_{GE} = \pm 20 \text{ V}, V_{CE} = 0 \text{ V}$	-	-	± 500	nA	
D5 - D6 CLAMPING DIODE							
Cathode to anode blocking voltage	V_{BR}	I _R = 100 μA	600	-	-		
For and other day	\/	I _F = 150 A	-	2.17	2.7	V	
Forward voltage drop V _{FM}		I _F = 150 A, T _J = 125 °C	-	1.61	-		
Reverse leakage current	I _{RM}	V _R = 600 V	-	0.25	200		
		V _R = 600 V, T _J = 125 °C	-	140	-	μA	
D1 - D2 - D3 - D4 AP DIODE							
Forward voltage drop	V _{FM}	I _F = 150 A	-	2.17	2.7	V	
		I _F = 150 A, T _J = 125 °C	-	1.61	-]	

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Q1 - Q2 - Q3 - Q4 TRENCH IGBT		•				
Total gate charge (turn-on)	Qg	I _C = 300 A	-	750	-	
Gate to emitter charge (turn-on)	Q _{ge}	V _{CC} = 400 V	-	210	-	nC
Gate to collector charge (turn-on)	Q_{gc}	V _{GE} = 15 V	-	300	-	
Turn-on switching loss	E _{on}	I _C = 150 A, V _{CC} = 300 V	-	2.1	-	
Turn-off switching loss	E _{off}	V_{GE} = 15 V, R_g = 10 Ω	-	3.1	-	
Total switching loss	E _{tot}	L = 500 μH, T _J = 25 °C	=.	5.2	-	
Turn-on switching loss	E _{on}	$I_C = 300 \text{ A}, V_{CC} = 300 \text{ V}$ $V_{GE} = 15 \text{ V}, R_g = 22 \Omega$	-	8.6	-	
Turn-off switching loss	E _{off}		-	15.4	-	mJ
Total switching loss	E _{tot}	L = 500 μH, T _J = 25 °C	=.	24	-	
Turn-on switching loss	E _{on}		-	2.6	-	
Turn-off switching loss	E _{off}	$I_{C} = 150 \text{ A}$ $V_{CC} = 300 \text{ V}$ $V_{GE} = 15 \text{ V}$ $R_{g} = 10 \Omega$ $L = 500 \mu\text{H}$ $T_{J} = 125 ^{\circ}\text{C}$	-	3.7	-	
Total switching loss	E _{tot}		=.	6.3	-	
Turn-on delay time	t _{d(on)}		=.	453	-	
Rise time	t _r		-	120	-	
Turn-off delay time	t _{d(off)}		-	366	-	ns
Fall time	t _f		-	119	-	



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Q1 - Q2 - Q3 - Q4 TRENCH IGBT			l			I
Turn-on switching loss	E _{on}		-	10.7	-	
Turn-off switching loss	E _{off}	I _C = 300 A	-	15.6	-	mJ
Total switching loss	E _{tot}	V _{CC} = 300 V	-	26.3	-	
Turn-on delay time	t _{d(on)}	$V_{GE} = 15 \text{ V}$	-	840	-	
Rise time	t _r	$R_g = 22 \Omega$ $L = 500 \mu H$	-	279	-	
Turn-off delay time	t _{d(off)}	T _J = 125 °C	-	566	-	ns
Fall time	t _f		-	129	-	
Input capacitance	C _{ies}	V _{GE} = 0 V	-	23.3	-	
Output capacitance	C _{oes}	V _{CC} = 30 V	-	1.7	-	nF
Reverse transfer capacitance	C _{res}	f = 1 MHz	-	0.7	-	
Reverse bias safe operating area	RBSOA	$T_J = 175 ^{\circ}\text{C}$, $I_C = 650 \text{A}$ $V_{CC} = 270 \text{V}$, $V_P = 600 \text{V}$ $R_g = 22 \Omega$, $V_{GE} = 15 \text{V}$ to 0V				
Short circuit safe operating area	SCSOA	$V_{CC} = 400 \text{ V}, V_p = 600 \text{ V}$ $R_g = 10 \Omega, V_{GE} = 15 \text{ V to } 0 \text{ V}$	-	-	5.0	μs
D5 - D6 CLAMPING DIODE	•				•	
Diode reverse recovery time	t _{rr}	V _R = 200 V	-	105	-	ns
Diode peak reverse current	I _{rr}	I _F = 50 A	-	13.5	-	Α
Diode recovery charge	Q _{rr}	dl/dt = 500 A/µs	-	712	-	nC
Diode reverse recovery time	t _{rr}	V _R = 200 V	-	166	-	ns
Diode peak reverse current	I _{rr}	I _F = 50 A	-	24.5	-	Α
Diode recovery charge	Q _{rr}	dl/dt = 500 A/µs, T _J = 125 °C	-	2050	-	nC
D1 - D2 - D3 - D4 AP DIODE						
Diode reverse recovery time	t _{rr}	V _R = 200 V	-	105	-	ns
Diode peak reverse current	I _{rr}	I _F = 50 A	-	13.5	-	Α
Diode recovery charge	Q _{rr}	dl/dt = 500 A/µs	-	712	-	nC
Diode reverse recovery time	t _{rr}	V _R = 200 V	-	166	-	ns
Diode peak reverse current	I _{rr}	I _F = 50 A	-	24.5	-	Α
Diode recovery charge	Q _{rr}	dl/dt = 500 A/µs, T _J = 125 °C	-	2050	-	nC

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS
Junction to case IGBT thermal resistance (per switch)	В	-	-	0.12	
Junction to case diode thermal resistance (per diode)	R _{thJC}	-	-	0.3	°C/W
Case to sink, flat, greased surface (per module)	R _{thCS}	-	0.05	-	
Mounting torque, case to heatsink: M6 screw		4	-	6	Nm
Mounting torque, case to terminal: 1, 2, 3, 4: M5 screw		2	-	5	INIII
Weight		=	270	-	g

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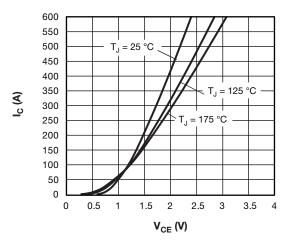


Fig. 1 - Typical Trench IGBT Output Characteristics, $V_{GE} = 15 \text{ V}$

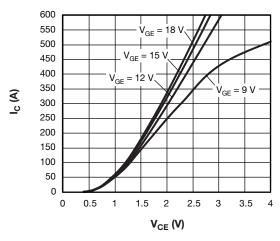


Fig. 2 - Typical Trench IGBT Output Characteristics, T_J = 125 $^{\circ}$ C

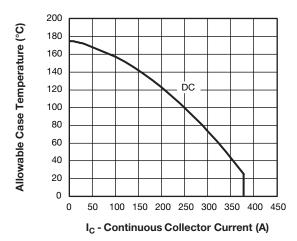


Fig. 3 - Maximum Trench IGBT Continuous Collector Current vs.

Case Temperature (per switch)

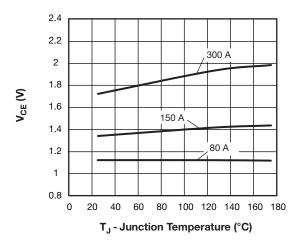


Fig. 4 - Typical Trench IGBT Collector to Emitter Voltage vs. Junction Temperature, $V_{\text{GE}} = 15 \text{ V}$

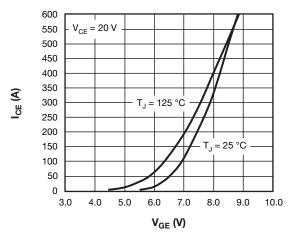


Fig. 5 - Typical Trench IGBT Transfer Characteristics

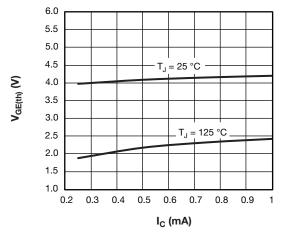


Fig. 6 - Typical Trench IGBT Gate Threshold Voltage

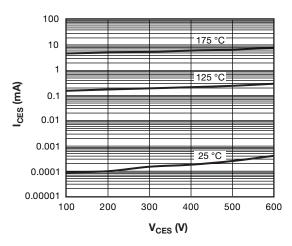


Fig. 7 - Typical Trench IGBT Zero Gate Voltage Collector Current

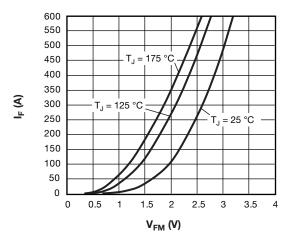


Fig. 8 - Typical Diode Forward Characteristics

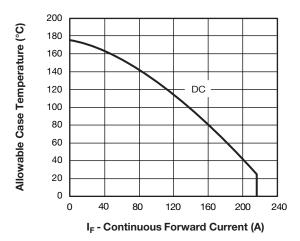


Fig. 9 - Maximum Diode Forward Current vs. Case Temperature

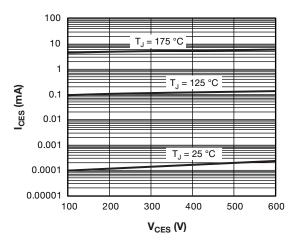


Fig. 10 - Typical Diode Reverse Leakage Current

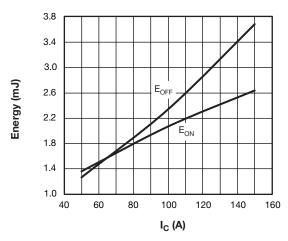


Fig. 11 - Typical Trench IGBT Energy Loss vs. I_C, T_J = 125 °C, V_{CC} = 300 V, R_q = 10 Ω , V_{GE} = 15 V, L = 500 μ H

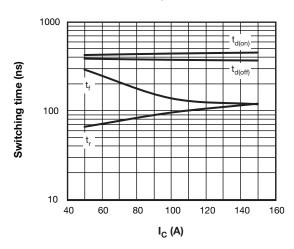


Fig. 12 - Typical IGBT Switching Time vs. $I_C,$ T_J = 125 °C, V_{CC} = 300 V, R_g = 10 $\Omega,$ V_{GE} = 15 V, L = 500 μH



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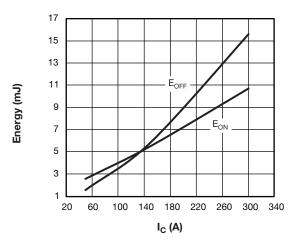


Fig. 13 - Typical Trench IGBT Energy Loss vs. I_C, T_J = 125 °C, V_{CC} = 300 V, R_g = 22 Ω , V_{GE} = 15 V, L = 500 μ H

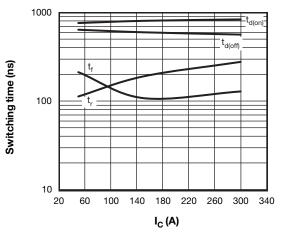


Fig. 14 - Typical IGBT Switching Time vs. $I_C,$ T_J = 125 °C, V_{CC} = 300 V, R_q = 22 $\Omega,$ V_{GE} = 15 V, L = 500 μH

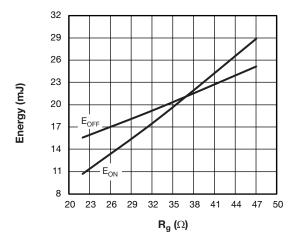


Fig. 15 - Typical Trench IGBT Energy Loss vs.Rg, T_J = 125 °C, V_{CC} = 300 V, I_C = 300 A, V_{GE} = 15 V, L = 500 μH

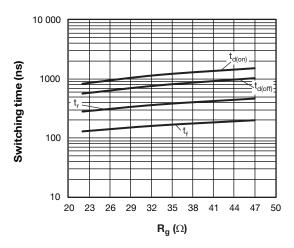


Fig. 16 - Typical Trench IGBT Switching Time vs.Rg, TJ = 125 °C, VCC = 300 V, IC = 300 A, VGE = 15 V, L = 500 μ H

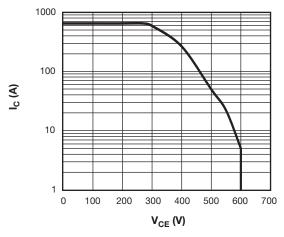


Fig. 17 - Trench IGBT Reverse Bias SOA $T_J = 175$ °C, $V_{GE} = 15$ V, $R_q = 22$ Ω

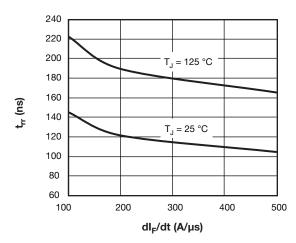
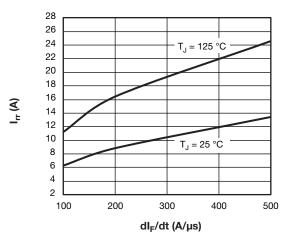


Fig. 18 - Typical Diode Reverse Recovery Time vs. dI_F/dt, $V_{rr} = 200 \ V, \ I_F = 50 \ A$





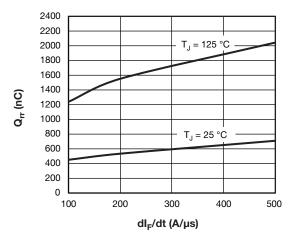


Fig. 19 - Typical Diode Reverse Recovery Current vs. dI_F/dt , $V_{rr} = 200 \text{ V}$, $I_F = 50 \text{ A}$

Fig. 20 - Typical Diode Reverse Recovery Charge vs. dI_F/dt , $V_{rr} = 200 \text{ V}, I_F = 50 \text{ A}$

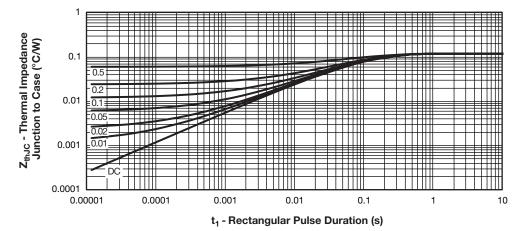


Fig. 21 - Maximum Thermal Impedance Z_{thJC} Characeristics (Trench IGBT)

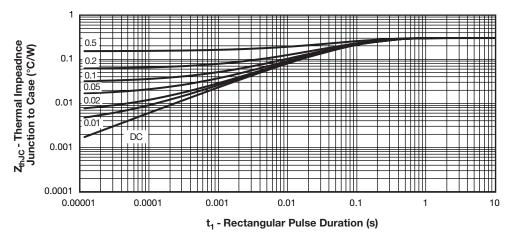
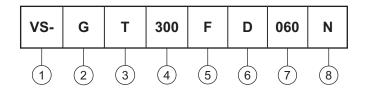


Fig. 22 - Maximum Thermal Impedance ZthJC Characeristics (Diode)

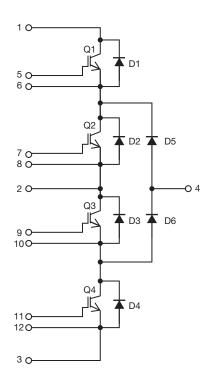
ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- Insulated gate bipolar transistor
- **3** T = trench IGBT
- Current rating (300 = 300 A)
- **5** F = 3-level circuit configuration
- 6 Package indicator D = dual INT-A-PAK low profile
- 7 Voltage rating (060 = 600 V)
- 8 N = ultrafast

CIRCUIT CONFIGURATION

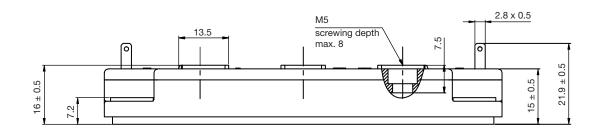


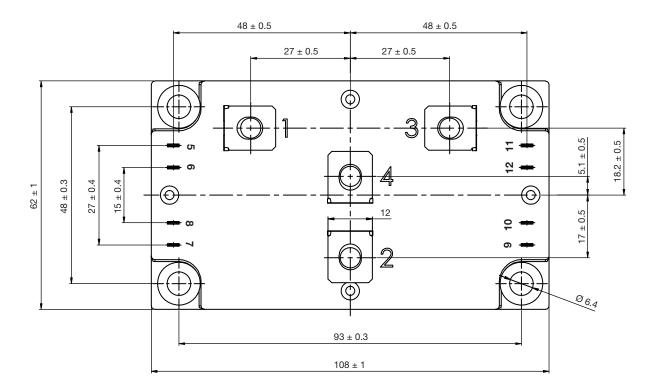
LINKS TO RELATED DOCUMENTS		
Dimensions	www.vishay.com/doc?95515	



DIAP Low Profile - 4 Leads

DIMENSIONS in millimeters





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