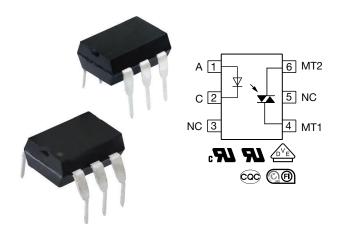
**Vishay Semiconductors** 

# Optocoupler, Phototriac Output, Non-Zero Crossing, 400 V<sub>DRM</sub>



www.vishay.com

#### DESCRIPTION

The K3020P, K3020PG series consists of a phototriac optically coupled to a gallium arsenide infrared-emitting diode in a 6-lead plastic dual inline package.

The non-zero crossing functionality enables full wave control. Featuring galvanic and electrical noise isolation, the output is able to directly switch AC loads or drive medium to high power TRIACs.

### **FEATURES**

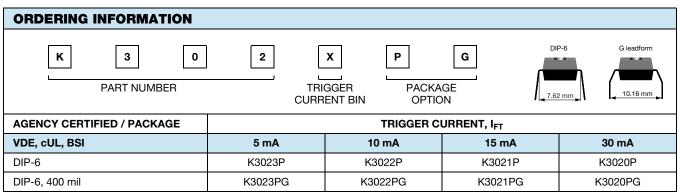
- 400 V blocking voltage
- · Wide range of trigger current
- 100 mA<sub>RMS</sub> on-state current
- Wide temperature range -55 °C to +100 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### APPLICATIONS

- Power TRIAC driver
- Isolated AC load switch
- Air condition
- Heaters
- White goods
- Industrial controls
- Office equipment

#### AGENCY APPROVALS

- UL
- cUL
- DIN EN 60747-5-5 (VDE 0884-5)
- CQC: GB4943-1-2011
- CQC: GB8898-2011
- FIMKO



#### Note

Additional options may be possible, please contact sales office.

COMPLIANT





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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION			UNIT	
INPUT				•	
Reverse voltage		V <sub>R</sub>	5	V	
Forward current		I <sub>F</sub>	80	mA	
Surge current	P.W. < 10 μs	I <sub>FSM</sub>	3	A	
Power dissipation		P <sub>diss</sub>	100	mW	
Junction temperature		Tj	125	°C	
OUTPUT					
Peak off-state voltage		V <sub>DRM</sub>	400	V	
On-state RMS current		I <sub>D(RMS)</sub>	100	mA	
Peak surge current	t <sub>p</sub> ≤ 10 ms	I <sub>FSM</sub>	1.5	A	
Power dissipation		P <sub>diss</sub>	300	mW	
Junction temperature		Tj	125	°C	
COUPLER					
Total power dissipation		P <sub>tot</sub>	350	mW	
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C	
Ambient temperature		T <sub>amb</sub>	-55 to +100	°C	
Lead soldering temperature	2 mm from case, t < 10 s	T <sub>sld</sub>	260	°C	

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						•	•
Forward voltage	I <sub>F</sub> = 50 mA		V <sub>F</sub>	-	1.3	1.6	V
Reverse voltage	I <sub>R</sub> = 10 μA		V <sub>R</sub>	5	-	-	V
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz		Cj	-	50	-	pF
OUTPUT						•	•
Forward peak off-state voltage (repetitive)	I <sub>DRM</sub> = 100 nA		V <sub>DRM</sub> <sup>(1)</sup>	400	-	-	V
Peak on-state voltage	I <sub>TM</sub> = 100 mA		V <sub>TM</sub>	-	1.5	3	V
Critical rate of rise of off-state voltage	$I_F = 0 \text{ A}, V_D = 0.67 \text{ V}_{DRM}$		dV/dt <sub>cr</sub>	-	10	-	V/µs
Critical rate of rise of on-state current commutation	$V_D = 30 V_{RMS}, I_D = 15 mA_{RMS}$		dV/dt <sub>crq</sub>	0.1	0.15	-	V/µs
COUPLER <sup>(2)</sup>						•	•
Emitting diode trigger current		K3020P	I <sub>FT</sub>	-	15	30	mA
		K3020PG	I <sub>FT</sub>	-	15	30	mA
		K3021P	I <sub>FT</sub>	-	8	15	mA
	V - 2V P - 150 O	K3021PG	I <sub>FT</sub>	-	8	15	mA
	$V_S$ = 3 V, $R_L$ = 150 $\Omega$	K3022P I <sub>FT</sub>	-	5	10	mA	
		K3022PG	I <sub>FT</sub>	-	5	10	mA
		K3023P	I <sub>FT</sub>	-	3	5	mA
		K3023PG	I <sub>FT</sub>	-	3	5	mA
Holding current	I <sub>F</sub> = 10 mA, V <sub>S</sub> ≥ 3 V		I <sub>H</sub>	-	200	-	μA

#### Notes

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<sup>(1)</sup> Test voltage must be applied within dV/dt ratings.

<sup>(2)</sup> I<sub>FT</sub> is defined as a minimum trigger current.

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SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification	According to IEC 68 part 1		55 / 100 / 21			
Pollution degree	According to DIN VDE 0109		2			
Comparative tracking index	Insulation group Illa	CTI	175			
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	VISO	4420	V <sub>RMS</sub>		
Tested withstanding isolation voltage	According to UL1577, t = 1 s		5300	V <sub>RMS</sub>		
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>		
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	VIORM	890	V <sub>peak</sub>		
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω		
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω		
Output safety power		P <sub>SO</sub>	265	mW		
Input safety current		I <sub>SI</sub>	130	mA		
Input safety temperature		T <sub>S</sub>	150	°C		
Creepage distance	DIP-6		≥7	mm		
Clearance distance	DIP-6		≥7	mm		
Creepage distance			≥ 8	mm		
Clearance distance	DIP-6, 400 mil		≥ 8	mm		
Insulation thickness		DTI	≥ 0.4	mm		
Input to output test voltage, method A	$V_{IORM} x 1.6 = V_{PR}$ , 100 % sample test with $t_M = 10$ s, partial discharge < 5 pC	V <sub>PR</sub>	1424	V <sub>peak</sub>		

Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

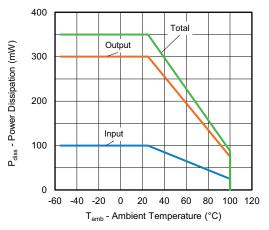


Fig. 1 - Total Power Dissipation vs. Ambient Temperature

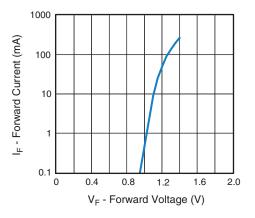
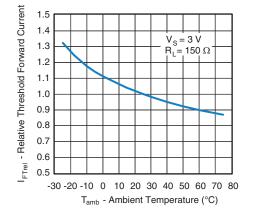
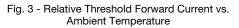


Fig. 2 - Forward Current vs. Forward Voltage







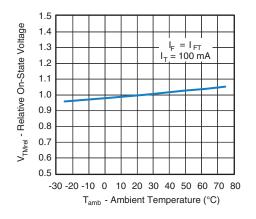


Fig. 4 - Relative On-State vs. Ambient Temperature

## **Vishay Semiconductors**

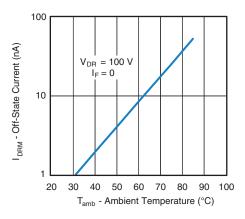


Fig. 5 - Off-State Current vs. Ambient Temperature

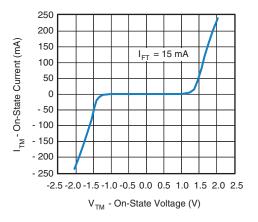
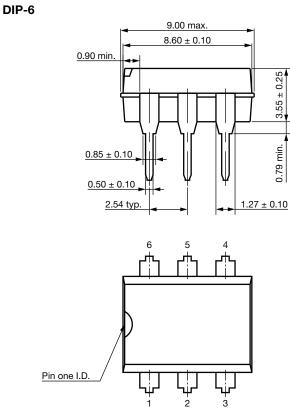


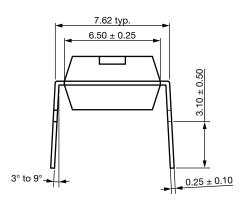
Fig. 6 - On-State Current vs. On-State Voltage



### **PACKAGE DIMENSIONS** (in millimeters)



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10.16 typ.

7.62 typ.

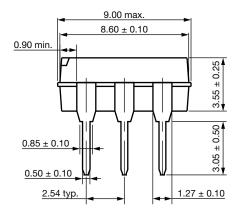
 $6.50 \pm 0.25$ 

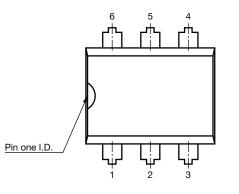
 $10.55 \pm 0.40$ 

0.10 min.

4

#### DIP-6, 400 mil





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0.25 ± 0.10

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## **Vishay Semiconductors**

#### **PACKAGE MARKING**



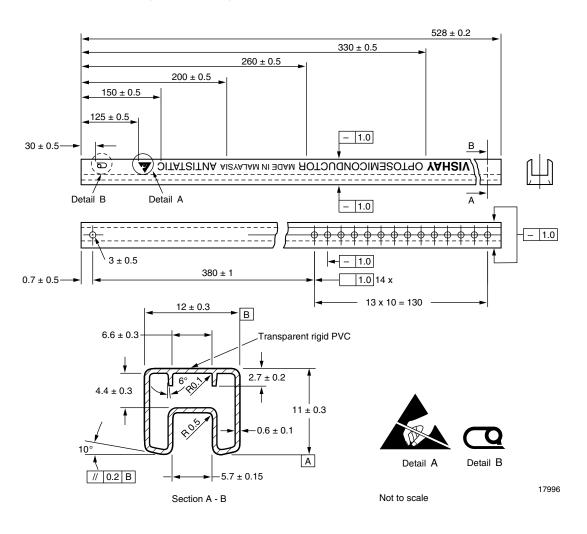
Fig. 7 - Example of K3020P

#### Notes

- The "G" of the G leadform type is not marked on the body.
- The VDE logo is only marked on ption1 parts.

#### **PACKAGING INFORMATION** (in millimeters)

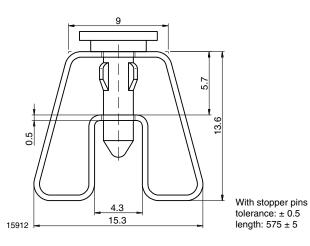
#### DIP-6





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DIP-6, 400 mil



#### SHIPPING INFORMATION

DEVICES PER TUBS						
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX			
DIP-6	50	40	2000			

### SOLDER PROFILES

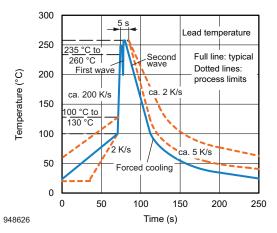


Fig. 8 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

#### HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited Conditions:  $T_{amb} < 30$  °C, RH < 85 % Moisture sensitivity level 1, according to J-STD-020



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