Vishay Semiconductors

Silicon NPN Phototransistor



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21568

VEMT2000X01

DESCRIPTION

VEMT2000X01 series are silicon NPN epitaxial planar phototransistors with daylight blocking filter in a miniature, black dome lens package for surface mounting. Filter bandwidth is matched with 830 nm to 950 nm IR emitters.

FEATURES

- Package type: surface mount
- Package form: GW, RGW
- Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.8
- AEC-Q101 gualified
- High radiant sensitivity
- · Daylight blocking filter matched with 830 nm to 950 nm IR emitters
- · Fast response times
- Angle of half sensitivity: $\varphi = \pm 15^{\circ}$
- · Package matched with IR emitter series VSMB2000X01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- · Lead (Pb)-free reflow soldering
- · Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

Note

Please see document "Vishay Material Category Policy": www.vishay.com/doc?99902

APPLICATIONS

- · Detector in automotive applications
- Photo interrupters
- Miniature switches
- Counters
- Encoders
- Position sensors

PRODUCT SUMMARY				
COMPONENT	I _{ca} (mA)	φ (deg)	λ _{0.5} (nm)	
VEMT2000X01	6	± 15	790 to 970	
VEMT2020X01	6	± 15	790 to 970	

Note

Test condition see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	RDERING CODE PACKAGING REMARKS PACKAGE		PACKAGE FORM	
VEMT2000X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing	
VEMT2020X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing	

Note

MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
TEST CONDITION	SYMBOL	VALUE	UNIT	
	V _{CEO}	20	V	
	V _{ECO}	7	V	
	Ι _C	50	mA	
		TEST CONDITION SYMBOL V _{CEO} V _{ECO}	TEST CONDITION SYMBOL VALUE V _{CEO} 20 V _{ECO} 7	

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For technical questions, contact: <u>detectortechsupport@vishay.com</u>

Document Number: 81595





RoHS COMPLIANT GREEN (5-2008)

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Power power dissipation	T _{amb} ≤ 75 °C	Pv	100	mW
Junction temperature		Tj	100	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Soldering temperature	Acc. reflow profile fig. 8	T _{sd}	260	°C
Thermal resistance junction/ambient	Acc. J-STD-051	R _{thJA}	250	K/W

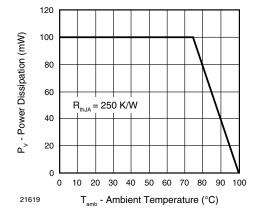


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter breakdown voltage	I _C = 0.1 mA	V _{CEO}	20			V
Collector dark current	$V_{CE} = 5 V, E = 0$	I _{CEO}		1	100	nA
Collector emitter capacitance	$V_{CE} = 0 V, f = 1 MHz, E = 0$	C _{CEO}		25		pF
Collector light current	$\begin{array}{l} E_{e} = 1 \ mW/cm^2, \lambda = 950 \ nm, \\ V_{CE} = 5 \ V \end{array}$	I _{ca}	3	6	9	mA
Angle of half sensitivity		φ		± 15		deg
Wavelength of peak sensitivity		λρ		860		nm
Range of spectral bandwidth		λ _{0.5}		790 to 970		nm
Collector emitter saturation voltage	I _C = 0.05 mA	V _{CEsat}			0.4	V
Temperature coefficient of Ica	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$, $V_{CE} = 5 \text{ V}$	Tk _{lca}		1.1		%/K

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BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

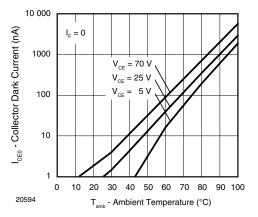


Fig. 2 - Collector Dark Current vs. Ambient Temperature

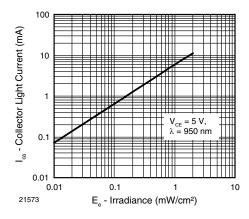


Fig. 3 - Collector Light Current vs. Irradiance

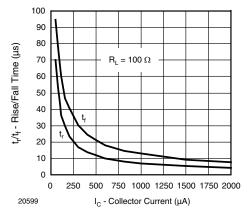


Fig. 4 - Rise/Fall Time vs. Collector Current

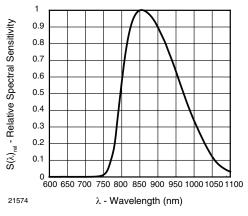


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

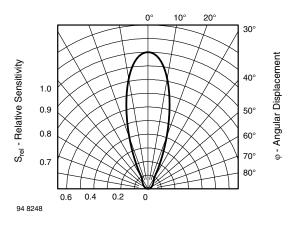


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

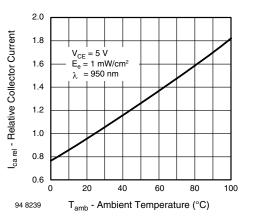


Fig. 7 - Relative Collector Current vs. Ambient Temperature

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REFLOW SOLDER PROFILE

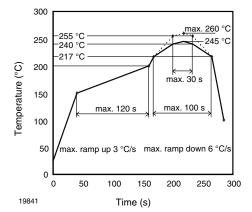


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

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DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

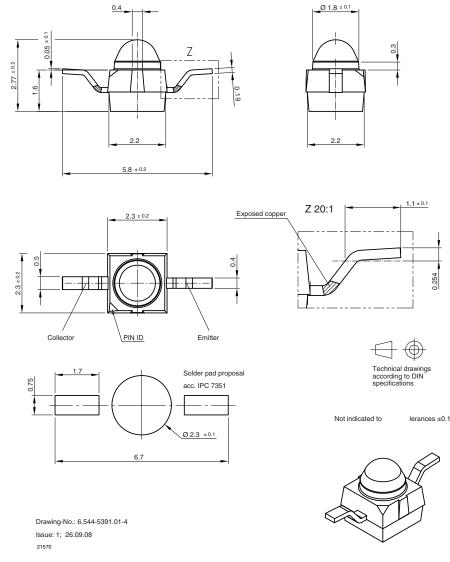
FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label: Floor life: 4 weeks Conditions: $T_{amb} < 30$ °C, RH < 60 % Moisture sensitivity level 2a, acc. to J-STD-020.

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

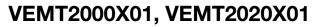




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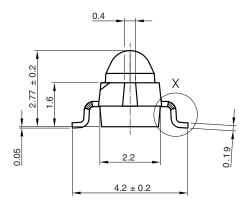
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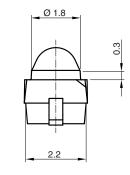


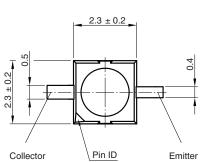


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PACKAGE DIMENSIONS VEMT2020X01 in millimeters







Solder pad proposal

2.45 5.15

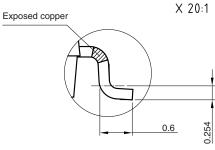
acc. IPC 7351

Drawing-No.: 6.544-5383.01-4

Issue: 4; 28.01.09

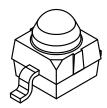
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0.75



Technical drawings according to DIN specifications

Not indicated tolerances ± 0.1



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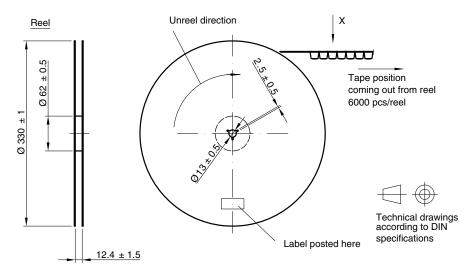
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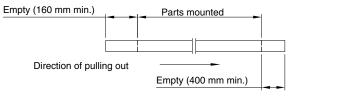


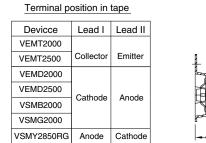
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TAPE AND REEL DIMENSIONS VEMT2000X01 in millimeters

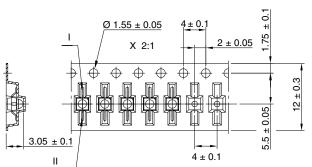


Leader and trailer tape:





Drawing-No.: 9.800-5100.01-4 Issue: 2; 18.03.10 ²¹⁵⁷²



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Vishay Semiconductors TAPE AND REEL DIMENSIONS VEMT2020X01 in millimeters Х Reel Unreel direction S 5 62± 0. Tape position coming out from reel 6000 pcs/reel Ø Ø 330 ± 1 technical drawings according to DIN specifications Label posted here 12.4 ± 1.5 Leader and trailer tape: Empty (160 mm min.) Parts mounted Direction of pulling out Empty (400 mm min.) Terminal position in tape 0.1 Ø 1.55 ± 0.05 4 ± 0.1 + Devicce Lead I Lead II 75 2 ± 0.05 X 2:1 **VEMT2020** Collector Emitter **VEMT2520** \oplus \oplus \oplus \oplus \oplus VSMB2020 12 ± 0.3 VSMG2020 Cathode Anode ± 0.05 VEMD2020 VEMD2520 5.5 VSMY2850G 3.05 ± 0.1 Cathode Anode 4 ± 0.1 Π

Drawing-No.: 9.800-5091.01-4 Issue: 3; 18.03.10 21571

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