Thick Film Resistor/Capacitor Networks, Single-In-Line, Conformal Coated SIP

FEATURES
- Isolated and bussed schematics available
- Thick film resistors
- NP0 or X7R capacitors for line terminator
- Wide operating temperature range (- 55 °C to 125 °C)
- Custom Resistor/Capacitor schematics available
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

Note
* Lead (Pb)-containing terminations are not RoHS-compliant. Exemptions may apply.

Pb Available

e1

RoHS* COMPLIANT

HALOGEN FREE

STANDARD ELECTRICAL SPECIFICATIONS

MODEL	SCHEMATIC	RESISTOR CHARACTERISTICS				CAPACITOR CHARACTERISTICS			
		POWER RATING $P_{70\,°C}$ W	RESISTANCE RANGE Ω	RESISTANCE TOLERANCE [1] ± %	TEMP. COEFF. ± ppm/°C	TYPE [2]	CAPACITANCE RANGE	CAPACITANCE TOLERANCE [3] ± %	CAPACITANCE VOLTAGE V_{DC}
TRC	01	0.20	10 to 1M	1, 2, 5	150	NP0	33 pF to 3900 pF	10, 20	50
						X7R	470 pF to 0.1 µF	10, 20	
	02	0.20	10 to 1M	1, 2, 5	150	NP0	33 pF to 3900 pF	10, 20	50
						X7R	470 pF to 0.1 µF	10, 20	
	09	0.20	10 to 1M	1, 2, 5	150	NP0	33 pF to 3900 pF	10, 20	50
						X7R	470 pF to 0.1 µF	10, 20	

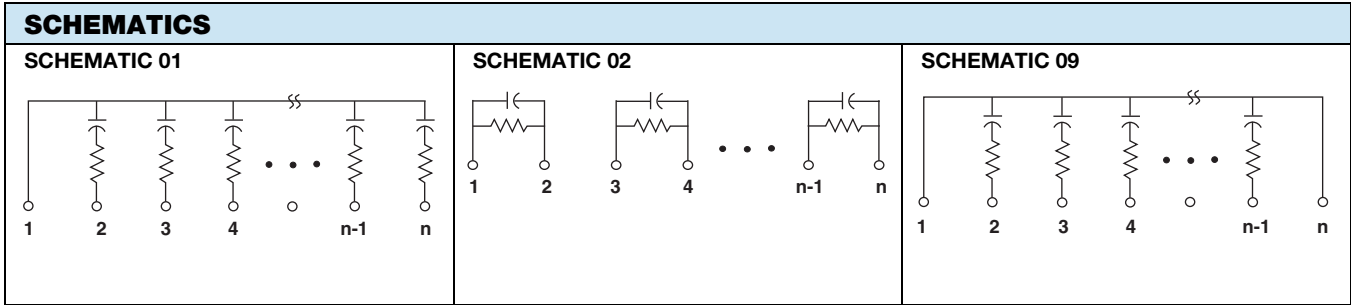**Notes**
[1] ± 2 % standard, ± 1 % and ± 5 % available
[2] NP0 capacitors may be substituted for X7R capacitors
[3] Tighter tolerances available on request

GLOBAL PART NUMBER INFORMATION

New Global Part Numbering: TRC0801N101J560KTB (preferred part number format)

T	R	C	0	8	0	1	N	1	0	1	J	5	6	0	K	T	B

GLOBAL MODEL	PIN COUNT	SCHEMATIC	CHARACTERISTICS	RESISTANCE VALUE	RESISTANCE TOLERANCE	CAPACITANCE VALUE	CAPACITANCE TOLERANCE	TERMINAL FINISH	PACKAGING
TRC	06 to 12 pin available **06** = 6 pin **08** = 8 pin **12** = 12 pin	**01** **02** **09**	**N** = NP0 **X** = X7R	2 digit significant figure, followed by a multiplier **101** = 100 Ω **220** = 22 Ω **102** = 1 kΩ	**F** = 1 % **G** = 2 % **J** = 5 %	(In picofarads) 2 digit significant figure, followed by a multiplier **101** = 100 pF **392** = 3000 pF **104** = 0.1 µF	**K** = 10 % **M** = 20 %	**T** = Sn90/Pb10 **C** = Sn95.5/ Ag3.9/ Cu0.6	**B** = Bulk

Historical Part Numbering: TRC0801101J560KS10 (will continue to be accepted)

TRC	08	01	101	J	560	K	S10
HISTORICAL MODEL	PIN COUNT	SCHEMATIC	RESISTANCE VALUE	RESISTANCE TOLERANCE	CAPACITANCE VALUE	CAPACITANCE TOLERANCE	TERMINAL FINISH

Notes
- For additional information on packaging, refer to the Through-hole Network Packaging document (www.vishay.com/doc?31542).

For technical questions, contact: te1resistors@vishay.com
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SCHEMATICS

SCHEMATIC 01

1 2 3 4 n-1 n

SCHEMATIC 02

1 2 3 4 n-1 n

SCHEMATIC 09

1 2 3 4 n-1 n

Note

• Custom schematics available

DIMENSIONS in inches (millimeters)
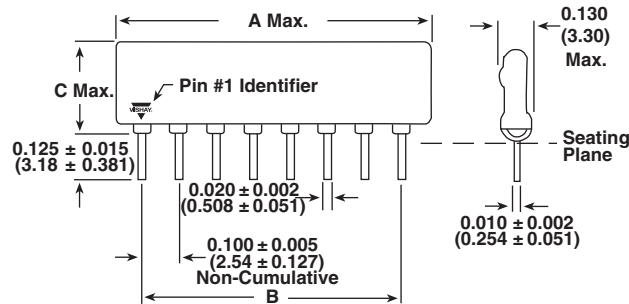
NUMBER OF PINS	A (Max.)	B ± 0.005 (0.127)	C (Max.)
6	0.590 (14.99)	0.500 (12.70)	0.350 (8.89)
7	0.690 (17.53)	0.600 (15.24)	0.350 (8.89)
8	0.790 (20.07)	0.700 (17.78)	0.350 (8.89)
9	0.890 (22.61)	0.800 (20.32)	0.350 (8.89)
10	0.990 (25.15)	0.900 (22.86)	0.350 (8.89)
11	1.09 (27.69)	1.00 (25.40)	0.350 (8.89)
12	1.19 (30.23)	1.10 (27.94)	0.350 (8.89)

Note

• Other sizes available

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