RoHS

COMPLIANT

HALOGEN

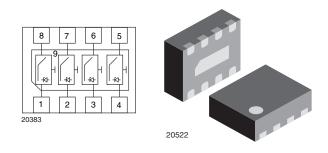
FREE

<u>GREEN</u>

(5-2008)

## **Vishay Semiconductors**

4-Channel EMI-Filter with ESD-Protection



www.vishay.com

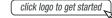
### MARKING (example only)

•YXX

Dot = pin 1 marking Y = type code (see table below) XX = date code

#### **DESIGN SUPPORT TOOLS**





• Low leakage current • Line resistance  $R_S = 100 \ \Omega$ 

4-channel EMI-filter

**FEATURES** 

- Typical cut off frequency  $f_{3dB} = 240 \text{ MHz}$
- ESD-protection acc. IEC 61000-4-2

Ultra compact LLP1713-9L packageLow package profile of 0.6 mm

± 10 kV contact discharge ± 12 kV air discharge

- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

| ORDERING INFORMATION |                           |      |                        |  |  |
|----------------------|---------------------------|------|------------------------|--|--|
| DEVICE NAME          | DEVICE NAME ORDERING CODE |      | MINIMUM ORDER QUANTITY |  |  |
| VEMI45AC-HNH         | VEMI45AC-HNH-GS08         | 3000 | 15 000                 |  |  |

| PACKAGE DATA |                 |              |        |   |                                      |                              |
|--------------|-----------------|--------------|--------|---|--------------------------------------|------------------------------|
| DEVICE NAME  | PACKAGE<br>NAME | TYPE<br>CODE | WEIGHT | MOLDING COMPOUND<br>FLAMMABILITY RATING | MOISTURE<br>SENSITIVITY LEVEL        | SOLDERING CONDITIONS         |
| VEMI45AC-HNH | LLP1713-9L      | С            | 3.7 mg | UL 94 V-0                               | MSL level 1<br>(according J-STD-020) | Peak temperature max. 260 °C |

| ABSOLUTE MAXIMUM RATINGS |   |                  |             |      |  |
|--------------------------|---|------------------|-------------|------|--|
| PARAMETER                | TEST CONDITIONS   | SYMBOL           | VALUE       | UNIT |  |
| Peak pulse current       | All I/O pin to pin 9; acc. IEC 61000-4-5; $t_p = 8/20 \ \mu s;$ single shot | I <sub>PPM</sub> | 2           | А    |  |
| ESD immunity             | Contact discharge acc. IEC 61000-4-2; 10 pulses                             | V                | ± 10        | kV   |  |
|                          | Air discharge acc. IEC 61000-4-2; 10 pulses                                 | V <sub>ESD</sub> | ± 12        | ΓV   |  |
| Operating temperature    | Junction temperature  | ТJ               | -40 to +125 | °C   |  |
| Storage temperature      |   | T <sub>STG</sub> | -55 to +150 | °C   |  |

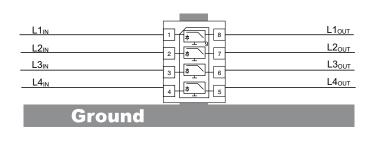
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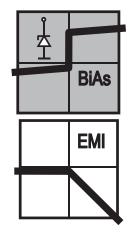
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#### **APPLICATION NOTE**

With the VEMI45AC-HNH 4 different signal or data lines can be filtered and clamped to ground. Due to the different clamping levels in forward and reverse direction the clamping behaviour is <u>Bi</u>directional and <u>Asymmetric</u> (BiAs).





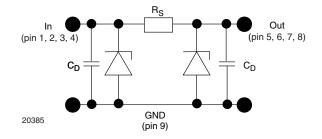
The 4 independent EMI-filter are placed between

pin 1 and pin 8, pin 2 and pin 7, pin 3 and pin 6 and pin 4 and pin 5.

They all are connected to a common ground pin 9 on the backside of the package.

The circuit diagram of one EMI-filter-channel shows two identical Z-diodes at the input to ground and the output to ground. These Z-diodes are characterized by the breakthrough voltage level ( $V_{BR}$ ) and the diode capacitance ( $C_D$ ). Below the breakthrough voltage level the Z-diodes can be considered as capacitors. Together with these capacitors and the line resistance  $R_S$  between input and output the device works as a low pass filter. Low frequency signals ( $f < f_{3dB}$ ) pass the filter while high frequency signals ( $f > f_{3dB}$ ) will be shorted to ground through the diode capacitances  $C_D$ .

20384

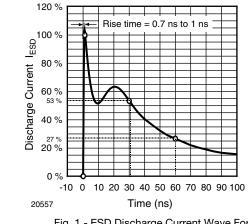


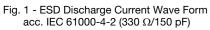
Each filter is symmetrical so that both ports can be used as input or output.

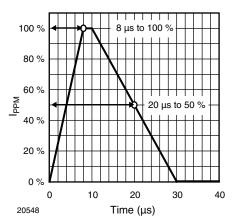


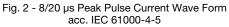
| PARAMETER                  | TEST CONDITIONS/REMARKS   | SYMBOL               | MIN.  | TYP.  | MAX. | UNIT    |
|----------------------------|---|----------------------|-------|-------|------|---------|
| Protection paths           | Number of channels which can be protected   | N <sub>channel</sub> | -     | -     | 4    | channel |
| Reverse stand off voltage  | Max. reverse working voltage  | V <sub>RWM</sub>     | -     | -     | 5    | V       |
| Reverse voltage            | at I <sub>R</sub> = 1 μA  | V <sub>R</sub>       | 5     | -     | -    | V       |
| Reverse current            | at $V_R = V_{RWM}$  | I <sub>R</sub>       | -     | < 0.1 | 1    | μA      |
| Reverse break down voltage | at I <sub>R</sub> = 1 mA  | V <sub>BR</sub>      | 6     | -     | -    | V       |
| Pos. clamping voltage      | at I <sub>PP</sub> = 1 A applied at the input, measured at the output; acc. IEC 61000-4-5     | V <sub>C-out</sub>   | -     | -     | 7    | V       |
|                            | at $I_{PP} = I_{PPM} = 2$ A applied at the input, measured at the output; acc. IEC 61000-4-5  | V <sub>C-out</sub>   | -     | -     | 8    | V       |
| Neg. clamping voltage      | at I <sub>PP</sub> = -1 A applied at the input, measured<br>at the output; acc. IEC 61000-4-5 | V <sub>C-out</sub>   | - 1   | -     | -    | V       |
|                            | at $I_{PP} = I_{PPM} = -2$ A applied at the input, measured at the output; acc. IEC 61000-4-5 | V <sub>C-out</sub>   | - 1.2 | -     | -    | V       |
| Input capacitance          | at $V_R = 0$ V; f = 1 MHz   | C <sub>IN</sub>      | -     | 20    | -    | pF      |
|                            | at V <sub>R</sub> = 2.5 V; f = 1 MHz  | C <sub>IN</sub>      | -     | 13    | -    | pF      |
| ESD-clamping voltage       | at ± 10 kV ESD-pulse acc. IEC 61000-4-2   | V <sub>CESD</sub>    | -     | 7.5   | -    | V       |
| Line resistance            | Measured between input and output; $I_S = 10 \text{ mA}$                                      | R <sub>S</sub>       | 90    | 100   | 110  | Ω       |
| Cut-off frequency          | $V_{IN}$ = 0 V; measured in a 50 $\Omega$ system  | f <sub>3dB</sub>     | -     | 240   | -    | MHz     |

TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)









Rev. 1.7, 09-Jan-2019

3 For technical questions, contact: <u>EMIFilter@vishav.com</u>



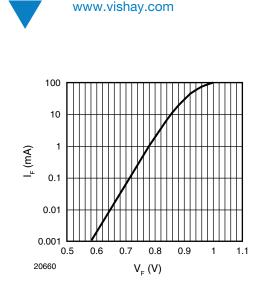


Fig. 3 - Typical Forward Current  $I_{\text{F}}$  vs. Forward Voltage  $V_{\text{F}}$ 

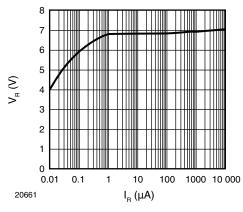


Fig. 4 - Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$ 

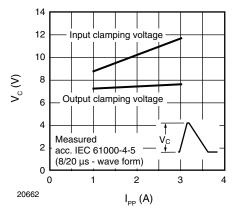


Fig. 5 - Typical Peak Clamping Voltage V\_C vs. Peak Pulse Current  $I_{\text{PP}}$ 

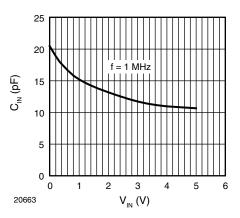


Fig. 6 - Typical Input Capacitance  $C_{IN}$  vs. Input Voltage  $V_{IN}$ 

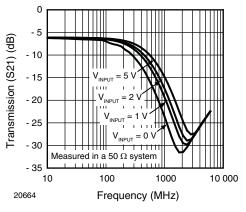


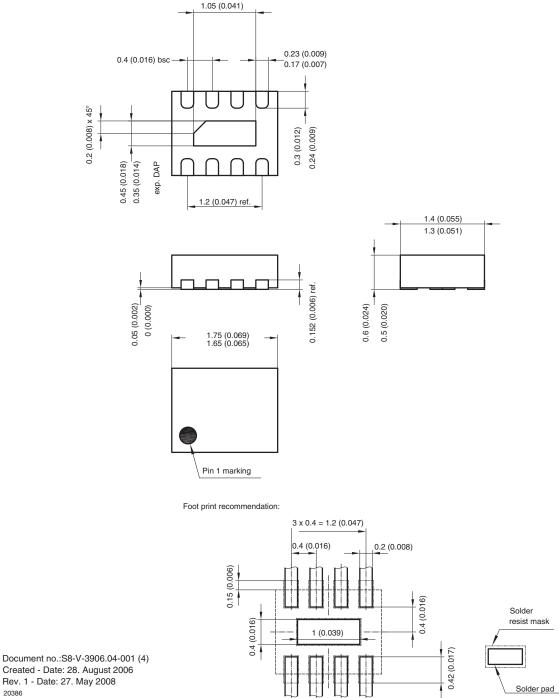
Fig. 7 - Typical Small Signal Transmission (S21) at  $\ Z_{O}$  = 50  $\Omega$ 

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#### PACKAGE DIMENSIONS in millimeters (inches): LLP1713-9L



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