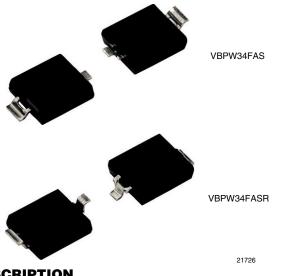
**Vishay Semiconductors** 

## Silicon PIN Photodiode



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DESCRIPTION

VBPW34FAS and VBPW34FASR are high speed and high sensitive PIN photodiodes. It is a surface mount device (SMD) including the chip with a 7.5 mm<sup>2</sup> sensitive area and a daylight blocking filter matched with IR emitters operating at wavelength 870 nm or 950 nm.

#### **FEATURES**

- · Package type: surface mount
- · Package form: GW, RGW
- Dimensions (L x W x H in mm): 6.4 x 3.9 x 1.2
- Radiant sensitive area (in mm<sup>2</sup>): 7.5
- · High radiant sensitivity
- · Daylight blocking filter matched with 870 nm to 950 nm emitters
- Fast response times
- Angle of half sensitivity:  $\varphi = \pm 65^{\circ}$
- · Floor life: 168 h, MSL 3, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- · Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

#### **APPLICATIONS**

- High speed detector for infrared radiation
- Infrared remote control and data free air transmissionsystems, e.g. in combination with TSFFxxxx

PRODUCT SUMMARY				
COMPONENT	I <sub>ra</sub> (μΑ)	φ (deg)	λ0.5 (nm)	
VBPW34FAS	55	± 65	780 to 1050	
VBPW34FASR	55	± 65	780 to 1050	

#### Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	KAGING REMARKS PACH		
VBPW34FAS	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Gullwing	
VBPW34FASR	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Reverse gullwing	

#### Note

MOQ: minimum order guantity

ABSOLUTE MAXIMUM RATINGS (Tamb = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V <sub>R</sub>	60	V
Power dissipation	T <sub>amb</sub> ≤ 25 °C	Pv	215	mW
Junction temperature		Тj	100	°C
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C
Soldering temperature	Acc. reflow sloder profile fig. 8	T <sub>sd</sub>	260	°C
Thermal resistance junction/ambient		R <sub>thJA</sub>	350	K/W



e

RoHS

COMPLIANT

HALOGEN

FREE



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PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	VF		1	1.3	V
Breakdown voltage	I <sub>R</sub> = 100 μA, E = 0	V <sub>(BR)</sub>	60			V
Reverse dark current	V <sub>R</sub> = 10 V, E = 0	I <sub>ro</sub>		2	30	nA
Diada and items	$V_{R} = 0 V, f = 1 MHz, E = 0$	CD		70		pF
Diode capacitance	V <sub>R</sub> = 3 V, f = 1 MHz, E = 0	C <sub>D</sub>		25	40	pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	Vo		350		mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>Vo</sub>		- 2.6		mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	l <sub>k</sub>		50		μA
Temperature coefficient of Ik	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>lk</sub>		0.1		%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}, \\ V_R = 5 \text{ V}$	I <sub>ra</sub>	45	55		μA
Angle of half sensitivity		φ		± 65		deg
Wavelength of peak sensitivity		λρ		950		nm
Range of spectral bandwidth		λ <sub>0.5</sub>		780 to 1050		nm
Noise equivalent power	$V_{R} = 10 \text{ V}, \lambda = 950 \text{ nm}$	NEP		4 x 10 <sup>-14</sup>		W/√Hz
Rise time	$V_{R} = 10 \text{ V},  \text{R}_{L} = 1  \text{k}\Omega,$ $\lambda = 820 \text{ nm}$	t <sub>r</sub>		100		ns
Fall time	$V_R = 10 V, R_L = 1 k\Omega,$ $\lambda = 820 nm$	t <sub>f</sub>		100		ns

BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

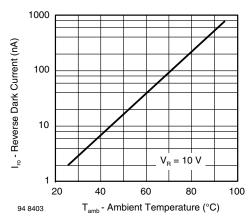


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

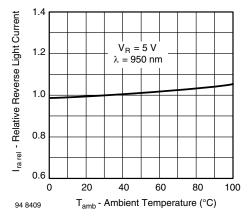


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

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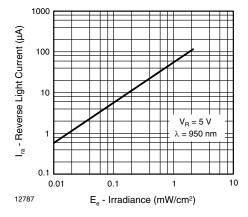


Fig. 3 - Reverse Light Current vs. Irradiance

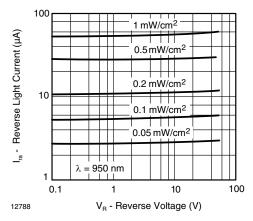


Fig. 4 - Reverse Light Current vs. Reverse Voltage

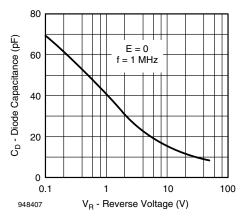


Fig. 5 - Diode Capacitance vs. Reverse Voltage

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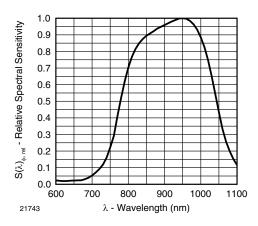


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

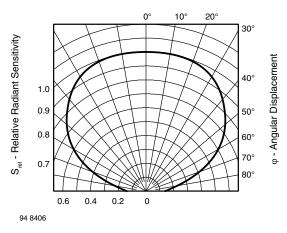


Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement

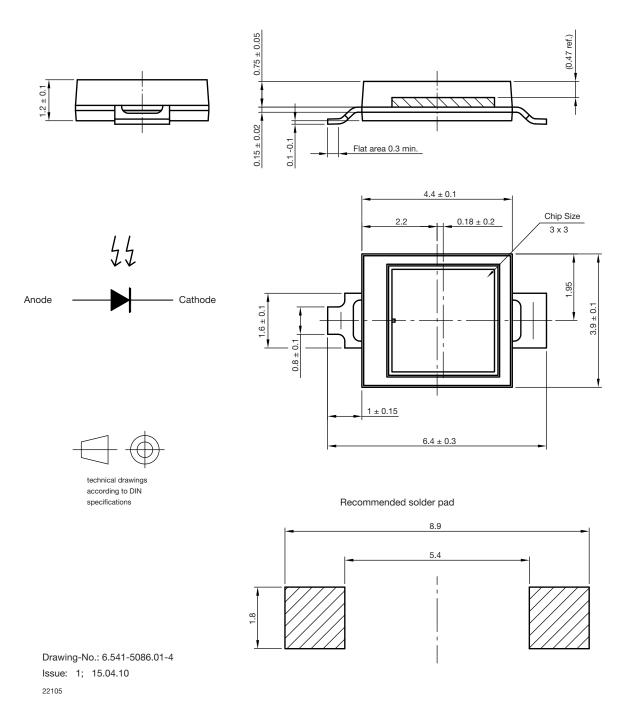
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### PACKAGE DIMENSIONS FOR VBPW34FAS in millimeters



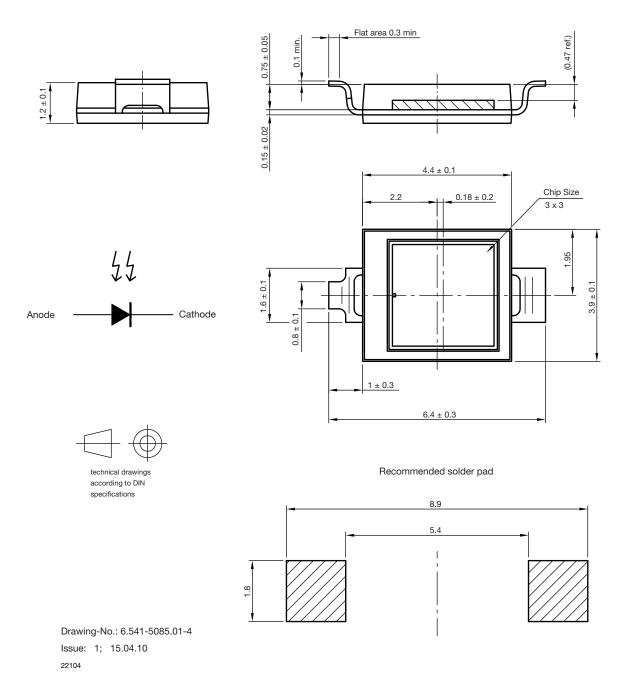
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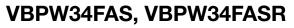
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#### PACKAGE DIMENSIONS FOR VBPW34FASR in millimeters



5

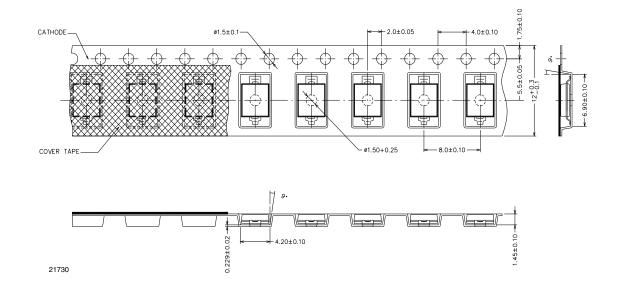
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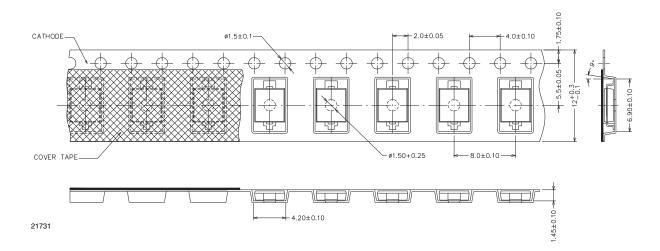


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#### TAPING DIMENSIONS FOR VBPW34FAS in millimeters



#### TAPING DIMENSIONS FOR VBPW34FASR in millimeters

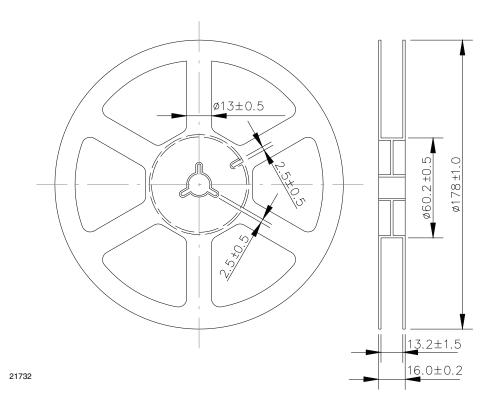


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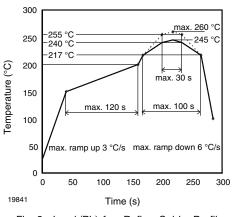


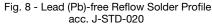
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### REEL DIMENSIONS FOR VBPW34FAS AND VBPW34FASR in millimeters



### SOLDER PROFILE





#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

#### **FLOOR LIFE**

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020: Moisture sensitivity: level 3 Floor life: 168 h Conditions:  $T_{amb} < 30$  °C, RH < 60 %

#### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions: 192 h at 40 °C (+ 5 °C), RH < 5 % or 96 h at 60 °C (+ 5 °C), RH < 5 %.

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