Vishay Semiconductors

Silicon PIN Photodiode



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DESCRIPTION

VBPW34FAS and VBPW34FASR are high speed and high sensitive PIN photodiodes. It is a surface mount device (SMD) including the chip with a 7.5 mm² sensitive area and a daylight blocking filter matched with IR emitters operating at wavelength 870 nm or 950 nm.

FEATURES

- · Package type: surface mount
- · Package form: GW, RGW
- Dimensions (L x W x H in mm): 6.4 x 3.9 x 1.2
- Radiant sensitive area (in mm²): 7.5
- · High radiant sensitivity
- · Daylight blocking filter matched with 870 nm to 950 nm emitters
- Fast response times
- Angle of half sensitivity: $\varphi = \pm 65^{\circ}$
- · Floor life: 168 h, MSL 3, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- · Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

APPLICATIONS

- High speed detector for infrared radiation
- Infrared remote control and data free air transmissionsystems, e.g. in combination with TSFFxxxx

PRODUCT SUMMARY				
COMPONENT	I _{ra} (μΑ)	φ (deg)	λ0.5 (nm)	
VBPW34FAS	55	± 65	780 to 1050	
VBPW34FASR	55	± 65	780 to 1050	

Note

Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	KAGING REMARKS PACH		
VBPW34FAS	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Gullwing	
VBPW34FASR	Tape and reel	MOQ: 1000 pcs, 1000 pcs/reel	Reverse gullwing	

Note

MOQ: minimum order guantity

ABSOLUTE MAXIMUM RATINGS (Tamb = 25 °C, unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V _R	60	V
Power dissipation	T _{amb} ≤ 25 °C	Pv	215	mW
Junction temperature		Тj	100	°C
Operating temperature range		T _{amb}	- 40 to + 100	°C
Storage temperature range		T _{stg}	- 40 to + 100	°C
Soldering temperature	Acc. reflow sloder profile fig. 8	T _{sd}	260	°C
Thermal resistance junction/ambient		R _{thJA}	350	K/W



e

RoHS

COMPLIANT

HALOGEN

FREE



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PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 50 mA	VF		1	1.3	V
Breakdown voltage	I _R = 100 μA, E = 0	V _(BR)	60			V
Reverse dark current	V _R = 10 V, E = 0	I _{ro}		2	30	nA
Diada and items	$V_{R} = 0 V, f = 1 MHz, E = 0$	CD		70		pF
Diode capacitance	V _R = 3 V, f = 1 MHz, E = 0	C _D		25	40	pF
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	Vo		350		mV
Temperature coefficient of Vo	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	TK _{Vo}		- 2.6		mV/K
Short circuit current	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	l _k		50		μA
Temperature coefficient of Ik	$E_e = 1 \text{ mW/cm}^2$, $\lambda = 950 \text{ nm}$	TK _{lk}		0.1		%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2, \lambda = 950 \text{ nm}, \\ V_R = 5 \text{ V}$	I _{ra}	45	55		μA
Angle of half sensitivity		φ		± 65		deg
Wavelength of peak sensitivity		λρ		950		nm
Range of spectral bandwidth		λ _{0.5}		780 to 1050		nm
Noise equivalent power	$V_{R} = 10 \text{ V}, \lambda = 950 \text{ nm}$	NEP		4 x 10 ⁻¹⁴		W/√Hz
Rise time	$V_{R} = 10 \text{ V}, \text{R}_{L} = 1 \text{k}\Omega,$ $\lambda = 820 \text{ nm}$	t _r		100		ns
Fall time	$V_R = 10 V, R_L = 1 k\Omega,$ $\lambda = 820 nm$	t _f		100		ns

BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

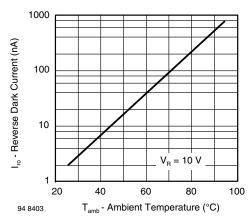


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

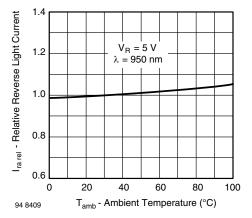


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

2 For technical questions, contact: <u>detectortechsupport@vishay.com</u>



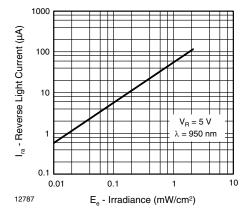


Fig. 3 - Reverse Light Current vs. Irradiance

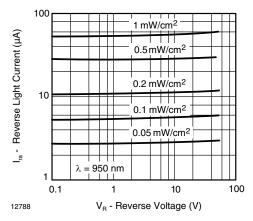


Fig. 4 - Reverse Light Current vs. Reverse Voltage

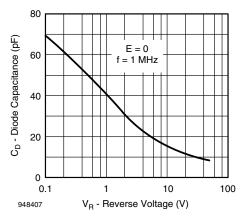


Fig. 5 - Diode Capacitance vs. Reverse Voltage

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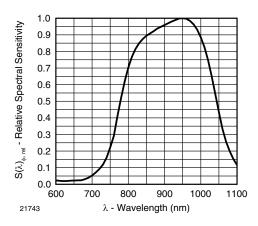


Fig. 6 - Relative Spectral Sensitivity vs. Wavelength

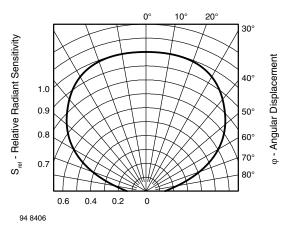


Fig. 7 - Relative Radiant Sensitivity vs. Angular Displacement

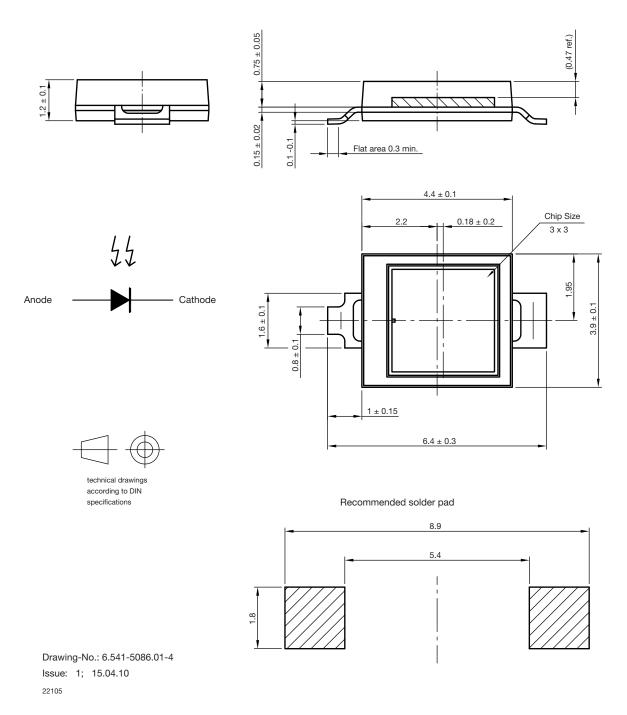
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PACKAGE DIMENSIONS FOR VBPW34FAS in millimeters



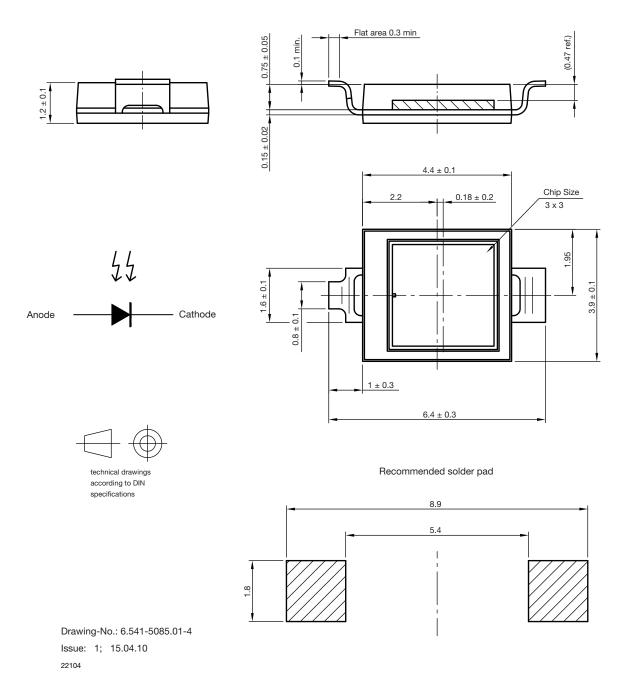
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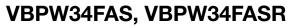
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PACKAGE DIMENSIONS FOR VBPW34FASR in millimeters



5

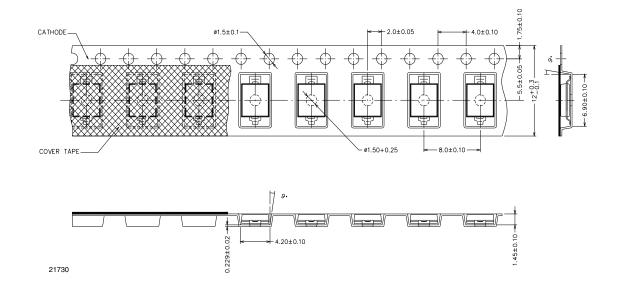
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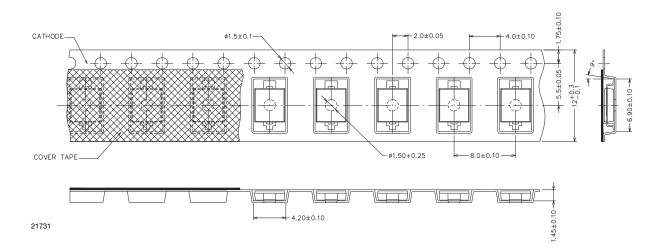


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TAPING DIMENSIONS FOR VBPW34FAS in millimeters



TAPING DIMENSIONS FOR VBPW34FASR in millimeters

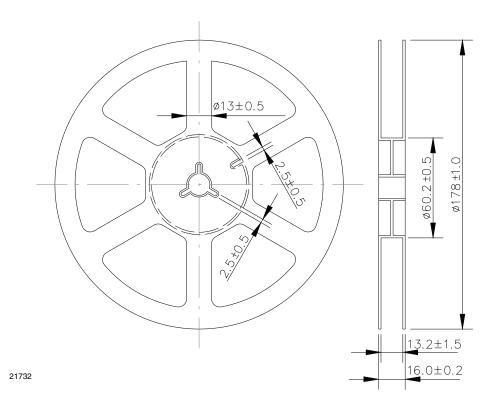


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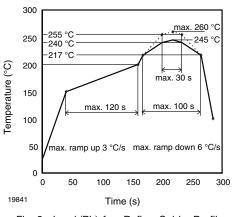


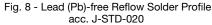
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REEL DIMENSIONS FOR VBPW34FAS AND VBPW34FASR in millimeters



SOLDER PROFILE





DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Time between soldering and removing from MBB must not exceed the time indicated in J-STD-020: Moisture sensitivity: level 3 Floor life: 168 h Conditions: $T_{amb} < 30$ °C, RH < 60 %

DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or recommended conditions: 192 h at 40 °C (+ 5 °C), RH < 5 % or 96 h at 60 °C (+ 5 °C), RH < 5 %.

7 For technical questions, contact: <u>detectortechsupport@vishay.com</u> Document Number: 81127

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