# GB200TS60NPbF

Vishay Semiconductors



## INT-A-PAK "Half-Bridge" (Ultrafast Speed IGBT), 209 A



INT-A-PAK

PRODUCT SUMMARY						
V <sub>CES</sub>	600 V					
I <sub>C</sub> DC	209 A					
V <sub>CE(on)</sub> at 200 A, 25 °C	2.6 V					
Package	INT-A-PAK					
Circuit	Half Bridge with SMD Gate Resistor					

#### FEATURES

Generation 5 Non Punch Through (NPT) technology



ROHS COMPLIANT

- Ultrafast: Optimized for hard switching speed 8 kHz to 60 kHz
- Low V<sub>CE(on)</sub>
- 10 µs short circuit capability
- Square RBSOA
- Positive V<sub>CE(on)</sub> temperature coefficient
- HEXFRED<sup>®</sup> antiparallel diode with ultrasoft reverse recovery characteristics
- Industry standard package
- Al<sub>2</sub>O<sub>3</sub> DBC
- UL approved file E78996
- Designed for industrial level
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### BENEFITS

- Benchmark efficiency for UPS and welding application
- Rugged transient performance
- Direct mounting on heatsink
- Very low junction to case thermal resistance

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V <sub>CES</sub>		600	V	
Continuous collector current		T <sub>C</sub> = 25 °C	209		
Continuous collector current	Ι <sub>C</sub>	T <sub>C</sub> = 80 °C	142		
Pulsed collector current	I <sub>CM</sub>		400	А	
Clamped inductive load current	I <sub>LM</sub>		400	A	
Diode continuous forward current		T <sub>C</sub> = 25 °C	178		
Dide continuous forward current	IF	$T_{\rm C} = 80 \ ^{\circ}{\rm C}$	121		
Gate to emitter voltage	V <sub>GE</sub>		± 20	V	
Movimum nower dissinction	P	$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	781	10/	
Maximum power dissipation	P <sub>D</sub>	T <sub>C</sub> = 80 °C	438	W	
Isolation voltage	V <sub>ISOL</sub>	Any terminal to case, t = 1 min	2500	V	
Operating junction temperature range	TJ		- 40 to + 150	°C	

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<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V <sub>BR(CES)</sub>	$V_{GE} = 0 \text{ V}, \text{ I}_{C} = 500 \mu\text{A}$	600	-	-		
	V <sub>CE(on)</sub>	$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 100 \text{ A}$	-	1.95	2.1		
Collector to emitter voltage		$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 200 \text{ A}$	-	2.6	2.84	N N	
		$V_{GE}$ = 15 V, $I_{C}$ = 100 A, $T_{J}$ = 125 °C	-	2.28	2.5	V	
		$V_{GE}$ = 15 V, $I_{C}$ = 200 A, $T_{J}$ = 125 °C	-	3.14	3.48		
Gate threshold voltage	V <sub>GE(th)</sub>	$V_{CE} = V_{GE}$ , $I_C = 500 \ \mu A$	3	4.2	6		
Collector to emitter leakage current		$V_{GE} = 0 V, V_{CE} = 600 V$	-	0.005	0.2	mA	
Collector to emitter leakage current	ICES	$V_{GE} = 0 \text{ V}, \text{ V}_{CE} = 600 \text{ V}, \text{ T}_{J} = 150 ^{\circ}\text{C}$	-	0.01	15	mA	
		I <sub>C</sub> = 100 A	-	1.39	1.78		
Diode forward voltage drop	V <sub>FM</sub>	I <sub>C</sub> = 200 A	-	1.64	2.2	v	
		I <sub>C</sub> = 100 A, T <sub>J</sub> = 125 °C	-	1.32	1.69		
		I <sub>C</sub> = 200 A, T <sub>J</sub> = 125 °C	-	1.67	2.30		
Gate to emitter leakage current	I <sub>GES</sub>	$V_{GE} = \pm 20 \text{ V}$	-	-	± 200	nA	

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on switching loss	Eon		-	3.65	-	
Turn-off switching loss	E <sub>off</sub>	$I_{C} = 200$ A, $V_{CC} = 360$ V, $V_{GE} = 15$ V, R <sub>g</sub> = 10 Ω, L = 200 µH, T <sub>J</sub> = 25 °C	-	6.9	-	
Total switching loss	E <sub>tot</sub>	···· ··· ··· ··· ··· ··· ··· ··· ··· ·	-	10.55	-	
Turn-on switching loss	Eon		-	3.8	-	mJ
Turn-off switching loss	E <sub>off</sub>		-	7.8	-	
Total switching loss	E <sub>tot</sub>		-	11.6	-	
Turn-on delay time	t <sub>d(on)</sub>	I <sub>C</sub> = 200 A, V <sub>CC</sub> = 360 V, V <sub>GE</sub> = 15 V, R <sub>a</sub> = 10 Ω, L = 200 μH, T <sub>J</sub> = 125 °C	-	507	-	ns
Rise time	t <sub>r</sub>	···g ···,··· p···, · g · · ·	-	133	-	
Turn-off delay time	t <sub>d(off)</sub>		-	538	-	
Fall time	t <sub>f</sub>		-	92	-	
Reverse bias safe operating area	RBSOA	$T_J$ = 150 °C, I <sub>C</sub> = 400 A, R <sub>g</sub> = 27 Ω, V <sub>GE</sub> = 15 V to 0	Fullsquare			
Short circuit safe operating area	SCSOA	$      T_J = 150 \ ^\circ C, \ V_{CC} = 400 \ V, \ V_P = 600 \ V, \\ R_g = 27 \ \Omega, \ V_{GE} = 15 \ V \ to \ 0 $	10	-	-	
Diode reverse recovery time	t <sub>rr</sub>		-	226	260	ns
Diode peak reverse current	I <sub>rr</sub>	I <sub>F</sub> = 50 A, dI <sub>F</sub> /dt = 200 A/µs, V <sub>CC</sub> = 400 V, T <sub>J</sub> = 25 °C	-	17	20	Α
Diode recovery charge	Q <sub>rr</sub>		-	1900	2600	nC
Diode reverse recovery time	t <sub>rr</sub>		-	290	330	ns
Diode peak reverse current	Irr	l <sub>F</sub> = 50 A, dl <sub>F</sub> /dt = 200 A/μs, V <sub>CC</sub> = 400 V, T <sub>-l</sub> = 125 °C	-	25	30	Α
Diode recovery charge	Q <sub>rr</sub>		-	3600	5000	nC

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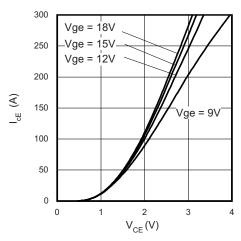
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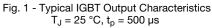
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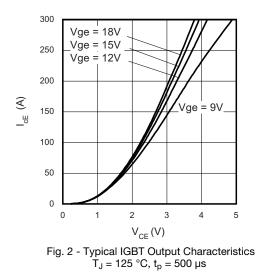


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THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>	- 40	-	150	°C	
lunction to case per log	IGBT		-	0.13	0.16		
Junction to case per leg	Diode	R <sub>thJC</sub>	-	0.19	0.32	°C/W	
Case to sink per module		R <sub>thCS</sub>	-	0.1	-		
Mounting torque case to heatsink			-	-	4	- Nm	
Mounting torque	case to terminal 1, 2, 3		-	-	3		
Weight			-	185	-	g	







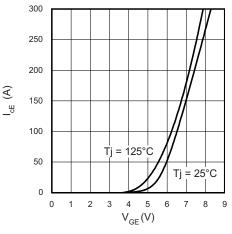


Fig. 3 - Typical Transfer Characteristics  $V_{CE}$  = 20 V,  $t_{p}$  = 500  $\mu s$ 

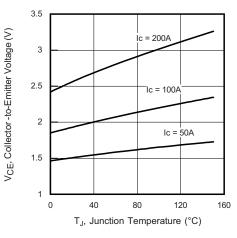


Fig. 4 - Typical Collector to Emitter Voltage vs. Junction Temperature

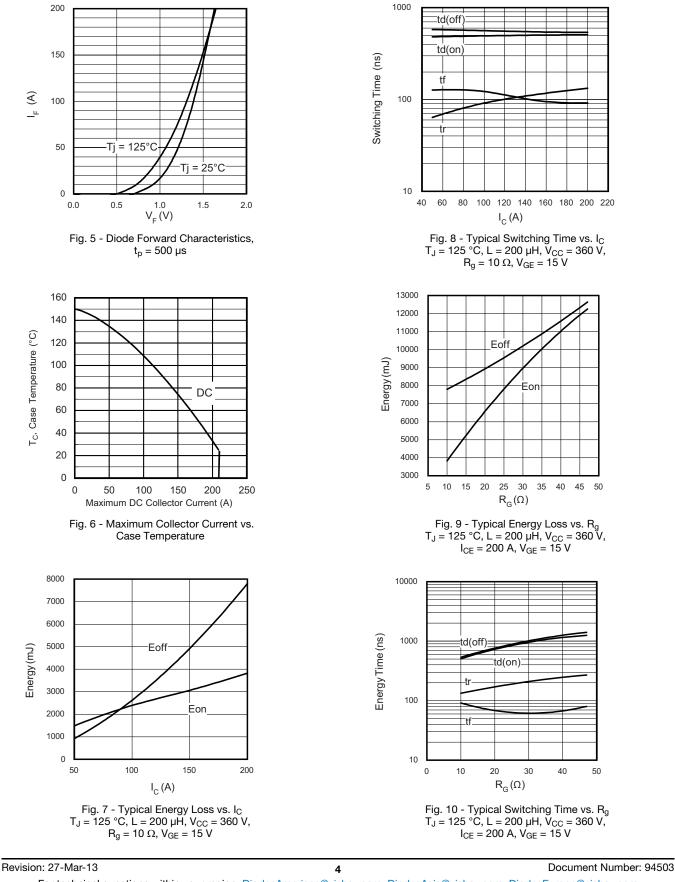
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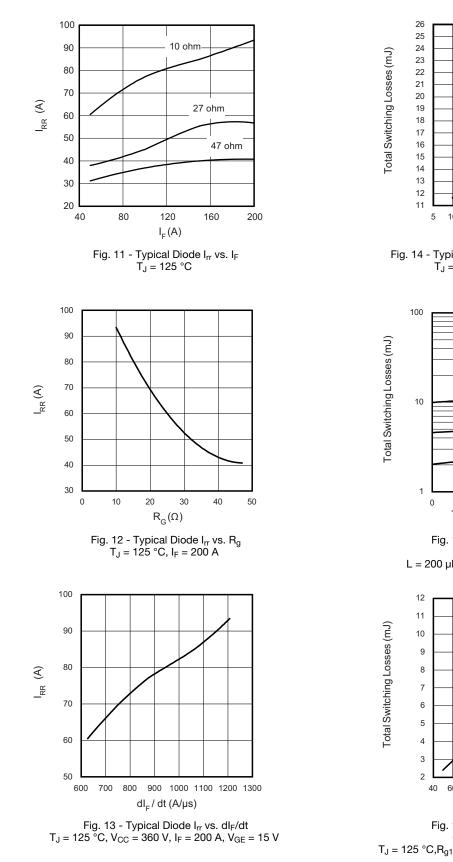


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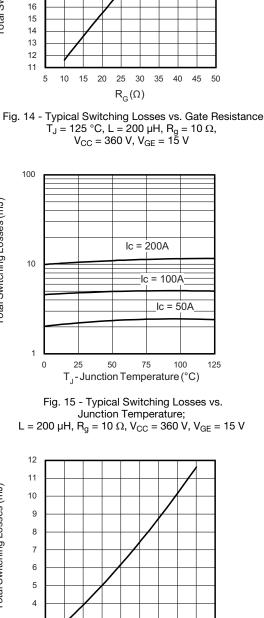
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2 40 60 80 100 120 140 160 180 200 220 I<sub>C</sub>(A) Fig. 16 - Typical Switching Losses vs.

 $\begin{array}{l} \text{Collector to Emitter Current;} \\ \text{T}_{\text{J}} = 125 \ ^{\circ}\text{C}, \text{R}_{\text{g1}} = 10 \ \Omega, \ \text{R}_{\text{g2}} = 0 \ \Omega, \ \text{V}_{\text{CC}} = 360 \ \text{V}, \ \text{V}_{\text{GE}} = 15 \ \text{V} \end{array}$ 

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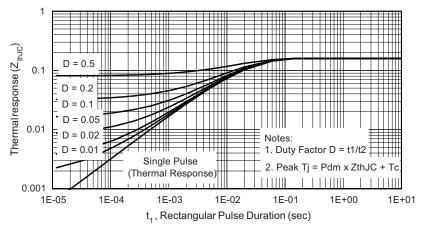
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Fig. 17 - Maximum Transient Thermal Impedance, Junction to Case (IGBT)

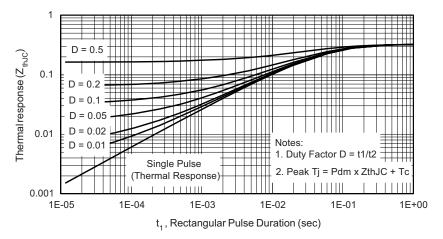


Fig. 18 - Maximum Transient Thermal Impedance, Junction to Case (HEXFRED®)

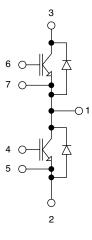
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#### **ORDERING INFORMATION TABLE**

<b>.</b>		_		_				
Device code	G	В	200	Т	S	60	Ν	PbF
	1	2	3	4	5	6	7	8
	1 - 2 -		lated G	•			IGBT)	
	3 -	Cur	rent ratii	ng (200	= 200 A	<b>(</b> )		
	4 -	- Circuit configuration (T = Half-bridge)						
	5 -	- Package indicator (S = INT-A-PAK)						
	6 -	- Voltage rating (60 = 600 V)						
	7 -	- Speed/type (N = Ultrafast IGBT)						
	8 -	Lea	d (Pb)-fr	ree				

#### **CIRCUIT CONFIGURATION**



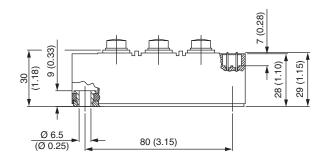
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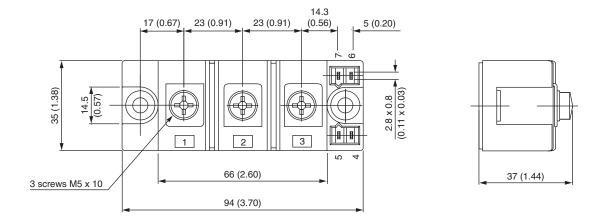




INT-A-PAK IGBT

#### **DIMENSIONS** in millimeters (inches)





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