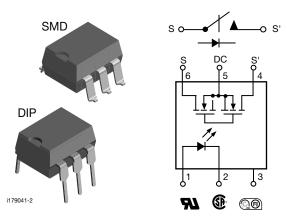
RoHS

COMPLIAN



1 Form A Solid-State Relay



DESCRIPTION

The LH1535 is robust, ideal for telecom and ground fault applications. It is an SPST normally open switch (form A) that replaces electromechanical relays in many applications. It is constructed using a GaAlAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high-voltage dielectrically isolated technology, is comprised of a photodiode array, switch control circuitry and MOSFET switches. In addition, it employs current-limiting circuitry which meets lightning surge testing as per ANSI/TIA-968-B and other regulatory voltage surge requirements when overvoltage protection is provided.

FEATURES

- Current limit protection
- Isolation test voltage 5300 V_{BMS}
- Typical R_{ON} 20 Ω, max. 25 Ω
- Load voltage 400 V
- Load current 120 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- · SMD lead available on tape and reel
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

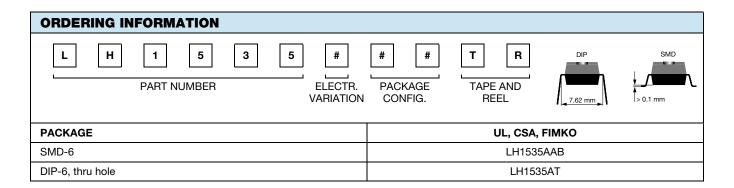
- · General telecom switching
- Instrumentation
- Industrial controls

Note

See "solid-state relays" (application note 56)

AGENCY APPROVALS

UL1577: file no. E52744 system code H, double protection CSA: certification no. 093751 FIMKO: 25419



1

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ABSOLUTE MAXIMUM RATINGS ($T_{amb} = 25 \text{ °C}$, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
INPUT					
LED continuous forward current		I _F	50	mA	
LED reverse voltage	I _R ≤ 10 μA	V _R	8	V	
OUTPUT					
DC or peak AC load voltage	$I_L \le 50 \ \mu A$	VL	400	V	
Continuous DC load current, bidirectional operation		١L	120	mA	
Continuous DC load current, unidirectional operation		١L	250	mA	
Peak load current (single shot)	t = 100 ms	I _P	(1)	mA	
SSR					
Ambient temperature range		T _{amb}	-40 to +85	°C	
Storage temperature range		T _{stg}	-40 to +150	°C	
Pin soldering temperature ⁽²⁾	t = 10 s max.	T _{sld}	260	°C	
Input to output isolation test voltage		V _{ISO}	5300	V _{RMS}	
Output power dissipation (continuous)		P _{diss}	550	mW	

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

(1) Refer to current limit performance application note for a discussion on relay operation during transient currents.

⁽²⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
LED forward current, switch turn-on	I _L = 100 mA, t = 10 ms	I _{Fon}		0.75	2	mA
LED forward current, switch turn-off	$V_L = \pm 150 V, t = 100 ms$	I _{Foff}	0.2	0.65		mA
LED forward voltage, switch turn-on	I _F = 10 mA	V _F	1.15	1.27	1.45	V
OUTPUT						
On-resistance AC/DC	I _F = 5 mA, I _L = 50 mA	R _{ON}	12	20	25	Ω
On-resistance DC	$I_{\rm F} = 5 {\rm mA}, I_{\rm L} = 100 {\rm mA}$	R _{ON}	3	6	6.25	Ω
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.5	200		GΩ
Current limit AC ⁽¹⁾ : pin 4 (±) to 6 (±)	$I_F = 5 \text{ mA}, V_L = \pm 6 \text{ V}, t = 5 \text{ ms}$	I _{LMT}	175	210	250	mA
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	Ι _Ο		0.5	200	nA
	$I_F = 0 \text{ mA}, V_L = \pm 400 \text{ V}$	Ι _Ο		136		nA
Output capacitance	I _F = 0 mA, V _L = 1 V	Co		21.6		pF
	$I_{\rm F} = 0 {\rm mA}, V_{\rm L} = 50 {\rm V}$	Co		9		pF
Switch offset	I _F = 5 mA	V _{OS}		0.4		V
Breakdown voltage	$I_F = 0 \text{ mA}$	V _{BR}		433		μV
TRANSFER		<u>.</u>				
Capacitance (input to output)	$V_{ISO} = 1 V$	CIO		0.75		pF

Notes

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

⁽¹⁾ No DC mode current limit available.

SWITCHING CARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I _F = 5 mA, I _L = 50 mA	t _{on}		0.7	2	ms
Turn-off time	$I_{F} = 5 \text{ mA}, I_{L} = 50 \text{ mA}$	t _{off}		0.6	2	ms

Rev. 1.9, 04-Mar-14

2

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LH1535AAB, LH1535AT



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SAFETY AND INSULATION RATINGS						
PARAMETER		TEST CONDITION	SYMBOL	VALUE	UNIT	
Climatic classification		IEC 68 part 1		40/85/21		
Pollution degree		DIN VDE 0109		2		
Tracking resistance (compar	ative tracking index)	Insulation group Illa	CTI	175		
Highest allowable overvoltage	je	Transient overvoltage	V _{IOTM}	8000	V _{peak}	
Max. working insulation volta	age	Recurring peak voltage	VIORM	890	V _{peak}	
Insulation resistance at 25 °C			R _{IS}	≥ 10 ¹²	W	
Insulation resistance at T _S Insulation resistance at 100 °C		V _{IO} = 500 V	R _{IS}	≥ 10 ⁹	W	
			R _{IS}	≥ 10 ¹¹	W	
Partial discharge test voltage	е	Methode a, V _{pd} = V _{IORM} x 1.875	V _{pd}	1669	V _{peak}	
Safety limiting values -	Case temperature		T _{SI}	175	°C	
maximum values allowed in	Input current		I _{SI}	300	mA	
the event of a failure	Output power		P _{SO}	700	mW	
Minimum external air gap (clearance)		Measured from input terminals to output terminals, shortest distance through air		≥7	mm	
Minimum external tracking (creepage)		Measured from input terminals to output terminals, shortest distance path along body		≥7	mm	

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

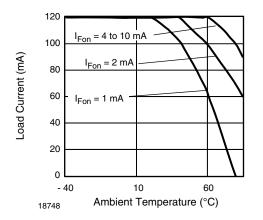


Fig. 1 - Recommended Operating Conditions

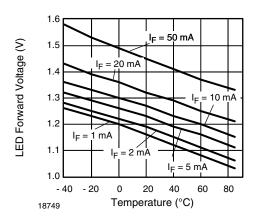


Fig. 2 - LED Voltage vs. Temperature

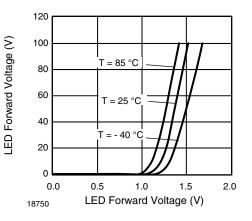


Fig. 3 - LED Forward Current vs. LED Forward Voltage

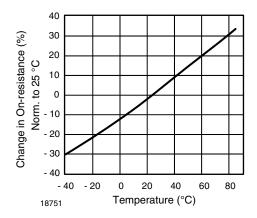


Fig. 4 - On-resistance vs. Temperature

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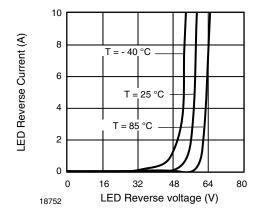


Fig. 5 - LED Reverse Current vs. LED Reverse Voltage

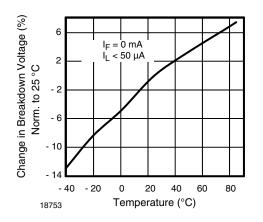


Fig. 6 - Switch Breakdown Voltage vs. Temperature

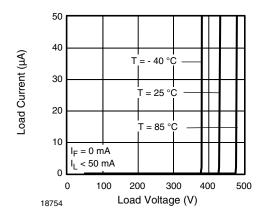


Fig. 7 - Switch Breakdown Voltage vs. Load Current

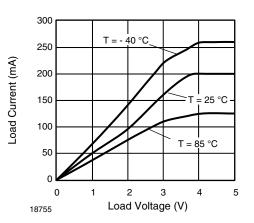


Fig. 8 - Load Current vs. Load Voltage

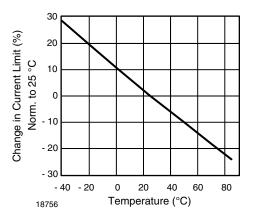


Fig. 9 - Current Limit vs. Temperature

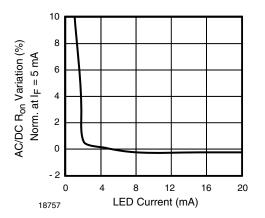


Fig. 10 - Variation in On-resistance vs. LED Current

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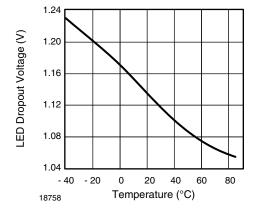


Fig. 11 - LED Dropout Voltage vs. Temperature

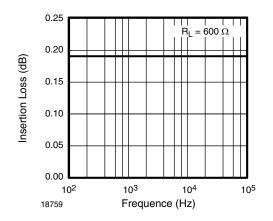


Fig. 12 - Insertion Loss vs. Frequency

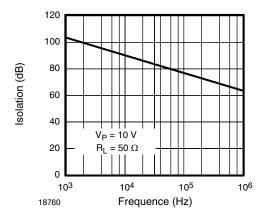


Fig. 13 - Output Isolation

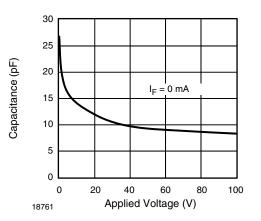


Fig. 14 - Switch Terminal Capacitance vs. Applied Voltage

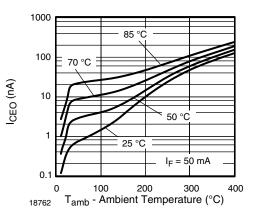


Fig. 15 - Leakage Current vs. Applied Voltage

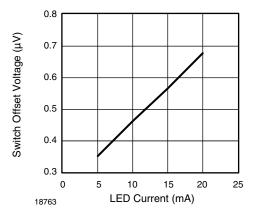


Fig. 16 - Switch Offset Voltage vs. LED Current

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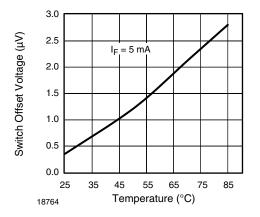


Fig. 17 - Switch Offset Voltage vs. Temperature

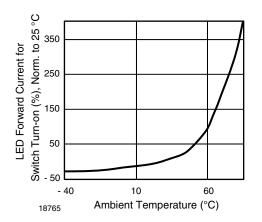


Fig. 18 - LED Current for Switch Turn-on vs. Temperature

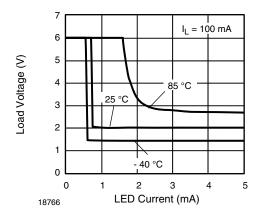


Fig. 19 - LED Current vs. Load Voltage

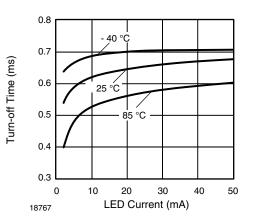


Fig. 20 - Turn-off Time vs. LED Current

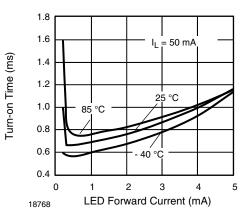


Fig. 21 - Turn-on Time vs. LED Current

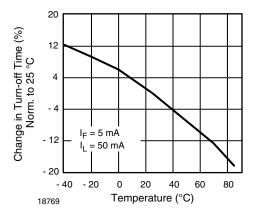


Fig. 22 - Turn-off Time vs. Temperature

6



LH1535AAB, LH1535AT

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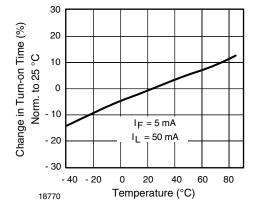
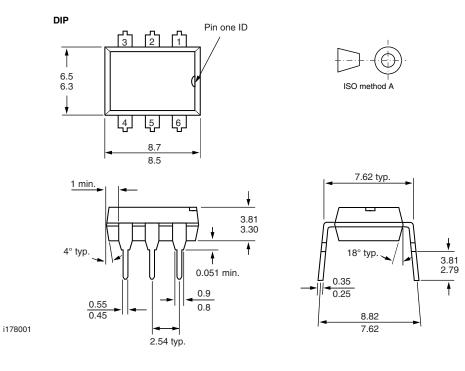


Fig. 23 - Turn-on Time vs. Temperature

PACKAGE DIMENSIONS in millimeters

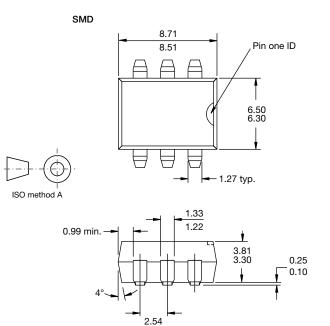


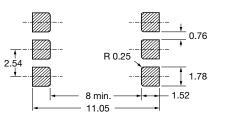
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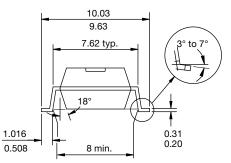


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PACKAGE MARKING (Example)

i178002



Note

• Tape and reel suffix (TR) is not part of the package marking.



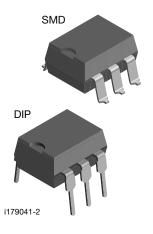
Footprint and Schematic Information for LH1535AAB, LH1535AT

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC
LH1535AAB	www.snapeda.com/parts/LH1535AAB/Vishay/view-part
LH1535AT	www.snapeda.com/parts/LH1535AT/Vishay/view-part

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