## 1 Form A Solid-State Relay



## DESCRIPTION

The LH1535 is robust, ideal for telecom and ground fault applications. It is an SPST normally open switch (form A) that replaces electromechanical relays in many applications. It is constructed using a GaAIAs LED for actuation control and an integrated monolithic die for the switch output. The die, fabricated in a high-voltage dielectrically isolated technology, is comprised of a photodiode array, switch control circuitry and MOSFET switches. In addition, it employs current-limiting circuitry which meets lightning surge testing as per ANSI/TIA-968-B and other regulatory voltage surge requirements when overvoltage protection is provided.

## FEATURES

- Current limit protection
- Isolation test voltage $5300 \mathrm{~V}_{\mathrm{RMS}}$
- Typical RoN $20 \Omega$, max. $25 \Omega$
- Load voltage 400 V
- Load current 120 mA
- High surge capability
- Clean bounce free switching
- Low power consumption
- SMD lead available on tape and reel
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912


## APPLICATIONS

- General telecom switching
- Instrumentation
- Industrial controls


## Note

- See "solid-state relays" (application note 56)


## AGENCY APPROVALS

UL1577: file no. E52744 system code H, double protection CSA: certification no. 093751
FIMKO: 25419


LH1535AAB, LH1535AT
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| ABSOLUTE MAXIMUM RATINGS ( $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$, unless otherwise specified) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT |
| INPUT |  |  |  |  |
| LED continuous forward current |  | $\mathrm{I}_{\mathrm{F}}$ | 50 | mA |
| LED reverse voltage | $\mathrm{I}_{\mathrm{R}} \leq 10 \mu \mathrm{~A}$ | $\mathrm{V}_{\text {R }}$ | 8 | V |
| OUTPUT |  |  |  |  |
| DC or peak AC load voltage | l L $\leq 50 \mu \mathrm{~A}$ | VL | 400 | V |
| Continuous DC load current, bidirectional operation |  | $\mathrm{I}_{\mathrm{L}}$ | 120 | mA |
| Continuous DC load current, unidirectional operation |  | $\mathrm{I}_{\mathrm{L}}$ | 250 | mA |
| Peak load current (single shot) | $\mathrm{t}=100 \mathrm{~ms}$ | $\mathrm{I}_{\mathrm{P}}$ | ${ }^{(1)}$ | mA |
| SSR |  |  |  |  |
| Ambient temperature range |  | $\mathrm{T}_{\text {amb }}$ | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature range |  | $\mathrm{T}_{\text {stg }}$ | -40 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Pin soldering temperature ${ }^{(2)}$ | $\mathrm{t}=10 \mathrm{~s}$ max. | $\mathrm{T}_{\text {sld }}$ | 260 | ${ }^{\circ} \mathrm{C}$ |
| Input to output isolation test voltage |  | $\mathrm{V}_{\text {ISO }}$ | 5300 | $\mathrm{V}_{\text {RMS }}$ |
| Output power dissipation (continuous) |  | $\mathrm{P}_{\text {diss }}$ | 550 | mW |

## Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
(1) Refer to current limit performance application note for a discussion on relay operation during transient currents.
(2) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

| ELECTRICAL CHARACTERISTICS ( $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$, unless otherwise specified) |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| INPUT |  |  |  |  |  |  |
| LED forward current, switch turn-on | $\mathrm{I}_{\mathrm{L}}=100 \mathrm{~mA}, \mathrm{t}=10 \mathrm{~ms}$ | $\mathrm{I}_{\text {fon }}$ |  | 0.75 | 2 | mA |
| LED forward current, switch turn-off | $\mathrm{V}_{\mathrm{L}}= \pm 150 \mathrm{~V}, \mathrm{t}=100 \mathrm{~ms}$ | $\mathrm{I}_{\text {Foff }}$ | 0.2 | 0.65 |  | mA |
| LED forward voltage, switch turn-on | $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ | $V_{F}$ | 1.15 | 1.27 | 1.45 | V |
| OUTPUT |  |  |  |  |  |  |
| On-resistance AC/DC | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{I}_{\mathrm{L}}=50 \mathrm{~mA}$ | RON | 12 | 20 | 25 | $\Omega$ |
| On-resistance DC | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{I}_{\mathrm{L}}=100 \mathrm{~mA}$ | $\mathrm{R}_{\mathrm{ON}}$ | 3 | 6 | 6.25 | $\Omega$ |
| Off-resistance | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}= \pm 100 \mathrm{~V}$ | R OFF | 0.5 | 200 |  | $\mathrm{G} \Omega$ |
| Current limit AC ${ }^{(1)}$ : $\operatorname{pin} 4( \pm)$ to $6( \pm)$ | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}= \pm 6 \mathrm{~V}, \mathrm{t}=5 \mathrm{~ms}$ | lımt | 175 | 210 | 250 | mA |
| Off-state leakage current | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}= \pm 100 \mathrm{~V}$ | 10 |  | 0.5 | 200 | nA |
|  | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}= \pm 400 \mathrm{~V}$ | $\mathrm{I}_{0}$ |  | 136 |  | nA |
| Output capacitance | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=1 \mathrm{~V}$ | $\mathrm{C}_{0}$ |  | 21.6 |  | pF |
|  | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{L}}=50 \mathrm{~V}$ | $\mathrm{C}_{0}$ |  | 9 |  | pF |
| Switch offset | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $\mathrm{V}_{\text {OS }}$ |  | 0.4 |  | V |
| Breakdown voltage | $\mathrm{I}_{\mathrm{F}}=0 \mathrm{~mA}$ | $\mathrm{V}_{\mathrm{BR}}$ |  | 433 |  | $\mu \mathrm{V}$ |
| TRANSFER |  |  |  |  |  |  |
| Capacitance (input to output) | $\mathrm{V}_{\text {ISO }}=1 \mathrm{~V}$ | $\mathrm{ClO}_{10}$ |  | 0.75 |  | pF |

## Notes

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.
(1) No DC mode current limit available.

SWITCHING CARACTERISTICS $\left(\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}\right.$, unless otherwise specified)

| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Turn-on time | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{I}_{\mathrm{L}}=50 \mathrm{~mA}$ | $\mathrm{t}_{\mathrm{on}}$ |  | 0.7 | 2 | ms |
| Turn-off time | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}, \mathrm{I}_{\mathrm{L}}=50 \mathrm{~mA}$ | $\mathrm{t}_{\text {off }}$ |  | 0.6 | 2 | ms |


| SAFETY AND INSULATION RATINGS |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETER |  | TEST CONDITION | SYMBOL | VALUE | UNIT |
| Climatic classification |  | IEC 68 part 1 |  | 40/85/21 |  |
| Pollution degree |  | DIN VDE 0109 |  | 2 |  |
| Tracking resistance (comparative tracking index) |  | Insulation group Illa | CTI | 175 |  |
| Highest allowable overvoltage |  | Transient overvoltage | $\mathrm{V}_{\text {IOTM }}$ | 8000 | $\mathrm{V}_{\text {peak }}$ |
| Max. working insulation voltage |  | Recurring peak voltage | VIORM | 890 | $V_{\text {peak }}$ |
| Insulation resistance at $25^{\circ} \mathrm{C}$ |  | $\mathrm{V}_{10}=500 \mathrm{~V}$ | $\mathrm{R}_{\text {IS }}$ | $\geq 10^{12}$ | W |
| Insulation resistance at $\mathrm{T}_{\text {S }}$ |  |  | $\mathrm{R}_{\text {IS }}$ | $\geq 10^{9}$ | W |
| Insulation resistance at $100^{\circ} \mathrm{C}$ |  |  | $\mathrm{R}_{\text {IS }}$ | $\geq 10^{11}$ | W |
| Partial discharge test voltage |  | Methode a, $\mathrm{V}_{\text {pd }}=\mathrm{V}_{\text {IORM }} \times 1.875$ | $V_{\text {pd }}$ | 1669 | $V_{\text {peak }}$ |
| Safety limiting values maximum values allowed in the event of a failure | Case temperature |  | $\mathrm{T}_{\mathrm{s}}$ | 175 | ${ }^{\circ} \mathrm{C}$ |
|  | Input current |  | $\mathrm{I}_{\mathrm{S}}$ | 300 | mA |
|  | Output power |  | $\mathrm{P}_{\text {SO }}$ | 700 | mW |
| Minimum external air gap (clearance) |  | Measured from input terminals to output terminals, shortest distance through air |  | $\geq 7$ | mm |
| Minimum external tracking (creepage) |  | Measured from input terminals to output terminals, shortest distance path along body |  | $\geq 7$ | mm |

TYPICAL CHARACTERISTICS $\left(T_{a m b}=25^{\circ} \mathrm{C}\right.$, unless otherwise specified)


Fig. 1 - Recommended Operating Conditions


Fig. 2 - LED Voltage vs. Temperature


Fig. 3 - LED Forward Current vs. LED Forward Voltage


Fig. 4 - On-resistance vs. Temperature


Fig. 5 - LED Reverse Current vs. LED Reverse Voltage


Fig. 6 - Switch Breakdown Voltage vs. Temperature


Fig. 7 - Switch Breakdown Voltage vs. Load Current


Fig. 8 - Load Current vs. Load Voltage


Fig. 9 - Current Limit vs. Temperature


Fig. 10 - Variation in On-resistance vs. LED Current


Fig. 11 - LED Dropout Voltage vs. Temperature


Fig. 12 - Insertion Loss vs. Frequency


Fig. 13 - Output Isolation


Fig. 14 - Switch Terminal Capacitance vs. Applied Voltage


Fig. 15 - Leakage Current vs. Applied Voltage


Fig. 16 - Switch Offset Voltage vs. LED Current


Fig. 17 - Switch Offset Voltage vs. Temperature


Fig. 18 - LED Current for Switch Turn-on vs. Temperature


Fig. 19 - LED Current vs. Load Voltage


Fig. 20 - Turn-off Time vs. LED Current


Fig. 21 - Turn-on Time vs. LED Current


Fig. 22 - Turn-off Time vs. Temperature

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Fig. 23 - Turnon Time vs. Temperature
PACKAGE DIMENSIONS in millimeters

DIP


ISO method A



ISO method A

i178002


PACKAGE MARKING (Example)

## Note

- Tape and reel suffix (TR) is not part of the package marking.


## Footprint and Schematic Information for LH1535AAB, LH1535AT

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.
Note that the 3D models for these parts can be found on the Vishay product page.

| PART NUMBER | FOOTPRINT / SCHEMATIC |
| :--- | :---: |
| LH1535AAB | $\underline{\text { www.snapeda.com/parts/LH1535AAB/Vishay/view-part }}$ |
| LH1535AT | $\underline{w w w . s n a p e d a . c o m / p a r t s / L H 1535 A T / V i s h a y / v i e w-p a r t ~}$ |

For technical issues and product support, please contact optocoupleranswers@vishay.com.


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