



DMN31D5L

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
201/	1.5Ω @ V <sub>GS</sub> = 4.0V	0.54
30V	2.0Ω @ V <sub>GS</sub> = 2.5V	0.5A

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

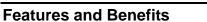
- Motor Control
- **Power Management Functions**
- Backlighting

SOT23





Top View



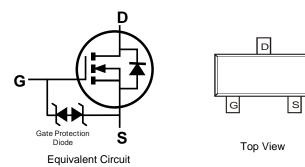
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)

N-CHANNEL ENHANCEMENT MODE MOSFET

Halogen and Antimony Free. "Green" Device (Note 3)

### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)



## Ordering Information (Note 4)

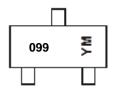
	Part Number	Case	Packaging			
	DMN31D5L-7	SOT23	3000/Tape & Reel			
DMN31D5L-13		SOT23	10000/Tape & Reel			
Notes:	tes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS). 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.					

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

## Marking Information



099 = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: F = 2018)

		_0.0)
M = Month	(ex: 9 = 5	September)

Date Code Key												
Year	2018	20	19	2020	20	021	2022	2	2023	2024		2025
Code	F	G	i	Н			J		K	L		М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	30	V		
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) VGs = 4V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +75°C	ID	0.5 0.4	А
Maximum Continuous Body Diode Forward Curre	Is	0.3	A		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 7	1%)	I <sub>DM</sub>	5	A	

## Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	350	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	357	°C/W
Total Power Dissipation (Note 6)		PD	520	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	240	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

			_			
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	-	n		n	1	
Drain-Source Breakdown Voltage	<b>BV</b> <sub>DSS</sub>	30	_		V	$V_{GS} = 0V, I_D = 100 \mu A$
Zero Gate Voltage Drain Current @T <sub>C</sub> = +25°C	I <sub>DSS</sub>		_	1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>		_	±10	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.8		1.6	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance		—		1.5	Ω	$V_{GS} = 4.0V, I_{D} = 10mA$
	R <sub>DS</sub> (ON)	—		2.0	Ω	$V_{GS} = 2.5V, I_D = 10mA$
Diode Forward Voltage	$V_{SD}$	_		1.2	V	$V_{GS} = 0V, I_{S} = 10mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	—	50		pF	
Output Capacitance	Coss	—	12		pF	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 0V, f = 1.0MHz
Reverse Transfer Capacitance	Crss	—	10		pF	
Total Gate Charge ( $V_{GS} = 4.5V$ )	Qg	_	0.5	_	nC	
Total Gate Charge (V <sub>GS</sub> = 10V)	Qg	—	1.2		nC	$V_{GS} = 10V, V_{DS} = 10V,$
Gate-Source Charge	Qgs	—	0.2		nC	$I_D = 250 \text{mA}$
Gate-Drain Charge	Q <sub>gd</sub>	_	0.1	_	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	3.5	_	ns	
Turn-On Rise Time		—	3.3	—	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	16.8	—	ns	$R_{G} = 25\Omega, I_{D} = 200mA$
Turn-Off Fall Time	t <sub>F</sub>	_	13.8	—	ns	

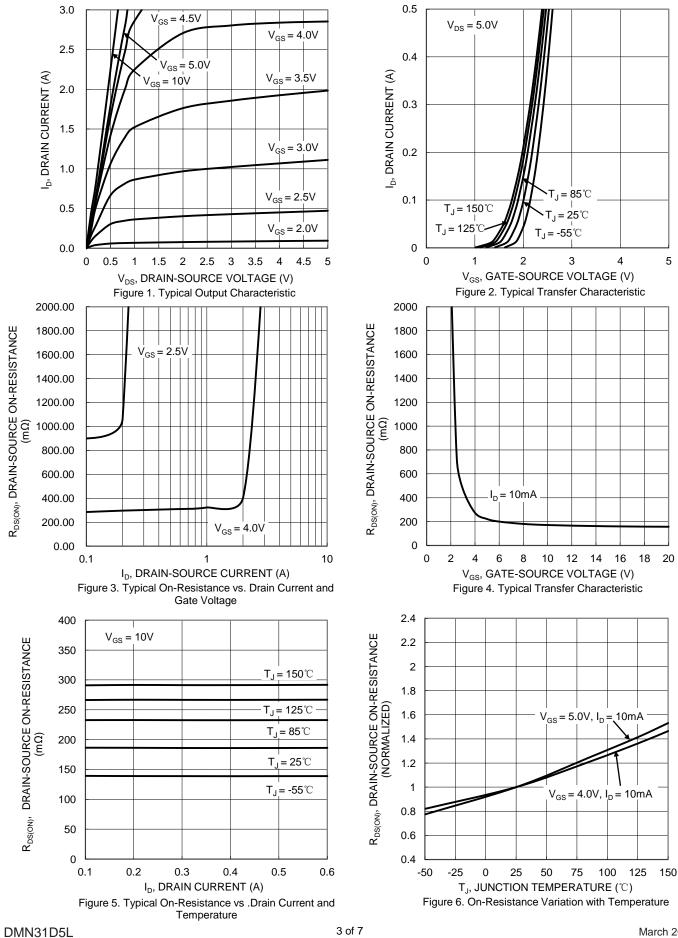
Notes:

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Device mounted on FR-4 substrate PC board, 202 copper, with 1inch square copper plate.
Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

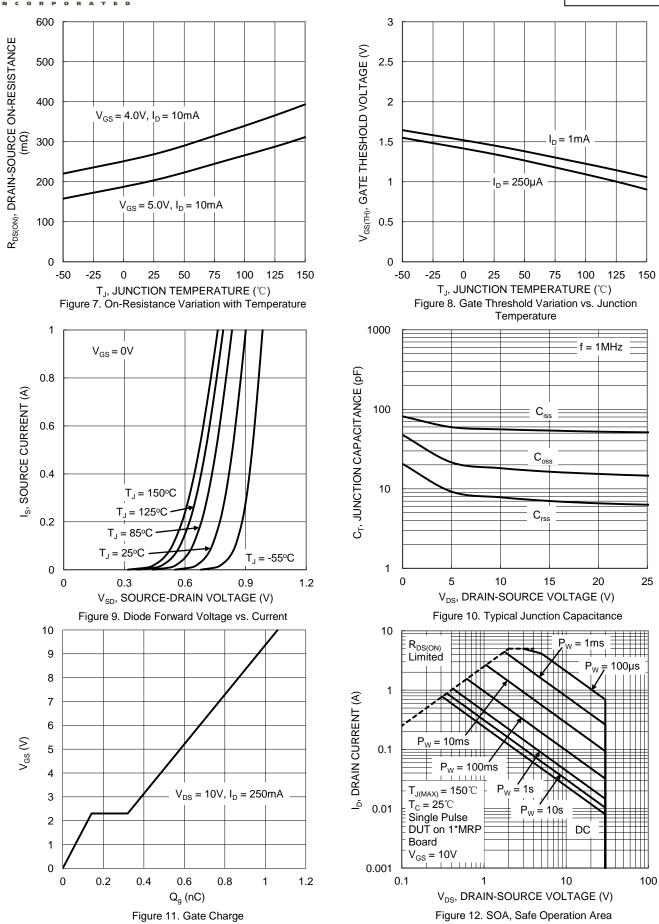




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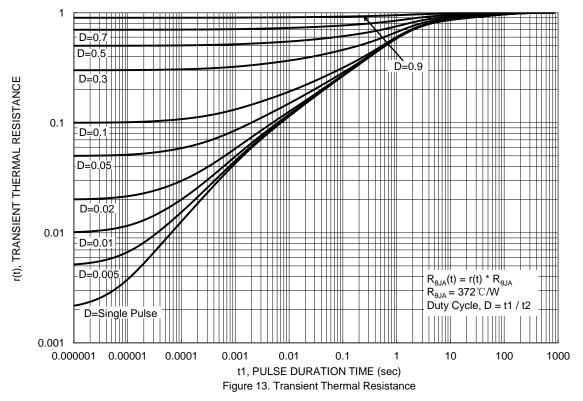






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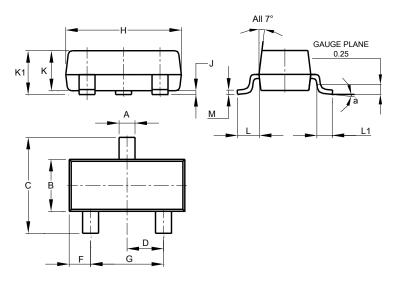


## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

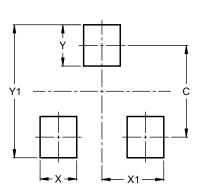
SOT23



	SOT23								
Dim	Min	Max	Тур						
Α	0.37	0.51	0.40						
В	1.20	1.40	1.30						
С	2.30	2.50	2.40						
D	0.89	1.03	0.915						
F	0.45	0.60	0.535						
G	1.78	2.05	1.83						
Н	2.80	3.00	2.90						
J	0.013	0.10	0.05						
K	0.890	1.00	0.975						
K1	0.903	1.10	1.025						
L	0.45	0.61	0.55						
L1	0.25	0.55	0.40						
М	0.085	0.150	0.110						
а	0°	8°							
All	Dimens	ions in	mm						

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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