



RS1MDFQ

1.0A SURFACE MOUNT FAST RECOVERY RECTIFIER

Product Summary (@TA = +25°C)

V _{RRM} (V)	I ₀ (A)	V _F Max (V)	I _R Max (μA)
1,000	1	1.3	5

Features and Benefits

- Glass Passivated Die Construction
- Fast Recovery Time for High Efficiency
- Surge Overload Rating to 30A Peak
- High Current Capability
- Low Profile Design, Package Height Less than 1.1mm
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Description and Applications

The RS1MDFQ is a rectifier packaged in the low profile D-FLAT package. Providing fast recovery time for high efficiency, this device is ideal for use in general applications such as:

- Reverse Protection
- Switching
- Blocking

Mechanical Data

- Case: D-FLAT
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 ³
- · Polarity: Cathode Band
- Weight: 0.035 grams (Approximate)



Top View

Ordering Information (Note 5)

Part Number	Compliance	Case	Packaging
RS1MDFQ-13	Automotive	D-FLAT	10,000/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to http://www.diodes.com/product_compliance_definitions.html.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

R1M = Product Type Marking Code

O!! = Manufacturers' Code Marking

YWW = Date Code Marking

Y = Last Digit of Year (ex: 6 for 2016)

WW = Week Code (01 to 53)

AB = Foundry and Assembly Code



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage (Note 6)	V _{RRM} V _{RWM} V _R	1,000	V
RMS Reverse Voltage	V _{R(RMS)}	700	V
Average Rectified Output Current @ T _A = +100°C	I _O	1.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		30	Α

Thermal Characteristics

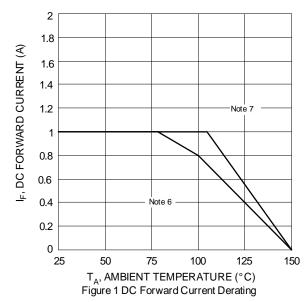
Characteristic	Symbol	Value	Unit
Typical Thermal Resistance, Junction to Terminal (Note 7)	R ₀ JT	31	°C/W
Typical Thermal Resistance, Junction to Air (Note 7)	R _{θJA}	83	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

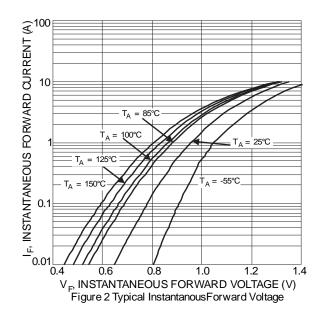
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 8)	$V_{(BR)R}$	1,000	_	_	V	I _R = 10μA
Forward Voltage Drop	V _F	_	0.95 0.83	1.3 —	V	I _F = 1A, T _J = +25°C I _F = 1A, T _J = +125°C
Leakage Current (Note 8)	I _R	_	0.2 5	5 —	μA	V _R = 1,000V, T _J = +25°C V _R = 1,000V, T _J = +125°C
Reverse Recovery Time	t _{RR}	_	140	500	ns	I _F = 0.5A, I _R = 1.0A, I _{RR} = 0.25A
Total Capacitance	Ст	_	5	_	pF	$V_R = 4.0V_{DC}$, $f = 1MHz$

Notes:

- 6. Device mounted on FR-4 substrate, 1" x 1", 2oz, single-sided, PC boards with 0.1" x 0.15" copper pads. 7. Device mounted on FR-4 substrate, 0.4" x 0.5", 2oz, single-sided, PC boards with 0.2" x 0.25" copper pads.
- 8. Short duration pulse test used to minimize self-heating effect.





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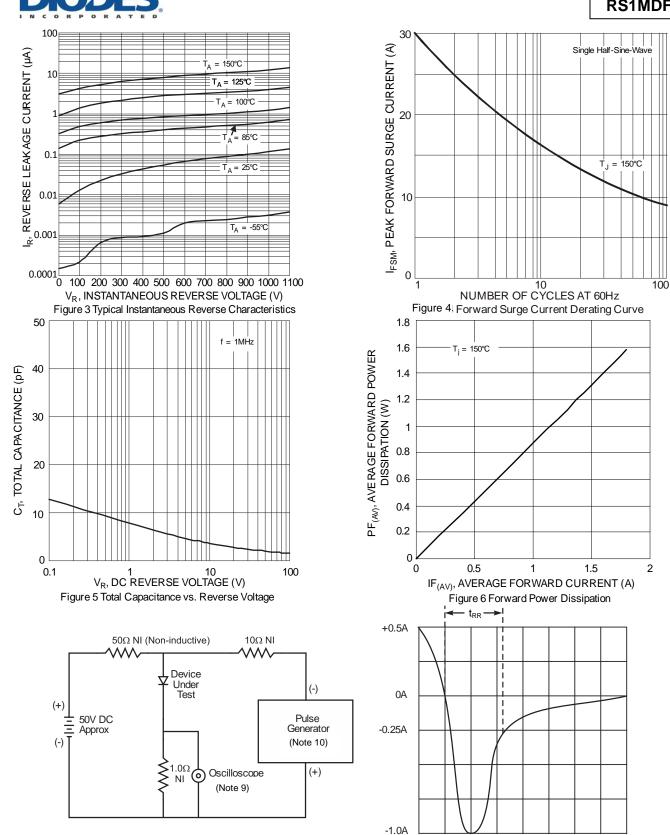


Figure 7 Reverse Recovery Time Characteristic and Test Circuit

9. Rise Time = 7.0ns max. Input Impedance = $1.0M\Omega$, 22pF. Notes: 10. Rise Time = 10ns max. Input Impedance = 50Ω .

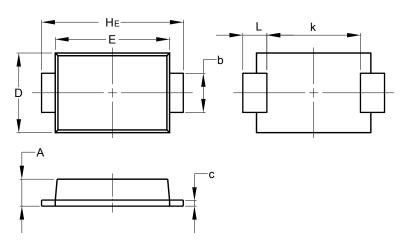
Set time base for 50/100 ns/cm



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

D-FLAT

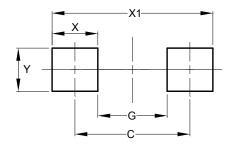


D-FLAT				
Dim	Min	Max		
Α	0.90	1.10		
b	1.25	1.65		
С	0.10	0.40		
D	2.25	2.95		
Е	3.95	4.60		
k	2.80	-		
HE	5.00	5.60		
L	0.50	1.30		
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

D-FLAT



Dimensions	Value (in mm)		
С	4.65		
G	2.80		
Х	1.85		
X1	6.50		
V	1.70		



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