

DFLS140

Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- Low Forward Voltage Drop
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: PowerDl[®]123
- Case Material: Molded Plastic.UL "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity: Cathode Band
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.01 grams (approximate)



Top View

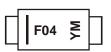
Ordering Information (Note 2)

| Part Number | Case | Packaging |
|-------------|--------------------------|------------------|
| DFLS140-7 | PowerDI [®] 123 | 3000/Tape & Reel |

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.

2. For packaging details, go to our website at http://www.diodes.com.

Marking Information



F04 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: T = 2006) M = Month (ex: 9 = September)

Date Code Key

| Year | 2004 | 20 | 05 | 2006 | 2007 | 20 | 08 | 2009 | 2010 | 20 | 11 | 2012 |
|-------|------|-----|-----|------|------|-----|-----|------|------|-----|-----|------|
| Code | R | 93 | S | Т | U | Ň | V | W | Х | Ň | Y | Z |
| Month | Jan | Feb | Mar | Apr | Мау | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |

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Maximum Ratings @T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

| Characteristic | Symbol | Value | Unit |
|-----------------------------------------------------------------------------------------------------|--------------------------------------------------------|-------|------|
| Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage | V _{RRM} V _{RWM} V _R | 40 | V |
| RMS Reverse Voltage | V _{R(RMS)} | 28 | V |
| Average Forward Current @ T _T = 119°C | I _{F(AV)} | 1.1 | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load | I _{FSM} | 40 | A |

Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
|---------------------------------------------------|------------------|-------------|------|
| Power Dissipation (Note 1) | PD | 1.67 | W |
| Power Dissipation (Note 2) | PD | 556 | mW |
| Thermal Resistance Junction to Ambient (Note 3) | R _{0JA} | 60 | °C/W |
| Thermal Resistance Junction to Ambient (Note 4) | R _{0JA} | 180 | °C/W |
| Thermal Resistance Junction to Soldering (Note 5) | R _{AJS} | 10 | °C/W |
| Operating Temperature Range | TJ | -55 to +125 | °C |
| Storage Temperature Range | T _{STG} | -55 to +150 | °C |

Electrical Characteristics @T_A = 25°C unless otherwise specified

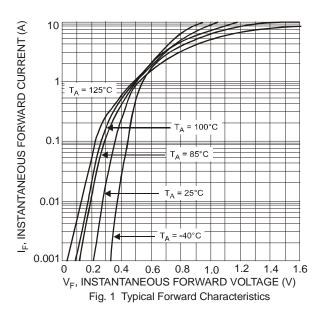
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition |
|------------------------------------|--------------------|-----|------|------|------|-------------------------------------|
| Reverse Breakdown Voltage (Note 6) | V _{(BR)R} | 40 | _ | _ | V | $I_R = 20\mu A$ |
| Forward Voltage | VF | _ | 0.45 | 0.51 | V | I _F = 0.5A |
| r orward voltage | ٧F | — | 0.53 | _ | | I _F =1.1A |
| Leakage Current (Note 6) | 1- | _ | _ | 20 | μA | $V_{R} = 40V, T_{J} = 25^{\circ}C$ |
| Leanage Current (Note O) | IR | — | | 6.0 | mA | $V_{R} = 40V, T_{J} = 100^{\circ}C$ |
| Total Capacitance | CT | _ | 28 | _ | pF | V _R = 10V, f = 1.0MHz |

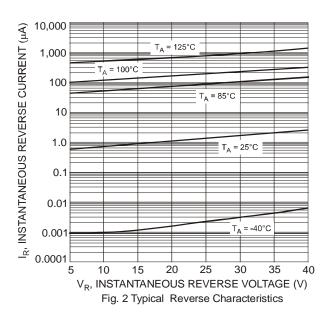
3. Part mounted on 50.8mm X 50.8mm GETEK board with 25.4mm X 25.4mm copper pad, 25% anode, 75% cathode. T_A = 25°C

4. Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads. $T_A = 25^{\circ}C$

5. Theoretical R_{0JS} calculated from the top center of the die straight down to the PCB/cathode tab solder junction.

6. Short duration pulse test used to minimize self-heating effect.

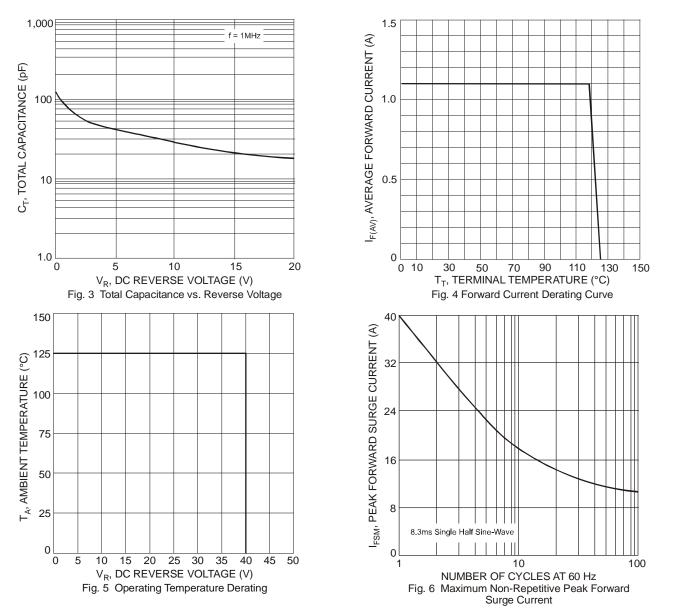




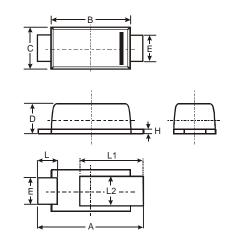
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Notes:





Package Outline Dimensions

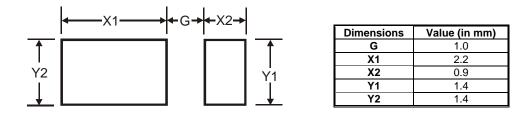


| PowerDI [®] 123 | | | | | | |
|--------------------------|----------------------|------|------|--|--|--|
| Dim | Min | Max | Тур | | | |
| Α | 3.50 | 3.90 | 3.70 | | | |
| В | 2.60 | 3.00 | 2.80 | | | |
| С | 1.63 | 1.93 | 1.78 | | | |
| D | 0.93 | 1.00 | 0.98 | | | |
| Е | 0.85 | 1.25 | 1.00 | | | |
| н | 0.15 | 0.25 | 0.20 | | | |
| L | 0.55 | 0.75 | 0.65 | | | |
| L1 | 1.80 | 2.20 | 2.00 | | | |
| L2 | 0.95 | 1.25 | 1.10 | | | |
| All D | All Dimensions in mm | | | | | |

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Suggested Pad Layout



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