

LL4148 / LL4448

## FAST SWITCHING SURFACE MOUNT DIODE

# **Features and Benefits**

- Fast Switching Speed
- Surface Mount Package Ideally Suited for Automatic Insertion
- General Purpose Rectification
- Silicon Epitaxial Planar Construction
- Lead Free Finish, RoHS Compliant (Note 1)

# **Mechanical Data**

- Case: MiniMELF
- Case Material: Glass: UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Sn97.5Ag2.5. Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Marking: Cathode Band Only
- Weight: 0.05 grams (approximate)

# Ordering Information (Note 2)

Part Number	Case	Packaging
LL4148-13	MiniMELF	10K/Tape & Reel, 13-inch
LL4448-7	MiniMELF	2.5K/Tape & Reel, 7-inch

Notes: 1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2). All applicable RoHS exemptions applied. 2. For Packaging Details, go to our website at http://www.diodes.com.

### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	LL4148	LL4448	Unit
Non-Repetitive Peak Reverse Voltage	V <sub>RM</sub> 100		00	V
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>		75	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	<b>V</b>	53	V
Forward Continuous Current (Note 3)	I <sub>FM</sub>	300	500	mA
Average Rectified Output Current (Note 3)	lo	1	50	mA
Non-Repetitive Peak Forward Surge Current $\frac{@ t = 1.0s}{@ t = 1.0 \mu s}$	IFSM		1.0 2.0	- A
Power Dissipation (Note 3) Derate above 25°C	Pp	-	00 .68	mW mW/°C
Thermal Resistance, Junction to Ambient Air (Note 3)	R <sub>0JA</sub>	300		K/W
Operating and Storage Temperature Range	TJ, TSTG	-65 T	O +175	°C

# Electrical Characteristics @TA = 25°C unless otherwise specified

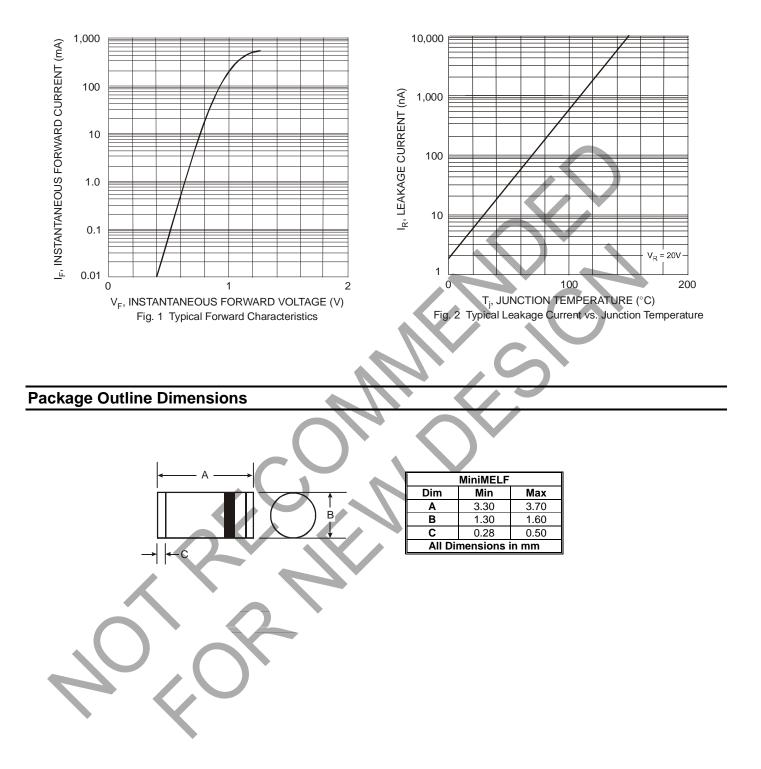
	Characteristic		Symbol	Min	Max	Unit	Test Condition
		LL4148		-	1.0		$I_F = 10 \text{mA}$
Forward Voltage	LL4448	VF	0.62	0.72	V	$I_F = 5.0 \text{mA}$	
		LL4448		-	1.0		$I_F = 100 \text{mA}$
			-	5.0	μA	V <sub>R</sub> = 75V	
Maximum Peak Reverse Current (Note 4)			I <sub>RM</sub>	-	50	μA	$V_R = 75V, T_J = 150^{\circ}C$
Maximum Peak Reverse Current (Note 4)		-		30	μA	$V_{R} = 75V, T_{J} = 150^{\circ}C$	
				-	25	nA	V <sub>R</sub> = 75V
Capacitance		CJ	-	4.0	pF	$V_{R} = 0, f = 1.0MHz$	
Reverse Recovery Time		t <sub>rr</sub>	-	4.0	ns	$I_F = 10mA$ , to $I_R = 1.0mA$ , $V_R = 6.0V$ , $R_L = 100\Omega$	

Notes: 3. Valid provided that device terminals are kept at ambient Temperature.

4. Short duration pulse test used to minimize self-heating effect.



### NOT RECOMMENDED FOR NEW DESIGN USE 1N4148W / 1N4448W





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