



Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _A = +25°C
001/	$24m\Omega @ V_{GS} = 4.5V$	7A
20V	$28m\Omega @ V_{GS} = 2.5V$	5A

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, which makes it ideal for high-efficiency power management applications.

Applications

- Backlighting
- DC-DC Converters
- Power Management Functions

N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

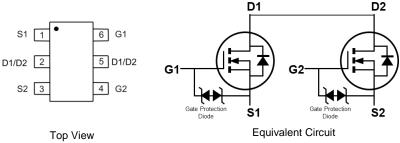
- Low On-Resistance
- Low-Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q101, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative.
- https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish—Matte Tin Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (c3)
- Weight: 0.013 grams (Approximate)



TSOT26



Ordering Information (Note 4)

	Part Number	Case	Packaging			
	DMN2024UVT-7	TSOT26	3000/Tape & Reel			
	DMN2024UVT-13	TSOT26	10,000/Tape & Reel			
Notes:	as: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS). 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.					

No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Date Code Key			AR4	MY		YM = Da Y or $\overline{Y} =$	roduct Type te Code Ma Year (ex: G th (ex: 9 = 5	arking G = 2019)				
Year	2018		2019	2020		2021	2022		2023	2024		2025
Code	F		G	Н		I J K L					М	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

DMN2024UVT

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Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	20	V		
Gate-Source Voltage	V _{GSS}	±10	V		
Continuous Drain Current (Note 6) V_{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	۱ _D	7.0 5.0	А
Maximum Continuous Body Diode Forward Curr	Is	2.3	A		
Pulsed Drain Current (10µs Pulse, Duty Cycle =	I _{DM}	35	A		

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	1.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{0JA}	124	°C/W
Total Power Dissipation (Note 6)		PD	1.6	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	78	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

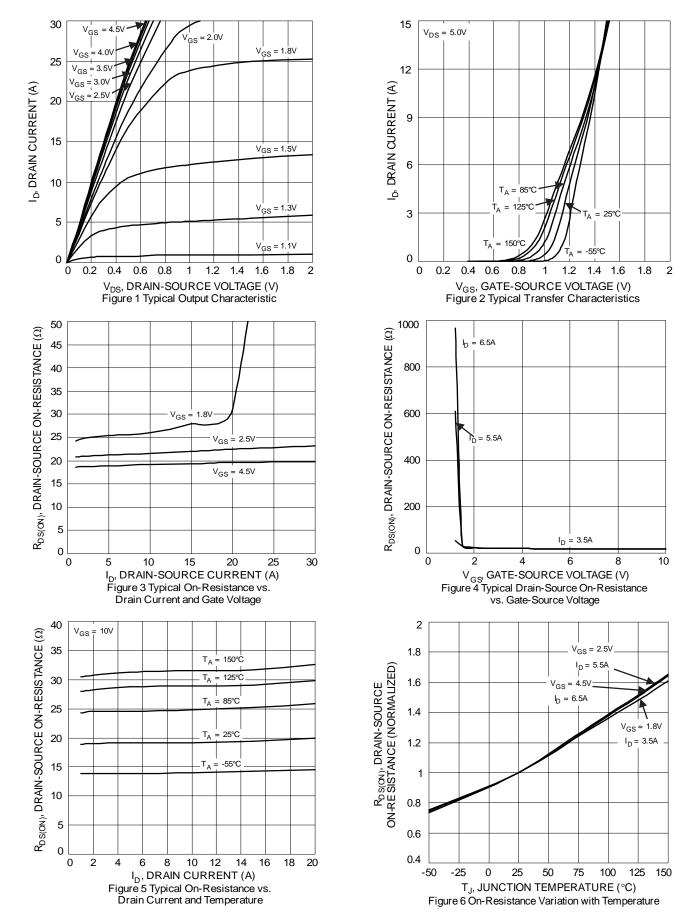
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	e y moor		.,,,	max	Unit	
Drain-Source Breakdown Voltage	BV _{DSS}	20	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current TJ = +25°C	IDSS	_	_	1.0	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	—	0.9	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
			19	24		$V_{GS} = 4.5V, I_D = 6.5A$
Static Drain-Source On-Resistance	R _{DS(ON)}	—	22	28	mΩ	$V_{GS} = 2.5V, I_D = 5.5A$
			25	34		V _{GS} = 1.8V, I _D = 3.5A
Diode Forward Voltage	V _{SD}	_	0.9	1.2	V	$V_{GS} = 0V, I_D = 5A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss	—	647	—	pF	
Output Capacitance	Coss	_	78	_	pF	−V _{DS} = 10V, V _{GS} = 0V −f = 1.0MHz
Reverse Transfer Capacitance	Crss	—	38	—	pF	
Gate Resistance	Rg	—	628	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Qg	_	7.1	_	nC	
Gate-Source Charge	Q _{gs}	_	0.9	—	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_D = 6.5A$
Gate-Drain Charge	Q _{gd}	_	0.7	—	nC	
Turn-On Delay Time	t _{D(ON)}	_	98	—	ns	
Turn-On Rise Time	t _R	_	140	—	ns	$V_{DS} = 10V, V_{GS} = 4.5V,$
Turn-Off Delay Time	t _{D(OFF)}	—	1024	_	ns	$R_L = 10\Omega, R_G = 6\Omega, I_D = 1A$
Turn-Off Fall Time	t _F	_	434	—	ns	
Reverse Recovery Time	t _{RR}	—	245	—	ns	I _F = 1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q _{RR}	—	149	—	nC	I _F = 1.0A, di/dt = 100A/µs

5. Device mounted on FR-4 PCB, with minimum recommended pad layout. Notes:

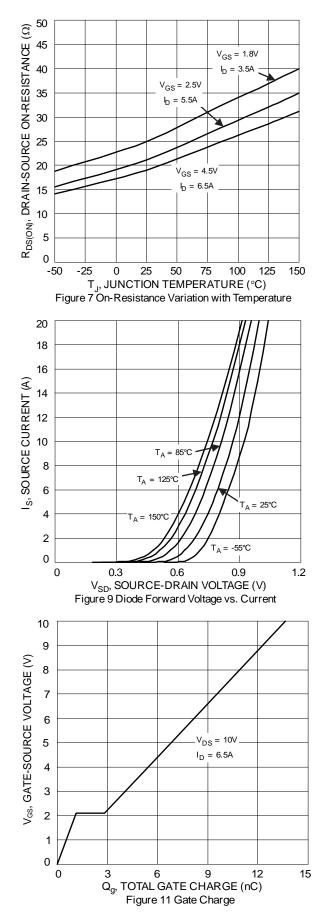
Device mounted on 1" x 1" FR-4 PCB with high-coverage 2oz. copper, single sided.
Short duration pulse test used to minimize self-heating effect.
Guaranteed by design. Not subject to product testing.

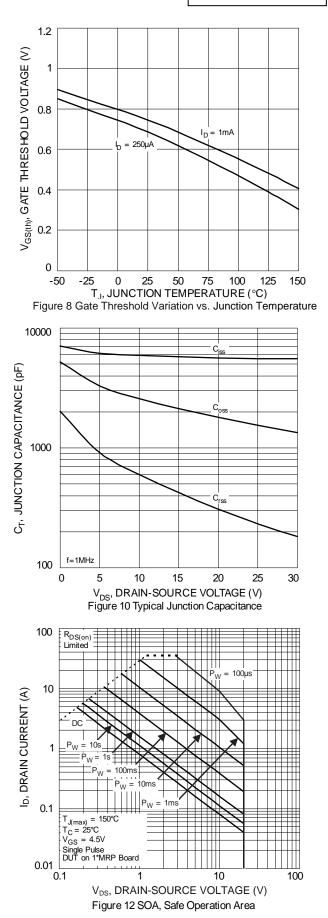




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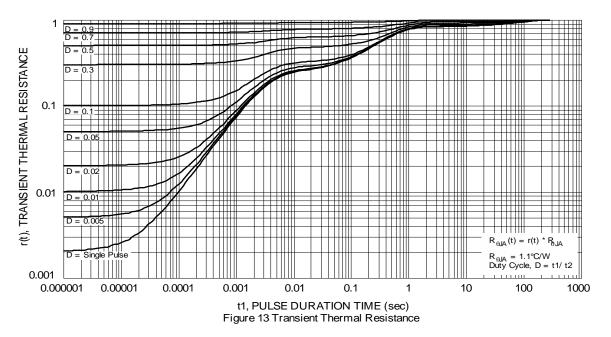






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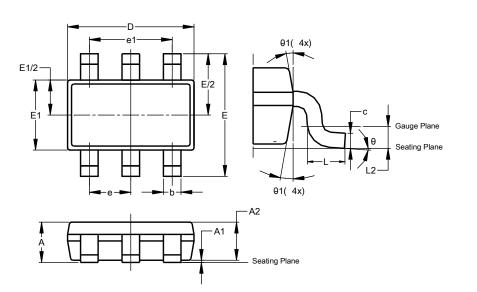






Package Outline Dimensions

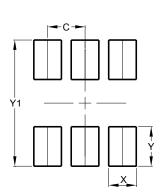
Please see http://www.diodes.com/package-outlines.html for the latest version.



1							
TSOT26							
Dim	Min	Max	Тур				
Α	-	1.00	-				
A1	0.010	0.100	-				
A2	0.840	0.900	-				
D	2.800	3.000	2.900				
Ε	2.800 BSC						
E1	1.500	1.700	1.600				
b	0.300	0.450	-				
С	0.120	0.200	-				
е	0.950 BSC						
e1	1	.900 BS	С				
L	0.30 0.50 -						
L2	0	.250 BS	С				
θ	0°	8°	4°				
θ1	4°	12°	-				
A	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



TSOT26

TSOT26

Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199





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