



**5A SURFACE MOUNT ULTRA-FAST RECOVERY RECTIFIER** 

### **Product Summary** (@ T<sub>A</sub> = +25°C)

V <sub>RRM</sub> (V)	I <sub>0</sub> (A)	V <sub>F</sub> (V)	Ι <sub>R</sub> (μΑ)
600	5	3.0	30

## **Description and Applications**

This device is intended for use as freewheeling and clamping diodes in a variety of switching power supplies and other power DCM and CCM PFC applications. It is especially suited for use in SMPS, home appliances, office equipment, and telecommunication applications.

## Features and Benefits

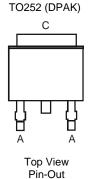
- Soft, Ultra-Fast Switching Capability for High-Efficiency
- Low Leakage Current
- High Current Capability
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen- and Antimony-Free. "Green" Device (Note 3)

## **Mechanical Data**

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound; UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish–Matte Tin Annealed over Copper Lead-Frame. Solderable per MIL-STD-202, Method 208 (3)
- Polarity: See Diagram



Top View



LEFT PIN	• N.o.	BOTTOMSIDE HEAT SINK
<b>RIGHT PIN</b>	o	HEAT SINK

Note: Pins Left & Right must be electrically connected at the printed circuit board.

### Ordering Information (Note 4)

Part Number	Case	Packaging
UF5JD1-13	TO252 (DPAK)	2,500 Pieces/Reel

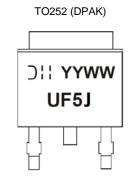
EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**

Notes:



UF5J = Product Type Marking Code )|| = Manufacturers' Code Marking YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 17 for 2017) WW = Week Code (01 - 53)



## Maximum Ratings (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	600	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	420	V
Average Rectified Output Current	lo	5	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	80	A

# **Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Case (Note 5)	R <sub>θJC</sub>	18	°C/W
Typical Thermal Resistance Junction to Ambient (Note 5)	R <sub>0JA</sub>	80	°C/W
Typical Thermal Resistance Junction to Case (Note 6)	R <sub>θJC</sub>	2	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	R <sub>0JA</sub>	18	°C/W
Operating and Storage Temperature Range	TJ, T <sub>STG</sub>	-65 to +150	°C

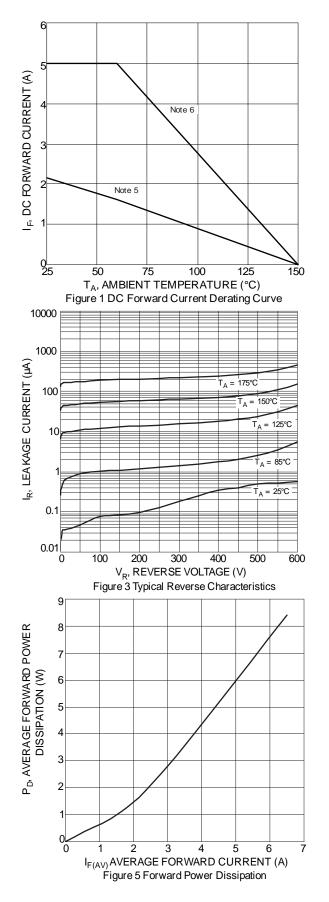
## Electrical Characteristics (@ T<sub>A</sub> = +25°C, unless otherwise specified.)

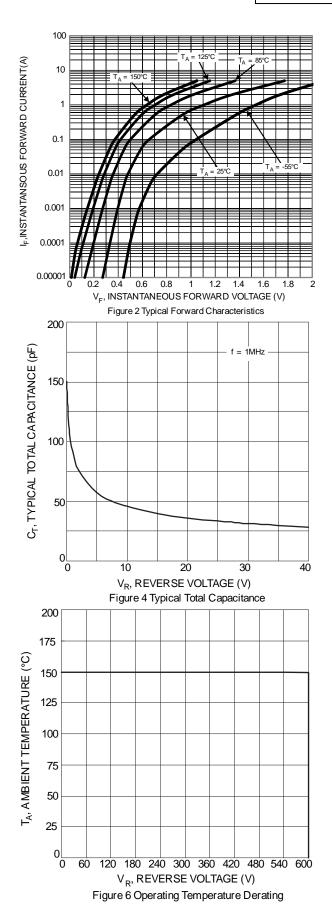
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	V <sub>(BR)R</sub>	600			V	$I_R = 30\mu A$
		_	1.1	1.6		$I_F = 1A, T_J = +25^{\circ}C$
		—	0.7	_	N	I <sub>F</sub> = 1A, T <sub>J</sub> = +125°C
Forward Voltage	¥-	—	1.5	2.3		$I_F = 3A, T_J = +25^{\circ}C$
Torward Voltage	V <sub>F</sub>	—	1.0		v	I <sub>F</sub> = 3A, T <sub>J</sub> = +125°C
		—	1.8	3.0		I <sub>F</sub> = 5A, T <sub>J</sub> = +25°C
		_	1.2			I <sub>F</sub> = 5A, T <sub>J</sub> = +125°C
Reverse Leakage Current (Note 7)	1-	_	0.57	30	μA	V <sub>R</sub> = 600V, T <sub>J</sub> = +25°C
Reverse Leakage Current (Note 7)	IR	—	0.04	5	mA	V <sub>R</sub> = 600V, T <sub>J</sub> = +125°C
Reverse Recovery Time		_	15	25	ns	I <sub>F</sub> = 0.5A, I <sub>R</sub> = 1.0A, I <sub>rr</sub> = 0.25A
Reverse Recovery Time	t <sub>RR</sub>	—	12	22		I <sub>F</sub> = 1A, V <sub>R</sub> = 30V, di/dt = 100A/µs
Reverse Recovery Charge	Q <sub>RR</sub>	_	5		nC	I <sub>F</sub> = 1A, V <sub>R</sub> = 30V, di/dt = 100A/µs
Total Capacitance	CT	_	45	50	pf	$V_{R} = 10V_{DC}$ , f = 1MHz

 Device mounted on FR4 PCB with 1x recommended pad layout.
Device mounted on 2-inch Al substrate PCB. Notes:

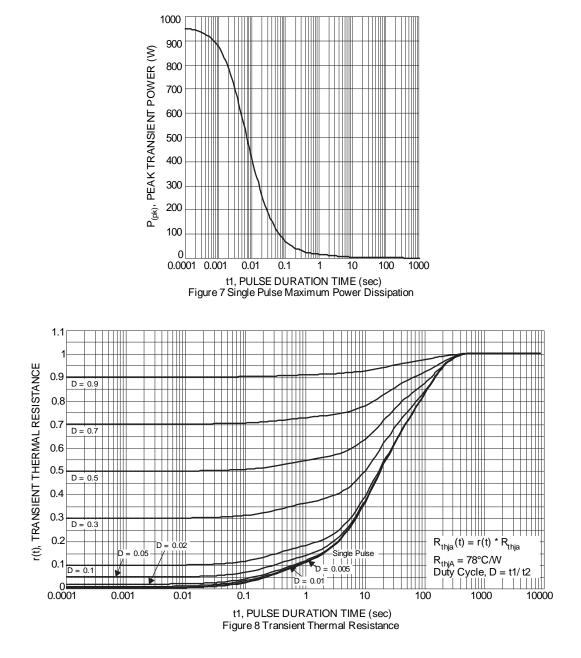
7. Short duration pulse test used to minimize self-heating effect.







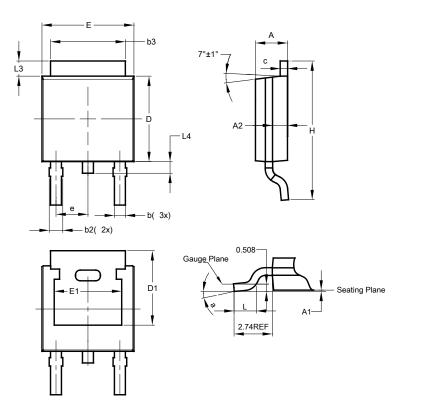






# **Package Outline Dimensions**

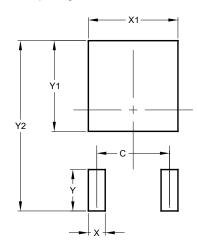
Please see http://www.diodes.com/package-outlines.html for the latest version.



	TO252 (DPAK)					
Dim	Min	Max	Тур			
Α	2.19	2.39	2.29			
A1	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
С	0.45	0.58	0.531			
D	6.00	6.20	6.10			
D1	5.21	-	-			
е	-	-	2.286			
Е	6.45	6.70	6.58			
E1	4.32	-	-			
Н	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°	-			
All	Dimen	sions i	n mm			

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	4.572
Х	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700



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