

SD103A - SD103C

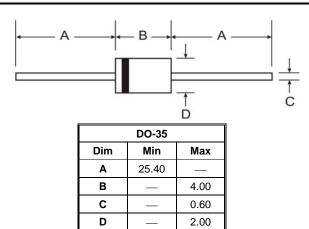
SCHOTTKY BARRIER DIODE

Features

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- Low Reverse Recovery Time
- Low Reverse Capacitance
- Lead Free Finish, RoHS Compliant (Note 2)

Mechanical Data

- Case: DO-35
- Case Material: Glass
- Moisture Sensitivity: Level 1 per J-STD-020C
- Leads: Solderable per MIL-STD-202, Method 208
- Terminals: Finish Sn96.5Ag3.5. Solderable per MIL-STD-202, Method 208 @3
- Polarity: Cathode Band
- Marking Information: See Page 3
- Ordering Information: See Page 3
- Weight: 0.13 grams (approximate)



All Dimensions in mm

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic	Symbol	SD103A	SD103B	SD103C	Unit
Peak Repetitive Reverse Voltage	V _{RRM}				
Working Peak Reverse Voltage	V _{RWM}	40	30	20	V
DC Blocking Voltage	V _R				
RMS Reverse Voltage	V _{R(RMS)}	28	21	14	V
Forward Continuous Current	I _{FM}		350		mA
Repetitive Peak Forward Current (Note 1) @ $t \le 1.0s$	I _{FRM}	1.0			А
Non-Repetitive Peak Forward Surge Current	I _{FSM}		15		А
8.3 ms Half Sine Wave	·F 3ivi				
Power Dissipation (Note 1)	P _d 400				mW
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{\theta JA}$	300			°C/W
Operating Junction Temperature	T _i		125		°C
Storage Temperature Range	T _{STG}	-55 to +150			°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 3)	SD103A SD103B SD103C	V _{(BR)R}	40 30 20	_	_	V	I _R = 100μA
Maximum Forward Voltage Drop		V_{FM}		_	0.37 0.60	V	$I_F = 20mA$ $I_F = 200mA$
Maximum Peak Reverse Current (Note 3)	SD103A SD103B SD103C	I _{RM}	_	_	5.0	μA	$V_{R} = 30V$ $V_{R} = 20V$ $V_{R} = 10V$
Total Capacitance		CT	_	50	_	pF	$V_{R} = 0V, f = 1.0MHz$
Reverse Recovery Time		t _{rr}		10		ns	$I_F = I_R = 50$ mA to 200mA, $I_{rr} = 0.1 \times I_R$, $R_L = 100\Omega$

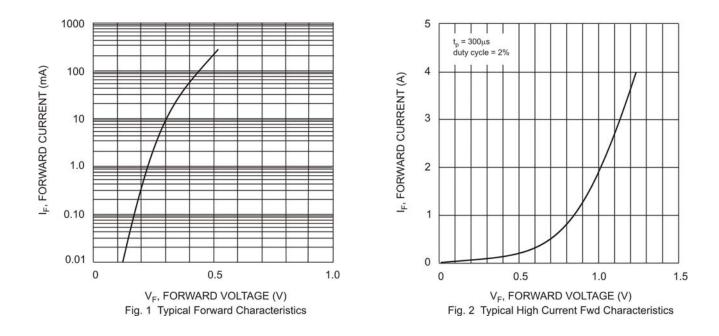
Notes: 1. Valid provided that device terminals are kept at ambient temperature.

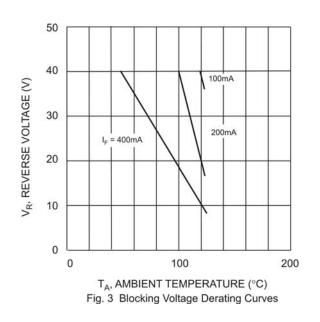
2. EC Directive 2002/95/EC (RoHS) revision 13.2.2003. Glass and high temperature solder exemptions applied where applicable,

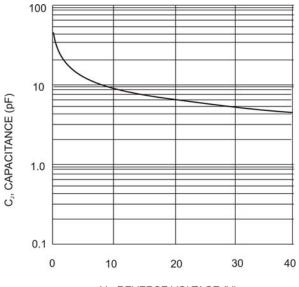
3. Short duration test pulse used to minimize self-heating effect.

see EU Directive Annex Notes 5 and 7.









 $V_{\mbox{\tiny R}},$ REVERSE VOLTAGE (V) Fig. 4 Typ. Junction Capacitance vs Reverse Voltage

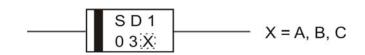


Ordering Information (Note 4)

Device	Packaging	Shipping
SD103A-A	DO-35	10,000 / Ammo Pak
SD103A-T	DO-35	10,000 / Tape & Reel
SD103B-A	DO-35	10,000 / Ammo Pak
SD103B-T	DO-35	10,000 / Tape & Reel
SD103C-A	DO-35	10,000 / Ammo Pak
SD103C-T	DO-35	10,000 / Tape & Reel

Notes: 4. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



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