

#### 60V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

## **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> T <sub>C</sub> = +25°C	
60V	$12m\Omega$ @ $V_{GS} = 10V$	70A	
60 V	$14m\Omega$ @ $V_{GS} = 4.5V$	55A	

#### **Features**

- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R<sub>DS(ON)</sub> Minimizes Power Losses
- Low Q<sub>G</sub> Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

## **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>), yet maintain superior switching performance, making it ideal for high efficiency power management applications.

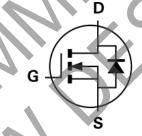
- High Frequency Switching
- Sync. Rectification
- DC-DC Converters

## **Mechanical Data**

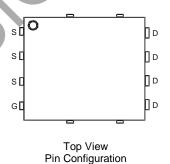
- Case: PowerDI<sup>®</sup>5060-8
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 63
- Weight: 0.097 grams (Approximate)







Internal Schematic



Top View Bottom View

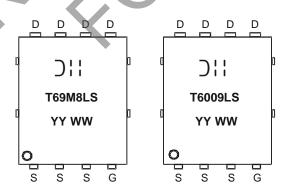
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT69M8LPS-13	PowerDI5060-8	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



☐ Hanufacturer's Marking
☐ T69M8LS & T6009LS = Product Type Marking Code
☐ YYWW = Date Code Marking
☐ YY = Year (ex: 16 = 2016)
☐ WW = Week (01 to 53)

PowerDI is a registered trademark of Diodes Incorporated.



#### NOT RECOMMENDED FOR NEW DESIGN **USE DMT6012LPS**

DMT69M8LPS

# **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	$V_{DSS}$	60	V
Gate-Source Voltage	V <sub>GSS</sub>	±16	V
Continuous Drain Current (Note 5)	ΙD	10.2 8.2	А
Continuous Drain Current (Note 6)	I <sub>D</sub>	70 55	Α
Maximum Continuous Body Diode Forward Current (Note 6)	I <sub>S</sub>	100	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	160	Α
Avalanche Current, L = 0.1mH	I <sub>AS</sub>	20.3	Α
Avalanche Energy, L = 0.1mH	E <sub>AS</sub>	20.6	mJ

## **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	$P_{D}$	2.3	W
Thermal Resistance, Junction to Ambient (Note 5)		R <sub>0JA</sub>	53	°C/W
Total Power Dissipation (Note 6)	$T_{C} = +25^{\circ}C$	$P_{D}$	113	W
Thermal Resistance, Junction to Case (Note 6)		Rejc	1.1	°C/W
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

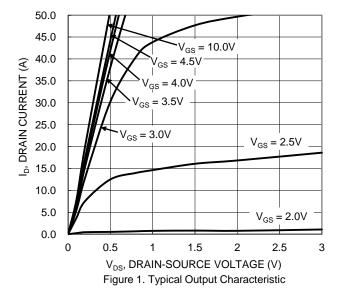
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	l		٧	$V_{GS} = 0V$ , $I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	$V_{DS} = 48V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	1	l	±100	nA	$V_{GS} = \pm 16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.7		2	<b>V</b>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance	D-	_	9.8	12	mΩ	$V_{GS} = 10V, I_D = 13.5A$	
Static Drain-Source Off-Resistance	R <sub>DS(ON)</sub>	_	12	14	11122	$V_{GS} = 4.5V, I_D = 11.5A$	
Diode Forward Voltage	$V_{SD}$		0.9	_	٧	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>		1,925	_		V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V, f = 1MHz	
Output Capacitance	Coss		438		pF		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	41	_			
Gate Resistance	$R_{G}$		1.7		Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 10V)	$Q_{G}$	_	33.5	_			
Total Gate Charge (V <sub>GS</sub> = 4.5V)	$Q_{G}$	_	15.6	_	nC	V 20V I 12.5A	
Gate-Source Charge	$Q_GS$	_	4.7	_	IIC	$V_{DS} = 30V, I_D = 13.5A$	
Gate-Drain Charge	$Q_{GD}$	_	5.3	_			
Turn-On Delay Time	t <sub>D(ON)</sub>		4.5				
Turn-On Rise Time	t <sub>R</sub>	_	8.6	_	ns	$V_{DD} = 30V$ , $V_{GS} = 10V$ , $R_G = 6\Omega$ , $I_D = 13.5A$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	35.9	_	115		
Turn-Off Fall Time	t <sub>F</sub>	_	15.7	_	1		
Body Diode Reverse Recovery Time	t <sub>RR</sub>	_	18.2	_	ns nC I <sub>F</sub> = 13.5A, di/dt = 400A/μs		
Body Diode Reverse Recovery Charge	Q <sub>RR</sub>	_	33.1	_			

 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:



## DMT69M8LPS





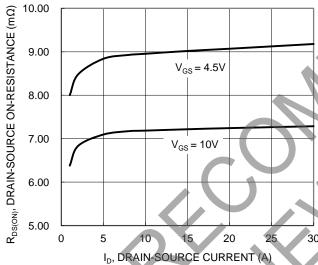


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

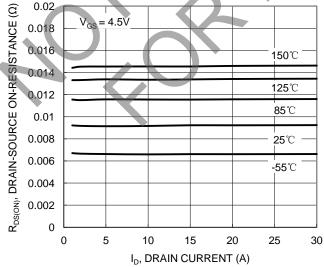


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

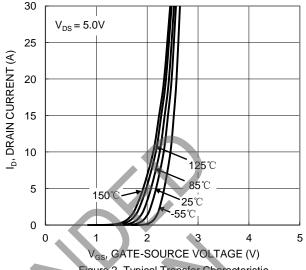


Figure 2. Typical Transfer Characteristic

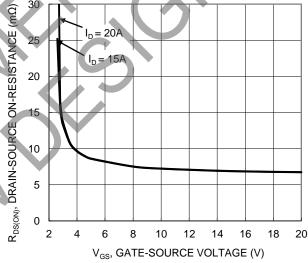


Figure 4. Typical Transfer Characteristic

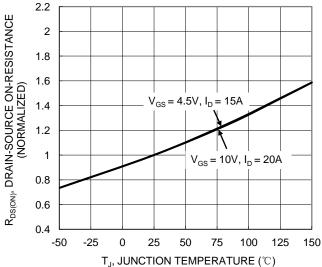


Figure 6. On-Resistance Variation with Temperature



#### NOT RECOMMENDED FOR NEW DESIGN **USE DMT6012LPS**

## DMT69M8LPS

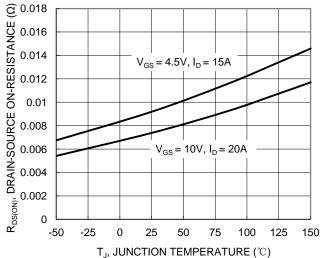


Figure 7. On-Resistance Variation with Temperature

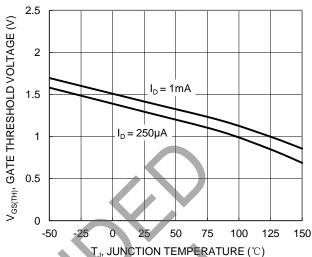
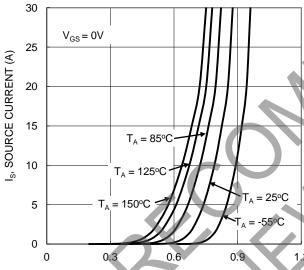
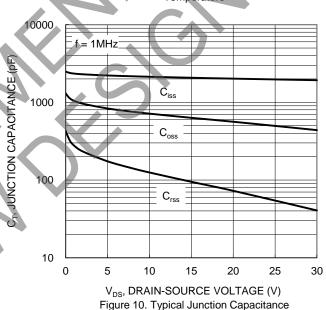
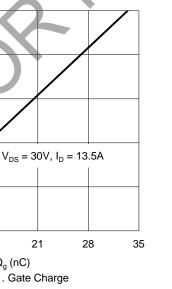


Figure 8. Gate Threshold Variation vs. Junction Temperature



V<sub>SD</sub>, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current





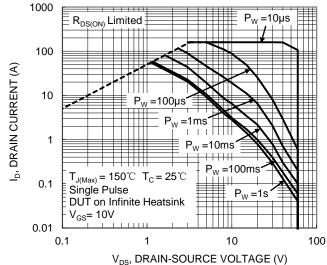


Figure 12. SOA, Safe Operation Area

10

4

2

0

0

7

14

 $Q_q$  (nC)

Figure 11. Gate Charge

21



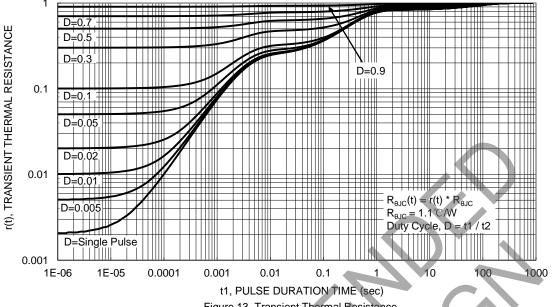


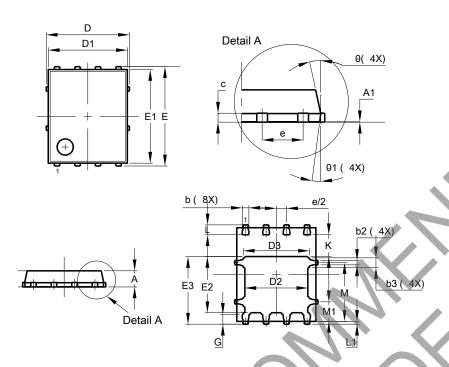
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8

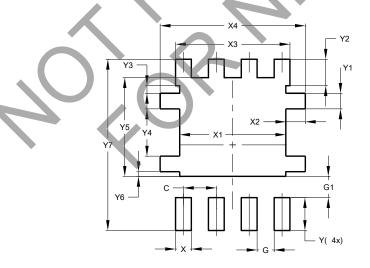


PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00	0.05	-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
С	0.230	0.330	0.277		
D	_	5.15 BSC	;		
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
E	6	6.15 BSC	,		
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е		1.27 BSC			
G	0.51	0.71	0.61		
K_	0.51	_	_		
L	0.51	0.71	0.61		
11	0.100	0.200	0.175		
M	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
θ	10°	12°	11°		
01	6°	8°	7°		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8



Dimensions	Value (in mm)				
С	1.270				
G	0.660				
G1	0.820				
Х	0.610				
X1	4.100				
X2	0.755				
Х3	4.420				
X4	5.610				
Υ	1.270				
Y1	0.600				
Y2	1.020				
Y3	0.295				
Y4	1.825				
Y5	3.810				
Y6	0.180				
Y7	6.610				



# NOT RECOMMENDED FOR NEW DESIGN USE DMT6012LPS

DMT69M8LPS

#### IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

#### LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
  - 1. are intended to implant into the body, or
  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2018, Diodes Incorporated

www.diodes.com