



DMN63D8LW

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>A</sub> = +25°C
30V	2.8Ω @ V <sub>GS</sub> = 10V	380mA
300	3.8Ω @ V <sub>GS</sub> = 5V	330mA

### Description

This MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- Motor Control
- Power Management Functions
- Backlighting

### N-CHANNEL ENHANCEMENT MODE MOSFET

### **Features and Benefits**

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 1kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

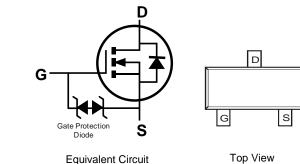
- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Alloy 42
  Leadframe. Solderable per MIL-STD-202, Method 208 <sup>(3)</sup>
- Weight: 0.006 grams (Approximate)





**SOT323** 

Top View



# Ordering Information (Note 4)

Part Number	Case	Packaging
DMN63D8LW-7	SOT323	3000/Tape & Reel
DMN63D8LW-13	SOT323	10000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**

МХХ	ΜY

 $\begin{array}{l} MXX = \underline{P} \text{roduct Type Marking Code} \\ YM = \underline{D} \text{ate Code Marking} \\ Y \text{ or } \overline{Y} = Y \text{ear (ex: B = 2014)} \\ M = \text{Month (ex: 9 = September)} \end{array}$ 

#### Date Code Key

	÷,											
Year	2014	2015	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025
Code	В	С	D	E	F	G	Н		J	K	L	М
		-				-			-			
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	30	V		
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	380 300	mA
Continuous Drain Current (Note 6) $V_{GS} = 10V$ t<5s $T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$			I <sub>D</sub>	430 340	mA
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%	6) (Note 6)	)	I <sub>DM</sub>	1.2	А

### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>0JA</sub>	426	°C/W
Total Power Dissipation (Note 6)		PD	420	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	301	°C/W
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	<b>.</b> ,		-76			
Drain-Source Breakdown Voltage	<b>BV</b> <sub>DSS</sub>	30		_	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	IDSS			1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I <sub>GSS</sub>			±10.0	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	0.8		1.5	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
				2.8		$V_{GS} = 10.0V, I_D = 250mA$
				3.8		$V_{GS} = 5.0V, I_D = 250mA$
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>			4.2	Ω	$V_{GS} = 4.5V, I_D = 250mA$
				4.5		$V_{GS} = 4.0V, I_D = 250mA$
				13		$V_{GS} = 2.5V, I_D = 10mA$
Forward Transconductance	<b>g</b> fs	80		_	mS	V <sub>DS</sub> = 10V, I <sub>D</sub> = 0.115A
Diode Forward Voltage	V <sub>SD</sub>		0.8	1.2	V	$V_{GS} = 0V, I_{S} = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		23.2	—		
Output Capacitance	Coss		3.0	—	pF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance	Crss	_	2.2			
Gate Resistance	R <sub>G</sub>		79.9	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge V <sub>GS</sub> = 10V	Qg		0.9			
Total Gate Charge V <sub>GS</sub> = 4.5V	Qg	_	0.4	_	nC	$V_{GS} = 10V, V_{DS} = 30V,$
Gate-Source Charge	Q <sub>gs</sub>		0.1		nc	I <sub>D</sub> = 150mA
Gate-Drain Charge	Q <sub>gd</sub>		0.2			
Turn-On Delay Time	t <sub>D(ON)</sub>		2.3			
Turn-On Rise Time	t <sub>R</sub>		3.9		20	V <sub>DD</sub> = 30V, I <sub>D</sub> = 0.115A, V <sub>GEN</sub> = 10V,
Turn-Off Delay Time	tD(OFF)	_	11.4		ns	$R_{GEN} = 25\Omega$
Turn-Off Fall Time	tF		16.7			

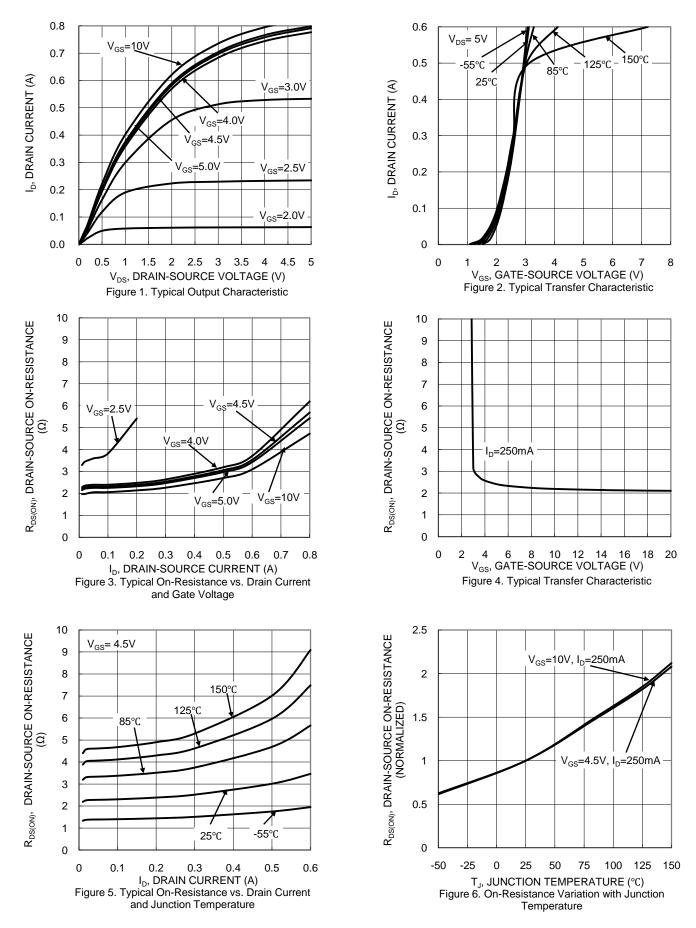
5. Device mounted on FR-4 PCB, with minimum recommended pad layout. Notes:

Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.



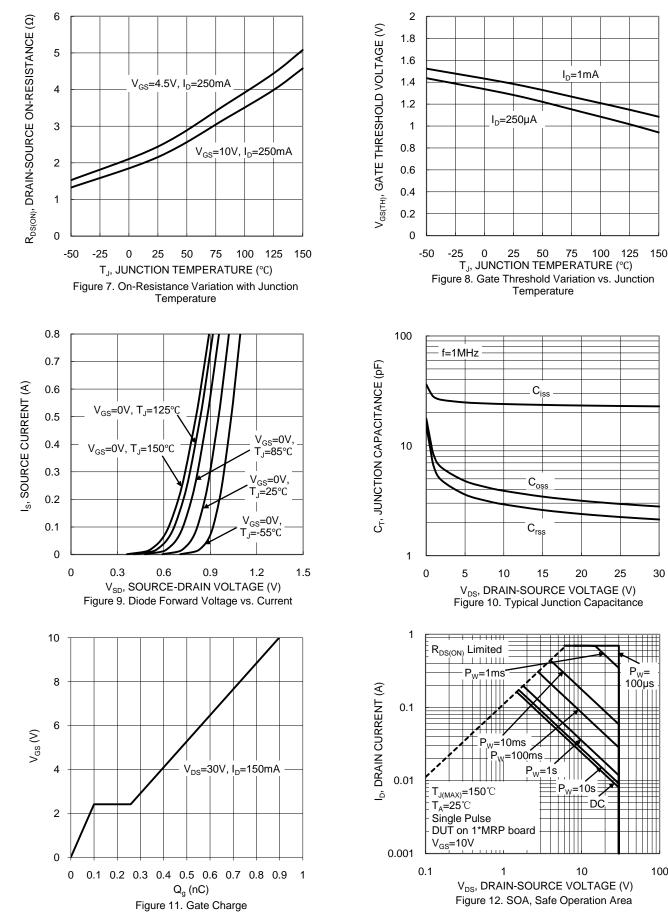
# DMN63D8LW



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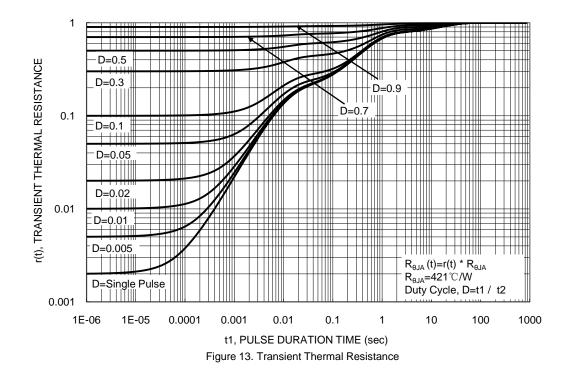
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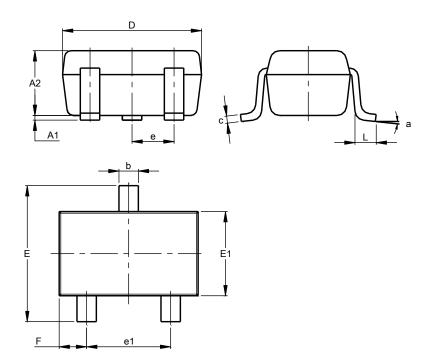
NEW PRODUCT





# Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

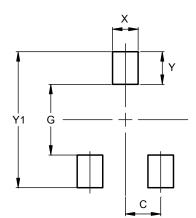


	SOT323							
Dim	Min	Max	Тур					
A1	0.00	0.10	0.05					
A2	0.90	1.00	0.95					
b	0.25	0.40	0.30					
С	0.10	0.18	0.11					
D	1.80	2.20	2.15					
Е	2.00	2.20	2.10					
E1	1.15	1.35	1.30					
е	C	).650 B	SC					
e1	1.20	1.40	1.30					
F	0.375	0.475	0.425					
L	0.25	0.40	0.30					
а		8°						
All	Dimen	sions	in mm					



# Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
X	0.470
Y	0.600
Y1	2.500

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