



1A DUAL COMMON CATHODE SCHOTTKY BARRIER DIODE DIE SIZE PACKAGE

Product Summary

V _{RRM} (V)	I _O (A)	V _F Max (V) @ +25°C	I _R Max (μΑ) @ +25°C
20	1	0.50	100

Description and Applications

The SDM1L20DCP3 is a 20V Dual Common Cathode Schottky Barrier Diodes that is optimized for very low forward voltage drop and low leakage current. It's housed in a compact die size package that occupies only 0.6mm² board space with low profile. The low thermal resistance enables designers to meet design challenges of increasing efficiency while reducing board space. It is ideally suited for use in portable applications such as:

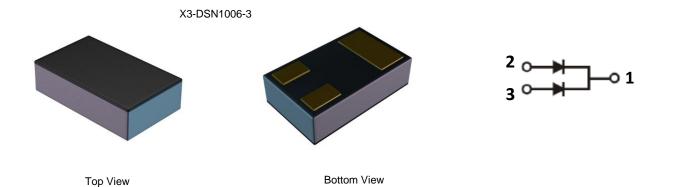
- Blocking Diode
- Reverse Protection Diode
- Boost Diode

Features and Benefits

- 0.6mm² Footprint, Off Board Profile of 0.275mm
- Low Forward Voltage Minimizes Power Dissipation Losses
- Low Leakage Maximizes Battery Power
- Soft, Fast Switching Capability
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: X3-DSN1006-3
- Moisture Sensitivity: Level 1 per J-STD-020
- Polarity Indicator: Cathode Dot
- Terminals: NiAu Bump. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.1mg (Approximate)



Ordering Information (Note 4)

-						
	Part Number	Case	Packaging			
	SDM1L20DCP3-7	X3-DSN1006-3	5,000/Tape & Reel			
Notes:	1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.					

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</p>

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



 $\begin{array}{l} XC = \mbox{Product Type Marking Code} \\ YM = \mbox{Date Code Marking} \\ Y \mbox{ or } \overline{Y} = \mbox{Year (ex: G = 2019)} \\ M = \mbox{Month (ex: 9 = September)} \\ Bar \mbox{Denotes Cathode Pin} \end{array}$

Date Code Key

Year	201	6	2017 2018		2018	20	2019 2020			2021	2	2022	
Code	D		E		F		G					J	
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec	
Code	1	2	3	4	5	6	7	8	9	0	Ν	D	



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

	Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Vo	ltage	V _{RRM}	20	V
Average Rectified Output C	urrent (Total)	lo	1	А
Repetitive Peak Forward Cu	irrent, $t_p \le 1ms$; $\delta \le 0.25$ (Single Diode)	I _{FRM}	3	A
Non-Repetitive Peak Forwa Single Half Sine-Wave Sup	rd Surge Current, 8.3ms erimposed on Rated Load (Single Diode)	I _{FSM}	10	А
ESD Rating	Human Body Model Charged Device Model	ESD	8 1	kV

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	R _{0JA}	245	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	R _{0JA}	105	°C/W
Operating Temperature Range	TJ	-55 to +150	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop (Single Diode)	VF		210 290 330 420	260 350 400 500	mV	$I_{F} = 10mA, T_{J} = +25^{\circ}C$ $I_{F} = 100mA, T_{J} = +25^{\circ}C$ $I_{F} = 200mA, T_{J} = +25^{\circ}C$ $I_{F} = 500mA, T_{J} = +25^{\circ}C$
Leakage Current (Note 7) (Single Diode)	I _R	—	15 32	50 100	μΑ	$V_R = 10V, T_J = +25^{\circ}C$ $V_R = 20V, T_J = +25^{\circ}C$
Junction Capacitance (Single Diode)	CJ	_	18	—	pF	V _R = 5V, T _J = +25°C, f = 1MHz
Reverse Recovery Time (Single Diode)	t _{RR}	_	8.6	—	ns	$I_F = 10mA$, $I_{RR} = 0.1 \times I_R$

Notes:

5. Device mounted on FR-4 substrate PC board, 2oz Cu with minimum recommended pad layout per http://www.diodes.com/package-outlines.html.

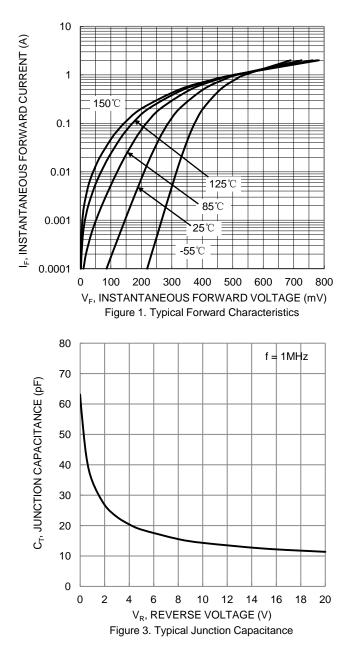
6. Device mounted on FR-4 substrate PC board, 1 inch square 2oz Cu pad.

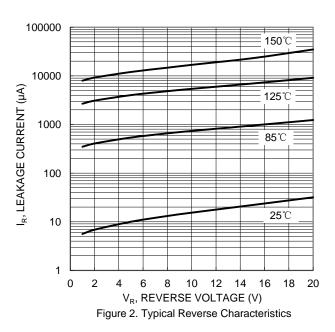
7. Short duration pulse test used to minimize self-heating effect.



NEW PRODUCT

SDM1L20DCP3

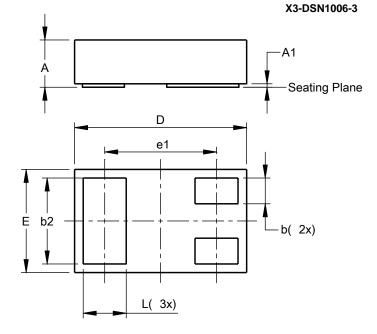






Package Outline Dimensions

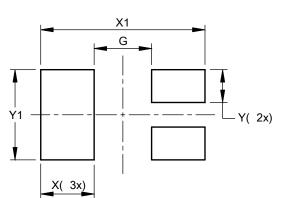
Please see http://www.diodes.com/package-outlines.html for the latest version.



	X3-DSN1006-3							
Dim	Min	Max	Тур					
Α	0.250	0.300	0.275					
A1	0.00	0.02	0.01					
b	0.130	0.170	0.150					
b2	0.480	0.520	0.500					
D	0.960	1.040	1.00					
Е	0.560	0.640	0.600					
e			0.350					
e1			0.650					
L	0.230	0.270	0.250					
All	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



X3-DSN1006-3

Dimensions	Value (in mm)		
G	0.350		
Х	0.325		
X1	1.00		
Y	0.200		
Y1	0.550		



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